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锁相环调频立体声发射器的设计与实现

ROHM BH1417

孙建林

BH1417 是一款集立体声 FM、频率 合成和 RF 放大器等功能于一体的专用 的调频发射集成电路,与简单的外围电路 配合使用,可发射音频 FM 立体声信号,配合普通的调频立体声接收机就可实现无线调频立体声传送。而且使用了控制频率稳定的锁相环电路,使频率不再漂移。此 FM 立体声发射器发射出的立体声信号分离度可以达到 50dB,失真小于0.3%,而且电路的稳定性大大加强。单就收发效果而言,已基本接近正规的 FM 电台。适用于学校、工厂、部队、旅游景区等,可以接电脑、DVD、MP3、MP4 等任何具有音频输出的设备。如果架设室外天线,有效覆盖范围可达 500 米。

BH1417 的原理特性

FM 发射电路采用稳定频率的锁相环系统。这一部分由高频振荡器、高频放大器及锁相环频率合成器组成。调频由变容二极管组成的高频振荡器实现,高频振荡器是锁相环的 VCO,立体声复合信号通过它直接进行调频。高频振荡器由第9引脚外部的 LC 回路与内部电路组成,振荡信号经过高频放大器从 11 引脚输出,同时输送到锁相环电路进行比较后,从第7引脚输出一个信号,对高频振荡器的值进行修正,确保频率稳定。一旦超过锁相环设定的频率,第7引脚将输出的电平拉高;如果低于设定频率,它将输出的电平拉低;相同的时候,它的电平将不变。

1) 将预加重电路、限幅电路、低通滤波电路(LPF)一体化,使音频信号的质量比分立元件的电路(如 BA1404、

NJM2035 等)有很大改进。

2) 采用锁相环锁频,并与调频发射电路一体化,使得发射的频率非常稳定。

3) 采用了 4 位拔码开关进行频率设定,可设定 14 个频点,使用非常方便。

BH1417的内部结构和引脚功能如图 1 所示。它由 5 部分组成:音频预处理电路(加重、限幅和低通滤波);基频产生电路(晶振、分频);锁相环电路(相位检测、锁频);频率设定电路(高低电平转换);调频发射电路。外围电路主要有拨码开关组成的频率控制电路、压控振荡器组成的载波产生电路、定时器以及一些耦合电容。

立体声调频发射整机电路设计

立体声调制电路: 音频信号通过 1 脚、22 脚输入后,由 BH1417 的 21、20、19、2、3、4 脚和外电路配合通过预加重电路,限幅电路和低能滤波器后送到混合器中,由 13 脚、14 脚输入的接入 7.6MHz晶体的振荡电路,通过 200 分频产生的38kHz副载波信号,同时 3kHz 副载波通过 2 分频后产生 19kHz 导频信号,(L-R)信号与 38kHz 的副载波进行平衡调制,调制后的复合信号通过 5 脚输出。

FM 发射电路:调频发射电路采用频率稳定的锁相环系统。15、16、17、18 脚输入的频率代码经过解码和鉴相后,由 7 脚输出 PLL 振荡器的控制信号 VCO。此 VCO 控制外部的分立元件组成的高频振荡电路产生 FM 调频的载波信号,并通过一个达林顿三极管 2SD2142 对 5 脚输出的复合立体声信号进行 FM 频率调制。调制后的信号通过 9 脚输入到 BH1417K,经过内部的射频放大器放大后的射频信号由 11 脚输出。输出后的信号可以直接接到发射天线上进行发射。

高频振荡电路:BH1417的5,7,9、10、12管脚配合于其连接的分立元件,构成调频载波的频率振荡和射频调制部分;

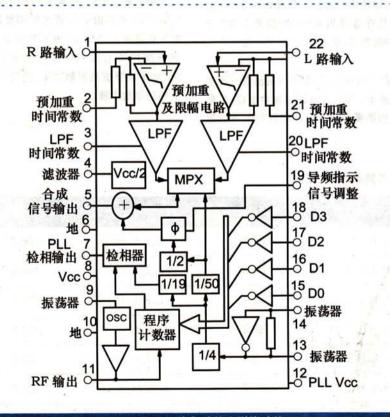


图 1 BH1417 内部功能和引脚功能

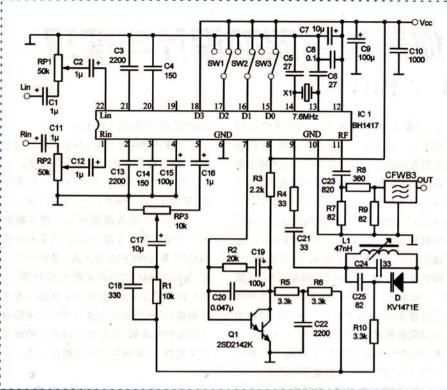


图 2 整机电路图

13、14 脚需要外接 7.6MHz 的晶体振荡器,提供给 BH1417 内部的鉴相、立体声信号调制等部分所需要的稳定时钟。6、8 为电源部分;11 脚与外部连接的元件构成调频信号发射部分。

由于 BH1417 内部工作所需的时钟都是来自 7.6MHz 的晶振,而晶振的工作频率一般都十分稳定。外部调频载波信号和载波调制电路都使用 VCO (压控振荡)控制的 PLL(锁相环)电路进行工作。所以,由 BH1417 组成的调频发射器发射频率十分稳定,不会在发射过程中出现跑频或者自激振荡。

无线调频发射器的制作

器件准备和 PCB 板制作:用 Protel99SE 绘制 PCB 板或用万用试验电路板按上述的电路图进行搭制。焊接过程中,首先要将 BH1417 这个贴片 SOP22封装的器件使用转接板进行转接,然后再通过引线焊接到试验板中心的位置上,

对其他分立元件,如果有条件的话,在上板之前用万用表等仪器测试一下,判

断其好坏。L1 电感需要自行绕制,制作时,可以使用一段直径 0.6mm~1mm 的 漆包线在直径为 4mm 的圆棒上绕制 5 圈,绕制后,抽出圆棒,在线圈中装入中周磁芯。

焊制成功后,一定要仔细检查一下电 路再通电。笔者在实践过程中发现,供电 电源的质量对整机的稳定性和信号的保 真度有较大的影响。建议使用 LM317 或7805 等三端稳压电路进行供电。要注意的是,BH1417 的供电电压不要超过 6V,推荐使用 5V,否则会对芯片造成损害。

整机调试:元器件焊接无误后,一般 无需做调试,只要 BH1417 没有发热,就 可以给 Lin 和 Rin 两端输入立体声信号, 然后设置 15、16、17、18 脚来确定一个发 射频率进行发射,如表 1 所示。

调试 时将 频率设定开关设定在87.7MHz 的位置上(开关全为低电位),将 L1 的磁芯全部旋出来,并在 C23 的输出端接根短导线。接上 9V 电源,用频率计(或接收机)确定频点是否正确,并用接收机监听接收效果,声音清晰、宏亮,没有杂音。如果频率不对,检查 V_{CC} 上的元件是否焊接错误。统调时,拆下 C23 处的短导线,接上天线和场强仪(或将接收机放到一定远处,以接收信号强弱来测信号强度),反复调整 L1 的铜芯,使场强最大,从而达到最佳接收效果。

发射频率在 87MHz ~ 89MHz 的频段是可以直接设置使用的,不必对振荡电路参数进行调整。如果要使用 106MHz ~ 107MHz 频段的话,则需要对振荡电路中C24、C25 电容值进行适当的调整即可。此发射器接上约 50cm 长的电线做天线,在开阔地段有效发射距离大概是 500米,如果需要提高发射功率,就要加一级高频功率放大器。

表 1. 各频点的发射频率

按码开关位置	频点(MHz)	按码开关位置	频点(MHz)	
L,L,L,L	87.8	L,L,L,H,	106.7	
H,L,L,L	87.9	H、L、L、H	106.9	
L\H\L\L	88.1	L.H.L.H	107.1	
H\H\L\L	88.3	H,H,L,H,	107.3	
L,L,H,L	,H,L 88.5 L,L,H,H		107.5	
H,L,H,L 88.7		H,L,H,H,	107.7	
L\H\H\L	88.9	L,H,H,H	107.9	

Wireless Audio Link IC

BH1417F

The BH1417F is a FM stereo transmitter IC that transmits simple configuration. The IC consists of a stereo modulator for generating stereo composite signals and a FM transmitter for broadcasting a FM signal on the air. The stereo modulator generates a composite signal which consists of the MAIN, SUB, and pilot signal from a 38kHz oscillator.

The FM transmitter radiates FM wave on the air by modulating the carrier signal with a composite signal. Frequency is set for North America.

Applications

Wireless speakers, Personal computer(sound board), Game machine, CD changer, Car TV, Car navigation

Features

- 1) It is possible to improve the timbre because it has the pre-emphasis circuit, limiter circuit, and the low-pass filter circuit.
- 2) Built-in pilot-tone system FM stereo modulator circuit.
- 3) The transmission frequency is stable because it has a PLL system FM transmitter circuit.
- 4) PLL controls data input in parallel (4bits, 14ch for North America).

● **Absolute maximum ratings** (Ta = 25°C, In measurement circuit.)

Parameter	Symbol	Limits	Unit	Conditions
Supply voltage	Vcc	+7.0	V	Pin8,12
Date input voltage	VIN-D	-0.3 to Vcc+0.3	V	Pin15,16,17,18
Phase comparator output voltage	Vout-p	-0.3 to Vcc+0.3	V	Pin7
Power dissipation	Pd	450 [*]	mW	
Storage temperature	Tstg	-55 to +125	°C	

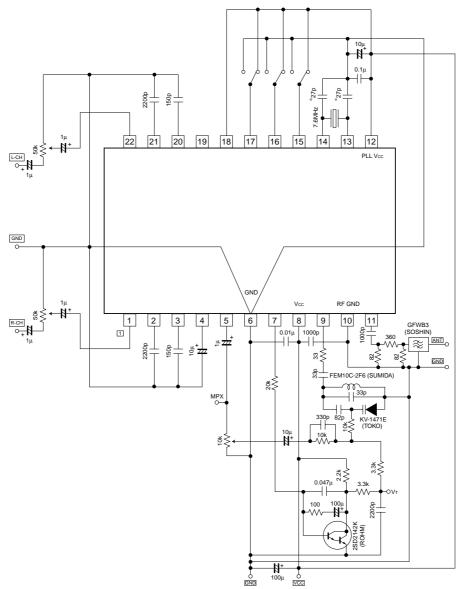
^{*} Derating : 4.5mW/°C for operation above Ta=25°C.

● Recommended operating conditions (Ta = 25°C)

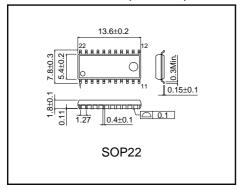
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Operating supply voltage	Vcc	4.0	_	6.0	V	Pin8,12
Operating temperature	Topr	-40	_	+85	°C	
Audio input level	VIN-A	_	_	-10	dBV	Pin1,22
Audio input frequency band	fin-a	20	_	15k	Hz	Pin1,22
Pre-emphasis time constant set up range	τ PRE	_	_	155	μs	Pin2,21
Transmission frequency(200kHz step)	fтх	87.7 106.7	_	88.9 107.9	MHz	Pin9,11
Control terminal "H" level input voltage	ViH	0.8Vcc	_	Vcc	V	Pin15,16,17,18
Control terminal "L" level input voltage	VIL	GND	_	0.2Vcc	V	Pin15,16,17,18

Application circuit

US BAND (88.0MHz~89.2MHz)



●External dimensions (Units : mm)



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STRUCTURE Silicon Monolithic Integrated Circuit

PRODUCT SERIES FM stereo transmitter IC for audio systems.

TYPE BH1417FV

FEATURES • It is possible to

• It is possible to attempt to improve a timbre because it has the pre-emphasis circuit, limiter circuit and low-pass filter circuit.

- Built-in the pilot-tone system FM stereo modulator circuit.
- The transmission frequency is stable because it has PLL system FM transmitter circuit.
- PLL controls data input in parallel (4bits, 14ch). It deal with U.S. band.

Parameter	Symbol	Limits	Unit	Conditions
Supply voltage	Vcc	+7.0	V	Pin 8, 13
Data input voltage	V _{IN-D}	-0.3 ~ Vcc+0.3	٧	Pin 17, 18, 19, 20
Phase comparator output voltage	V _{OUT-P}	-0.3 ~ Vcc+0.3	٧	Pin 7
Power dissipation	Pd	630	mW	(*1)
Storage temperature	Tstg	-55 ∼ +12 5	°C	

^(*1) To use at a temperature higher than Ta=25°C, derate 6.3mW per 1°C.

Status of this document

The Japanese version of this document is the formal specification. A customer may use this translation version only for a reference to help reading the formal version. If there are any differences in translation version of this document, formal version takes priority.

Application example

The application circuit is recommended for use. Make sure to confirm the adequacy of the characteristics.

When using the circuit with changes to the external circuit constants, make sure to leave an adequate margin for external components including static and transitional characteristics as well as dispersion of the IC.

Note that ROHM cannot provide adequate confirmation of patents.

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Operating Range

Parameter	Symbol	Limits	Unit	Conditions
Operating supply voltage	Vcc	4.0 ~ 6.0	V	Pin 8, 13
Operating temperature	Topr	-40 ∼ +85	င	
Audio input level	V _{IN-A}	~ -10	dBV	Pin 1, 24
Audio input frequency band	f _{IN-A}	20 ~ 15k	Hz	Pin 1, 24
Pre-emphasis time constant set up range	T PRE	~ 155	μsec	Pin 2, 23
Transmission frequency	f _{TX}	87.7~88.9(step0.2) 106.7~107.9(step0.2)	MHz	Pin 10, 12
Control terminal "H" level input voltage	V _{IH}	0.8Vcc ~ Vcc	V	Pin 17, 18, 19, 20
Control terminal "L" level input voltage	VIL	GND ∼ 0.2Vcc	V	Pin 17, 18, 19, 20

O Electrical Characteristics

Unless otherwise specified $\,$ Ta=25°C , $\,$ Vcc=5.0V $\,$

Signal source: f_{IN}=400Hz

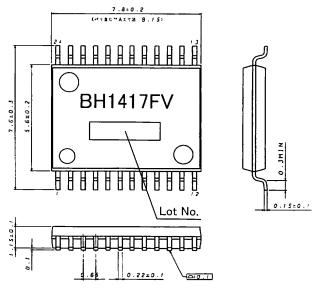
Signal source : III		Limits			0 1111	
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Quiescent current	la	14	20	28	mA	
Channel separation	Sep	25	40		dB	V _{IN} =-20dBV, L→R, R→L
Total harmonic distortion	THD	_	0.1	0.3	%	V _{IN} =-20dBV, L+R
Channel balance	C.B	-2	0	+2	dB	V _{IN} =-20dBV, L+R
Input output gain	Gv	-2	0	+2	dB	V _{IN} =-20dBV, L+R
Pilot modulation rate	M _P	12	15	18	%	V _{IN} =-20dBV, L+R, Pin5
Sub carrier rejection ratio	SCR	_	-30	-20	dB	V _{IN} =-20dBV, L+R
Pre-emphasis time constant	T PRE	40	50	60	μsec	V _{IN} =-20dBV, L+R
Limiter input level	V _{IN(LIM)}	-16	-13	-10	dBV	Output level at 1dB gain compression
LPF cut off frequency	f _{C(LPF)}	12	15	18	kHz	Vo=-3dB, Pin2, 23 Open
Transmission output level	V_{TX}	96	99	102	dΒμV	f _{TX} =107.9MHz
"H" level input current	l _{iH}	_		1.0	μΑ	Pin 17, 18, 19, 20 V _{IN} =5V
"L" level input current	ᆜ	-1.0	-	_	μΑ	Pin 17, 18, 19, 20 V _{IN} =0V
"H" level output voltage	V _{OH}	Vcc-1.0	Vcc-0.15		٧	Pin 7 l _{OUT} =-1.0mA
"L" level output voltage	V _{OL}	_	0.15	1.0	V	Pin 7 l _{OUT} =1.0mA
"off" level leak current 1	l _{OFF1}	_	_	100	nA	Pin 7 V _{OUT} =5V
"off" level leak current 2	I _{OFF2}	-100	_	_	nA	Pin 7 V _{OUT} =GND

This product is not designed for protection against radioactive rays.

 $[\]bigcirc$ The specification of transmission output level be based on the Radio Law in every country and the area.

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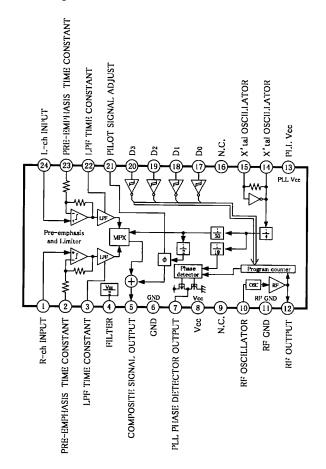
O External Dimension Diagram · Marking Diagram



SSOP-B24 (Unit: mm)

O Block Diagram





No.	Name	No.	Name
1	R-ch INPUT	13	PLL Vcc
2	PRE-EMPHASIS TIME CONSTANT	14	X'tal OSCILLATOR
3	LPF TIME CONSTANT	15	X'tal OSCILLATOR
4	FILTER	16	N.C.
5	COMPOSITE SIGNAL OUTPUT	17	D _o
6	GND	18	D ₁
7	PLL PHASE DETECTOR OUTPUT	19	D ₂
8	Vcc	20	D_3
9	N.C.	21	PILOT SIGNAL ADJUST
10	RF OSCILLATOR	22	LPF TIME CONSTANT
11	RF GND	23	PRE-EMPHASIS TIME CONSTANT
12	RF OUTPUT	24	L-ch INPUT



O Cautions On Use

(1) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.

(2) GND potential

Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.

(3) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.

(4) Shorts between pins and misinstallation

When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is misinstalled and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.

(5) Operation in strong magnetic fields

Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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