2SB0929 (2SB929), 2SB0929A (2SB929A)

Silicon PNP epitaxial planar type

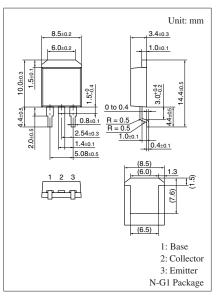
For power amplification Complementary to 2SD1252, 2SD1252A

■ Features

- \bullet High forward current transfer ratio $h_{F\!E}$ which has satisfactory linearity
- Low collector-emitter saturation voltage V_{CE(sat)}
- N type package enabling direct soldering of the radiating fin to the printed circuit board, etc. of small electronic equipment.

■ Absolute Maximum Ratings $T_C = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage	2SB0929	V _{CBO}	-60	V
(Emitter open)	2SB0929A		-80	
Collector-emitter voltage	2SB0929	V _{CEO}	-60	V
(Base open)	2SB0929A		-80	
Emitter-base voltage (Col	V_{EBO}	-5	V	
Collector current	I_C	-3	A	
Peak collector current	I_{CP}	-5	A	
Collector power dissipati	P _C	35	W	
	$T_a = 25$ °C		1.3	
Junction temperature	T_{j}	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	



Note) Self-supported type package is also prepared.

■ Electrical Characteristics $T_C = 25$ ° $C \pm 3$ °C

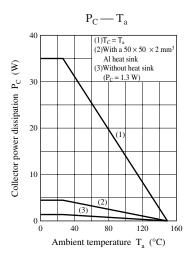
Parameter		Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage	2SB0929	V _{CEO}	$I_C = -30 \text{ mA}, I_B = 0$	-60			V
(Base open)	2SB0929A			-80			
Collector-emitter cutoff	2SB0929	I _{CES}	$V_{CE} = -60 \text{ V}, V_{BE} = 0$			-200	μΑ
current (E-B short)	2SB0929A		$V_{CE} = -80 \text{ V}, V_{BE} = 0$			-200	
Collector-emitter cutoff	2SB0929	I_{CEO}	$V_{CE} = -30 \text{ V}, I_{B} = 0$			-300	μΑ
current (Base open)	2SB0929A		$V_{CE} = -60 \text{ V}, I_{B} = 0$			-300	
Emitter-base cutoff current (Col	lector open)	I_{EBO}	$V_{EB} = -5 \text{ V}, I_C = 0$			-1	mA
Forward current transfer rat	io	h _{FE1} *	$V_{CE} = -4 \text{ V}, I_{C} = -1 \text{ A}$	70		250	_
		h _{FE2}	$V_{CE} = -4 \text{ V}, I_{C} = -3 \text{ A}$	10			
Base-emitter voltage		V_{BE}	$V_{CE} = -4 \text{ V}, I_{C} = -3 \text{ A}$			-1.8	V
Collector-emitter saturation	voltage	V _{CE(sat)}	$I_C = -3 \text{ A}, I_B = -0.375 \text{ A}$			-1.2	V
Transition frequency		f_T	$V_{CE} = -10 \text{ V}, I_{C} = -0.5 \text{ A}, f = 10 \text{ MHz}$		30		MHz
Turn-on time		t _{on}	$I_C = -1 A$,		0.5		μs
Strage time		t _{stg}	$I_{B1} = -0.1 \text{ A}, I_{B2} = 0.1 \text{ A}$		1.2		μs
Fall time		$t_{\rm f}$	$V_{CC} = -50 \text{ V}$		0.3		μs

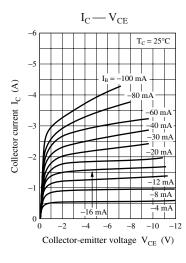
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

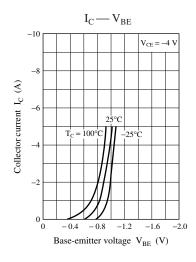
2. *: Rank classification

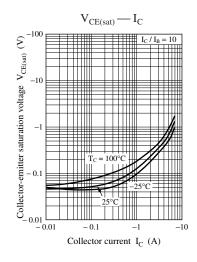
Rank	Q	Р		
h _{FE1}	70 to 150	120 to 250		

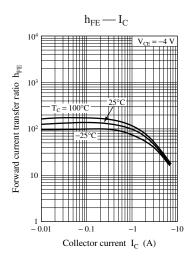
Note) The part number in the parenthesis shows conventional part number.

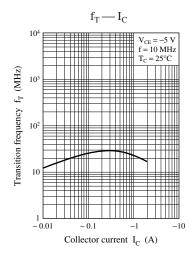


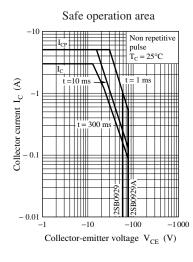


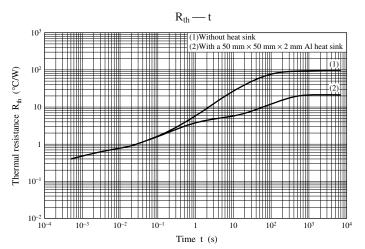












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