

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE (L²-π-MOSV)

2SK2399

HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS

CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

INDUSTRIAL APPLICATIONS

Unit in mm

- 4V Gate Drive
- Low Drain-Source ON Resistance : $R_{DS(ON)} = 0.17\Omega$ (Typ.)
- High Forward Transfer Admittance : $|Y_{fs}| = 4.5S$ (Typ.)
- Low Leakage Current : $I_{DSS} = 100\mu A$ (Max.) ($V_{DS} = 100V$)
- Enhancement-Mode : $V_{th} = 0.8 \sim 2.0V$ ($V_{DS} = 10V, I_D = 1mA$)

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	V_{DSS}	100	V
Drain-Gate Voltage ($R_{GS} = 20k\Omega$)	V_{DGR}	100	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current	DC	I_D	5 A
	Pulse	I_{DP}	20 A
Drain Power Dissipation (Tc = 25°C)	P_D	20	W
Single Pulse Avalanche Energy**	E_{AS}	180	mJ
Avalanche Current	I_{AR}	5	A
Repetitive Avalanche Energy*	E_{AR}	2	mJ
Channel Temperature	T_{ch}	150	°C
Storage Temperature Range	T_{stg}	-55~150	°C

THERMAL CHARACTERISTICS

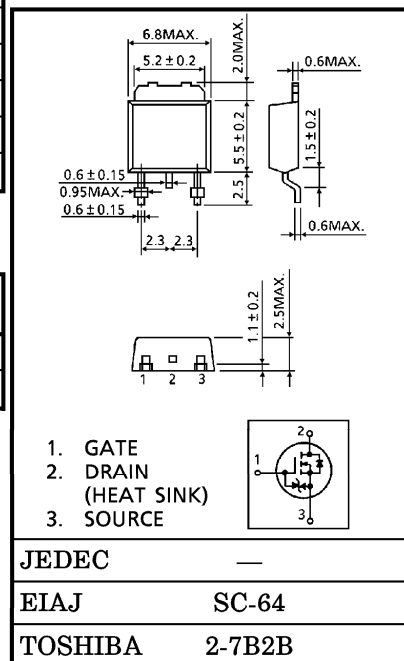
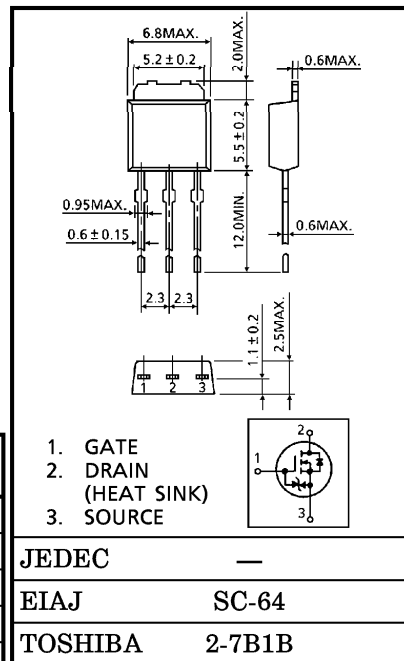
CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{th(ch-c)}$	6.25	°C/W
Thermal Resistance, Channel to Ambient	$R_{th(ch-a)}$	125	°C/W

Note ;

* Repetitive rating ; Pulse Width Limited by Max. junction temperature.

** $V_{DD} = 25V$, Starting $T_{ch} = 25°C$, $L = 11.6mH$, $R_G = 25\Omega$, $I_{AR} = 5A$

This transistor is an electrostatic sensitive device. Please handle with caution.



Weight : 0.36g

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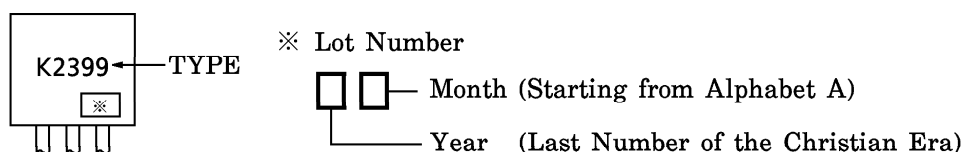
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

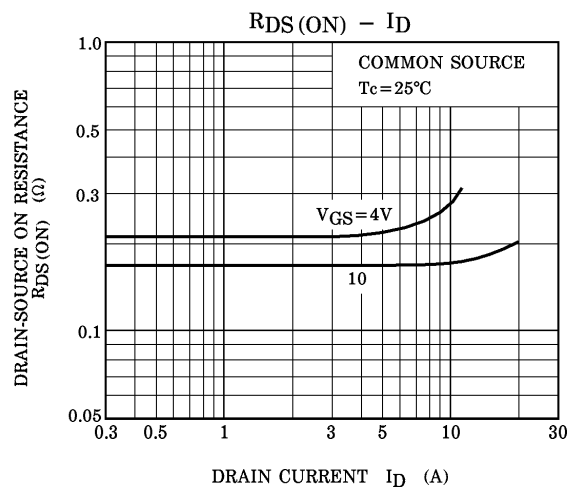
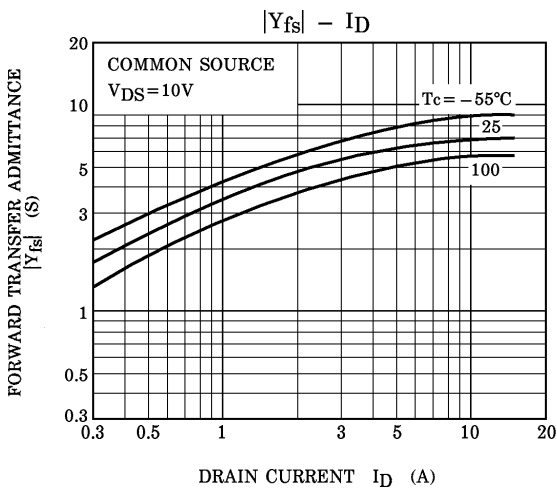
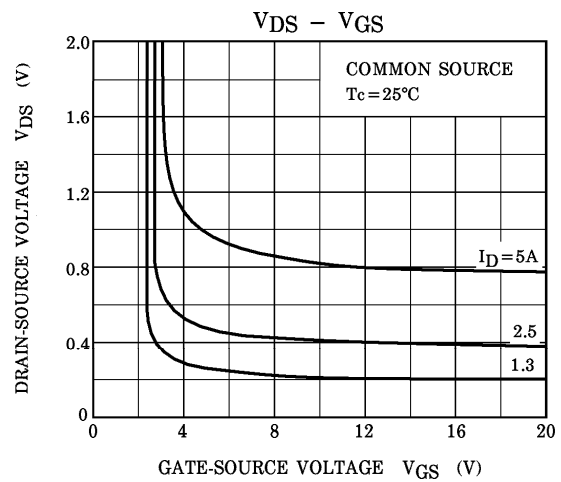
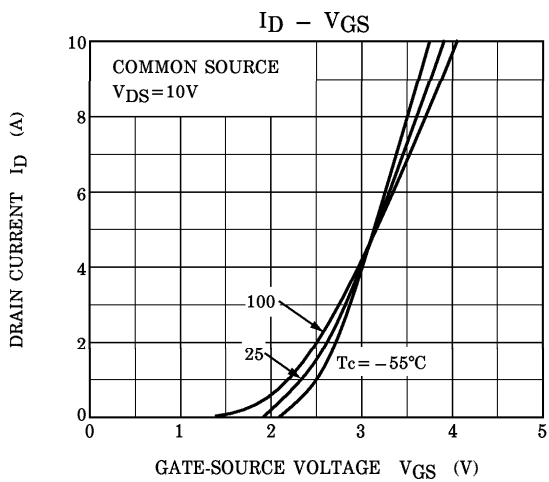
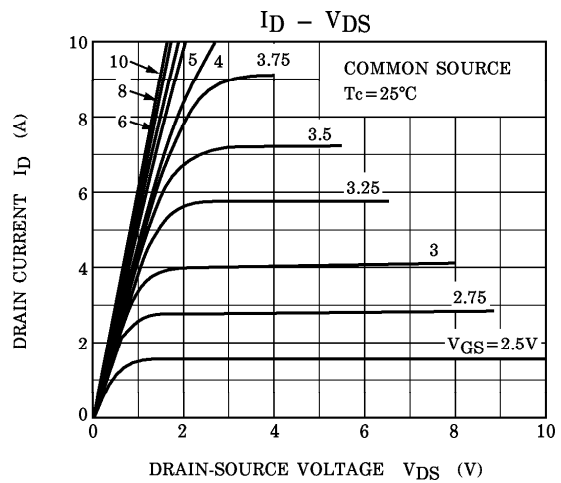
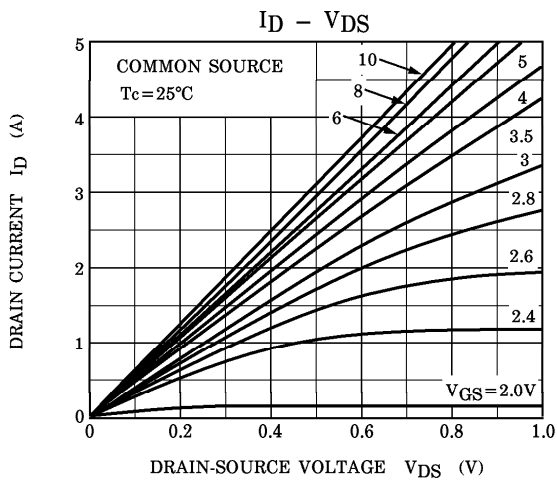
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		I_{GSS}	$V_{GS} = \pm 16V, V_{DS} = 0V$	—	—	± 10	μA
Drain Cut-off Current		I_{DSS}	$V_{DS} = 100V, V_{GS} = 0V$	—	—	100	μA
Drain-Source Breakdown Voltage		$V_{(BR)DSS}$	$I_D = 10mA, V_{GS} = 0V$	100	—	—	V
Gate Threshold Voltage		V_{th}	$V_{DS} = 10V, I_D = 1mA$	0.8	—	2.0	V
Drain-Source ON Resistance		$R_{DS(ON)}$	$V_{GS} = 4V, I_D = 2.5A$	—	0.22	0.30	Ω
			$V_{GS} = 10V, I_D = 2.5A$	—	0.17	0.23	
Forward Transfer Admittance		$ Y_{fs} $	$V_{DS} = 10V, I_D = 2.5A$	2.0	4.5	—	S
Input Capacitance		C_{iss}	$V_{DS} = 10V, V_{GS} = 0V, f = 1MHz$	—	500	—	pF
Reverse Transfer Capacitance		C_{rss}		—	80	—	
Output Capacitance		C_{oss}		—	190	—	
Switching Time	Rise Time	t_r		—	17	—	ns
	Turn-on Time	t_{on}		—	25	—	
	Fall Time	t_f		—	50	—	
	Turn-off Time	t_{off}		—	195	—	
Total Gate Charge (Gate-Source Plus Gate-Drain)		Q_g	$V_{DD} \approx 80V, V_{GS} = 10V, I_D = 5A$	—	22	—	nC
Gate-Source Charge		Q_{gs}		—	15	—	
Gate-Drain ("Miller") Charge		Q_{gd}		—	7	—	

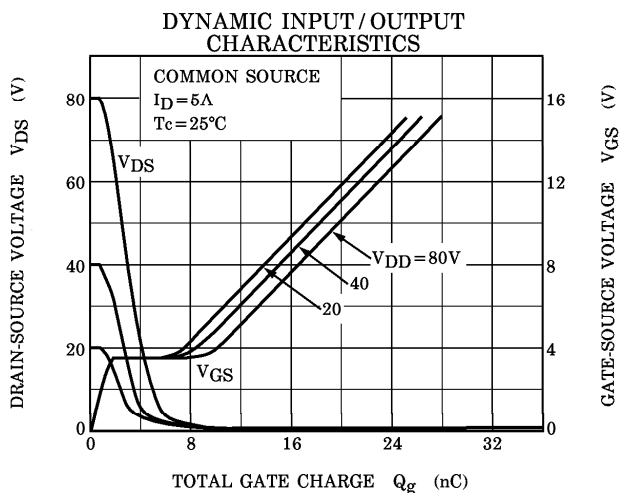
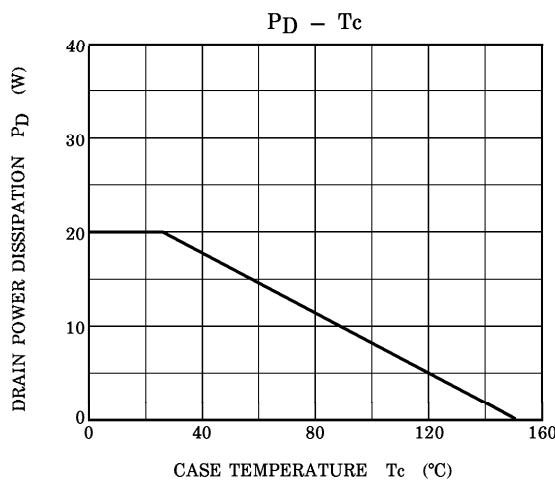
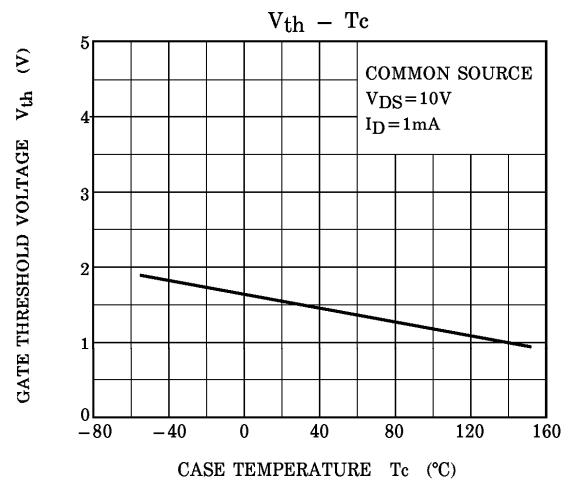
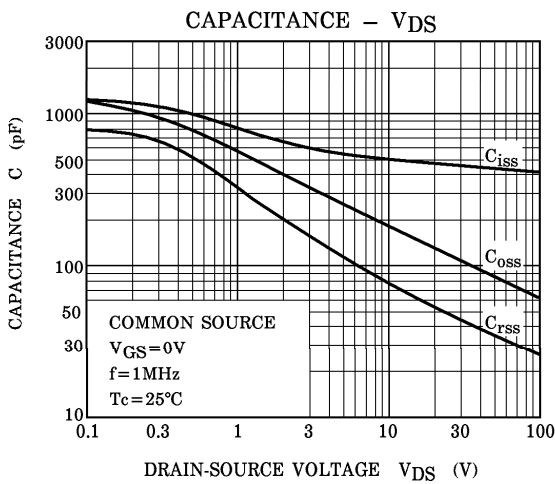
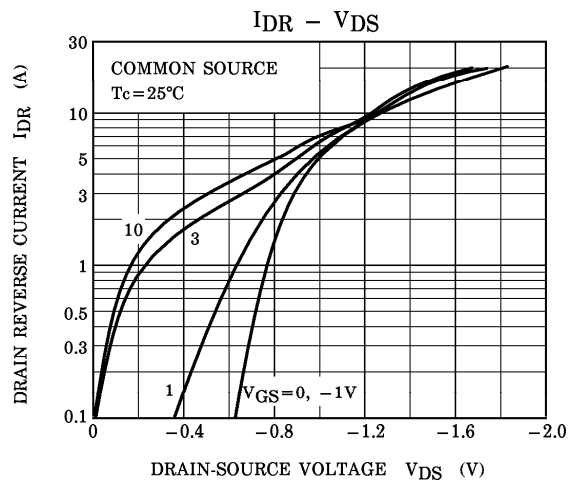
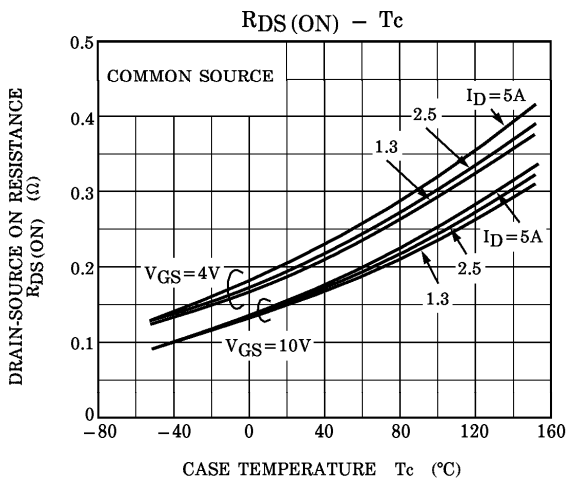
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

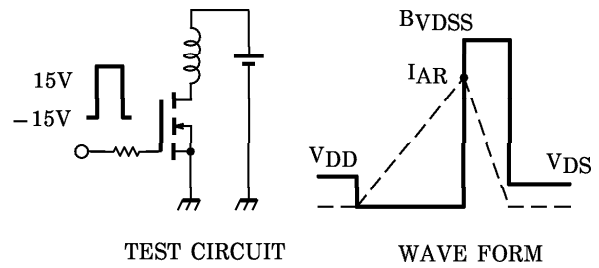
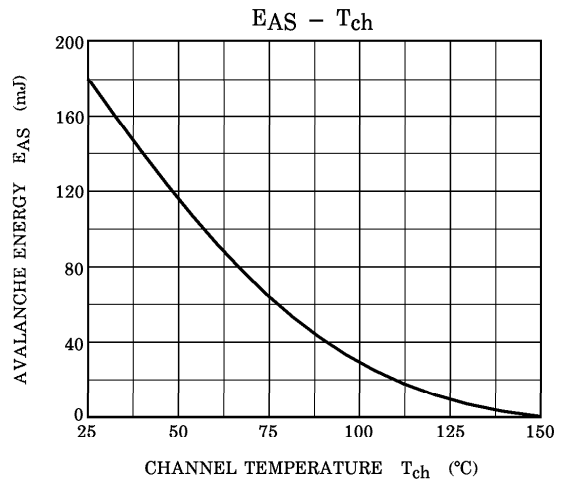
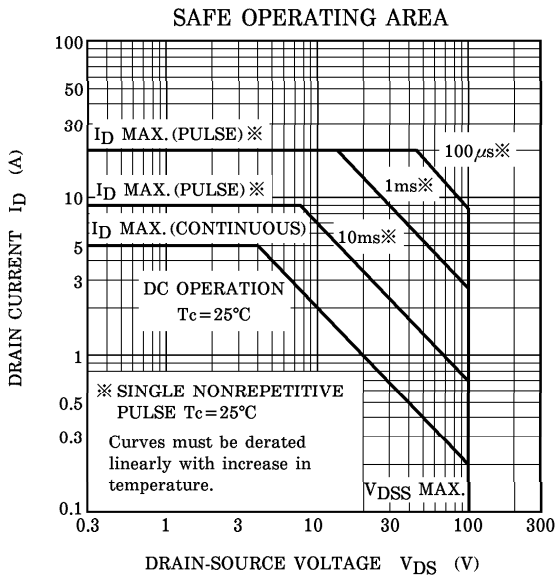
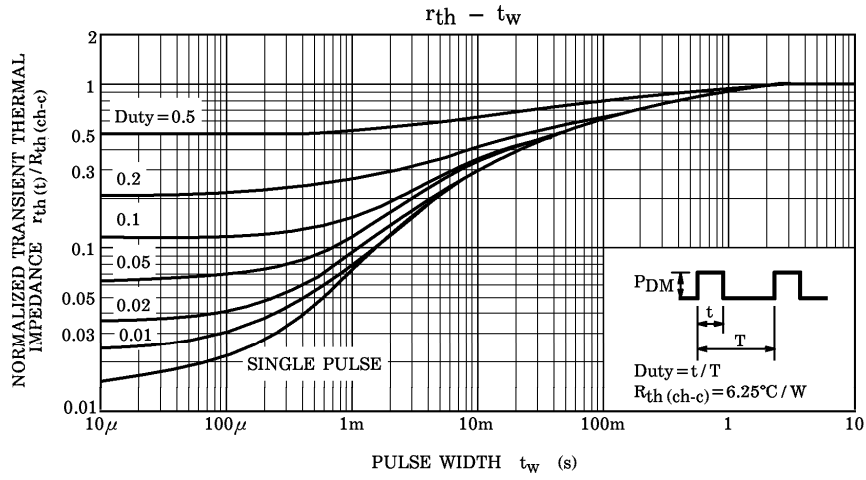
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I_{DR}	—	—	—	5	A
Pulse Drain Reverse Current	I_{DRP}	—	—	—	20	A
Diode Forward Voltage	V_{DSF}	$I_{DR} = 5A, V_{GS} = 0V$	—	—	-1.7	V
Reverse Recovery Time	t_{rr}	$I_{DR} = 5A, V_{GS} = 0V$	—	160	—	ns
Reverse Recovery Charge	Q_{rr}	$dI_{DR} / dt = 50A / \mu s$	—	0.28	—	μC

MARKING









Peak $I_{AR} = 5A$, $R_G = 25\Omega$
 $V_{DD} = 25V$, $L = 11.6mH$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$