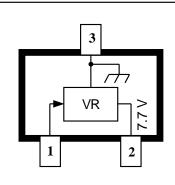
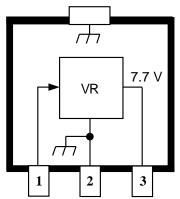
8178

PRODUCT PREVIEW

(Subject to change without notice)
April 5, 1996





Dwg. PD-0xx

ABSOLUTE MAXIMUM RATINGS

Input Voltage, V _I	V
Reverse Input Voltage, V _{RI} 120	٧
Output Current, I _O TBI	D*
Operating Temperature Range,	

 T_S -40°C to +170°C

* Output current rating is limited by input voltage, duty cycle, and ambient temperature. Under any set of conditions, do not exceed a junction temperature of +150°C. See next page.

LOW-DROPOUT, 7.7 V PREREGULATOR

Designed specifically to meet the stringent requirements of automotive applications, the A8178LLR and A8178LLT provide an output voltage of 7.7 V $\pm 15\%$ for supply voltages greater than 6.9 V. They also provide a low-dropout tracking output for supply voltages down to 2.5 V. These regulators are intended to be used with sensitive automotive electronics systems that do not have the capability of low dropout operation and cannot withstand the high-voltage transients typical of automotive applications. These 7.7 V preregulators will withstand all ISO pulses (ignition coil disconnect requires an external diode) and will continue to operate during positive power supply transients (noise).

These devices are supplied in small-outline plastic transistor packages for surface-mount applications. The A8178LLR is supplied in the minimum footprint SOT-23/TO-236AB; the A8178LLT is furnished in the SOT-89/TO-243AA for increased allowable package power dissipation.

This document contains information on a product under development. Allegro MicroSystems, Inc. reserves the right to change or discontinue this product without notice.

Always order by complete part number:

A8178LLT



ELECTRICAL CHARACTERISTICS over operating temperature range.

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Output Voltage	V _o	6.9 V ≤ V _I ≤ 24 V	6.5	7.7	8.8	V
		2.5 V ≤ V ₁ ≤ 6.9 V	V _I - 0.4	_	_	V
		V _I = 120 V, pulse test	_	_	11	V
Output Volt. Temp. Coeff.	$\alpha_{ m VO}$		_	_	TBD	mV/°C
Line Regulation	$\Delta V_{O(\Delta VI)}$		_	_	TBD	mV
Load Regulation	$\Delta V_{O(\Delta IO)}$		_	_	TBD	mV
Quiescent Current	I _Q	$V_1 = 13.5 \text{ V}, I_0 = 0$	-	_	2.5	mA
		$2.5 \text{ V} \le \text{V}_1 \le 8 \text{ V}, \text{ I}_0 = 0$	_	_	5.6	mA
Power Dissipation	P _d	V _I = 13.5 V, I _O = 15 mA	_	_	130	mW
Thermal Resistance*	$R_{\theta JA}$	A8178LLR		575	_	°C/W
		A8178LLT	_	200	_	°C/W

Typical values are at T_A = +25°C and are given for circuit design information only.

INTERNAL PROTECTIVE FEATURES

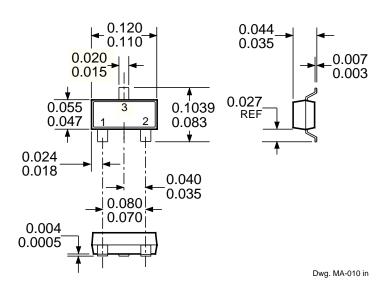
ISO Pulse No.	Test	Test Conditions (at $T_A = +25^{\circ}C$)
1	Inductive Turn Off (Negative)	$V_S = -100 \text{ V}, R_S = 10 \Omega, t_r = 1 \mu \text{s}, t_d = 2 \text{ ms}$
2	Inductive Turn Off (Positive)	$V_S = 100 \text{ V}, R_S = 10 \Omega, t_r = 1 \mu s, t_d = 50 \mu s, non-operating$
3a	Capacitive/Inductive Coupling (Neg)	$V_S = -150 \text{ V}, R_S = 50 \Omega, t_r = 50 \text{ ns}, t_d = 100 \text{ ns}$
3b	Capacitive/Inductive Coupling (Pos)	$V_S = 100 \text{ V}, R_S = 50 \Omega, t_r = 50 \text{ ns}, t_d = 100 \text{ ns}$
4	Reverse Battery	V _S = -14 V, t _d = 20 s, non-operating
5	Load Dump	$V_S = 86.5 \text{ V}, R_S = 0.5 \Omega, t_r = 5 \text{ ms}, t_d = 400 \text{ ms}$
6	Ignition Coil Disconnect EXTERNAL PROTECTION REQ'D	V_S = -300 V, R_S = 30 Ω , t_r = 60 μs , t_d = 300 μs
7	Field Decay (Negative)	V_S = -80 V, R_S = 10 Ω , t_r = 5 ms, t_d = 100 ms



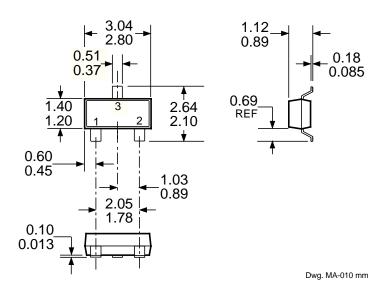
^{*}Mounted on 2.24" x 2.24" solder-coated copper-clad board in still air.

A8178LLR

Dimensions in Inches (for reference only)



Dimensions in Millimeters (controlling dimensions)

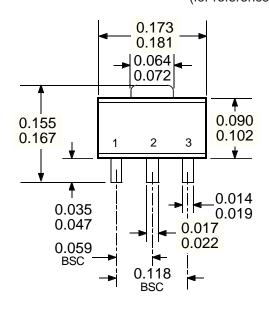


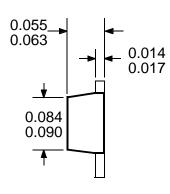
NOTES: 1. Lead spacing tolerance is non-cumulative.

2. Exact body and lead configuration at vendor's option within limits shown.

8178 LOW-DROPOUT, 7.7 V PREREGULATOR

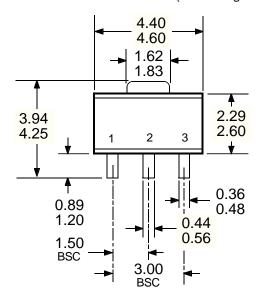
A8178LLTDimensions in Inches (for reference only)

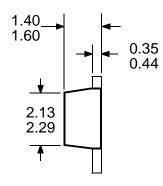




Dwg. MA-009-3 in

Dimensions in Millimeters (controlling dimensions)





Dwg. MA-009-3 mm

NOTES: 1. Lead spacing tolerance is non-cumulative.

2. Exact body and lead configuration at vendor's option within limits shown.

Allegro MicroSystems, Inc. reserves the right to make, from time to time, such departures from the detail specifications as may be required to permit improvements in the design of its products.

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