DATA SHEET

AC10DSMA,AC10FSMA

10 A RESIN INSULATION TYPE TRIAC

DESCRIPTION

NEC

The AC10DSMA and AC10FSMA are resin insulation type TRIACs with an effective current of 10 A (Tc = 85° C).

These products are covered with resin mold on the entire case and are electrically insulated with electrodes, giving them a considerable advantage over conventional TRIACs when mounting on a heatsink board or performing high-density mounting.

These products features ratings and electrical characteristics equal to TO-220AB package TRIAC and a high reliability design.

FEATURES

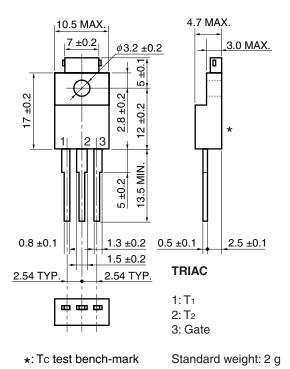
- Insulation type TRIAC fully covered with resin on the entire case other than electrode leads
- Insulation voltage and conduction equal to conventional mica and polyester film
- Can be replaced with TO-220AB package
- High allowable on-current when using a single unit

APPLICATIONS

Non-contact switches of motor speed control, heater temperature control, lamp light control

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PACKAGE DRAWING (Unit: mm)



MAXIMUM RATINGS

Parameter	Symbol	AC10DSMA	AC10FSMA	Unit	Remarks	
Non-repetitive Peak Off-state Voltage	Vdsm	500	500 700			
Repetitive Peak Off-state Voltage	Vdrm	400	600	V	-	
Effective On-state Current	IT(RMS)	10 (T c :	= 85°C)	А	Refer to Figure 11 and 12.	
Surge On-state Current	Ітям	80 (50 H	z 1 cycle)	А	Refer to Figure 2.	
		88 (60 Hz 1 cycle)				
Fusing Current	∕i⊤²dt	28 (1 ms ≤	i t ≤ 10 ms)	A ² s	-	
Critical Rate Rise of On-state Current	dl⊤/dt	5	0	A/µs	-	
Peak Gate Power Dissipation	Рсм	5.0 (f ≥ 50 Hz	z, Duty ≤ 10%)	W	-	
Average Gate Power Dissipation	P _{G(AV)}	0	.5	W	-	
Peak Gate Current	Ідм	±3 (f ≥ 50 Hz	, Duty ≤ 10%)	А	-	
Junction Temperature	Tj	-40~	+125	°C	-	
Storage Temperature	Tstg	-55~	+150	°C	-	

ELECTRICAL CHARACTERISTICS (T_j = 25°C)

Parameter		Symbol	Conditions		MIN.	TYP.	MAX.	Unit	Remarks
Repetitive Peak Off-state Current		IDRM	Vdm = Vdrm	T _j = 25°C	-	-	100	μA	_
				T _j = 125°C	-	-	2	mA	_
On-state Voltage		Vtm	Ітм = 10 А		-	-	1.3	V	Refer to Figure 1.
Gate Trigger Current	Mode I	Іст	Vрм = 12 V,	T2+, G+	-	-	20	mA	Refer to Figure 4.
	П		RL = 30 Ω	T2, G+	_	_	_		
	=			T2, G	_	_	20		
	IV			T2+, G-	-	-	20		
Gate Trigger Voltage	Mode I	Vgt	Vрм = 12 V,	T2+, G+	-	-	1.5	v	Refer to Figure 4.
	Ш		RL = 30 Ω	T ₂ –, G+	-	-	-		
	=			T2, G	-	-	1.5		
	IV			T2+, G-	-	-	1.5		
Gate Non-trigger Voltage		Vgd	$T_j = 125^{\circ}C, V_{DM} = \frac{1}{2} V_{DRM}$		0.3	-	-	V	-
Holding Current		Ін	V _{DM} = 24 V, I _{TM} = 10 A		_	30	_	mA	_
Critical Rate Rise of Off-state Voltage		dv/dt	$T_j = 125^{\circ}C, V_{DM} = \frac{2}{3} V_{DRM}$		-	100	_	V∕µs	_
Commutating Critical Rate Rise of		(dv/dt)c	T _j = 125°C,		10	-	_	V∕µs	_
Off-state Voltage			(diī/dt)c = -5 A/ms, V _D = 400 V						
Thermal Resistance Note		Rth(j-c)	Junction-to-case AC		_	-	3.5	°C/W	Refer to Figure 13.

Note The thermal resistance with a 50 Hz or 60 Hz sine wave current, as shown in the following expression:

 $R_{th(j-c)} = \frac{T_{j(max)} - T_{c}}{P_{T(AV)}}$

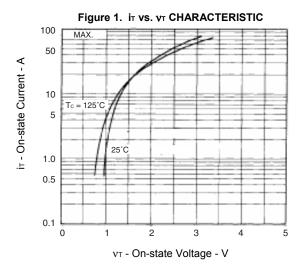
T_{j(max)}: Maximum junction temperature

Tc: Case temperature

PT(AV): Average on-dissipation

TYPICAL CHARACTERISTICS

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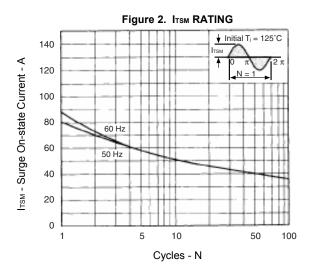
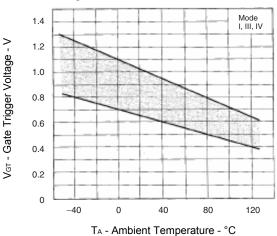
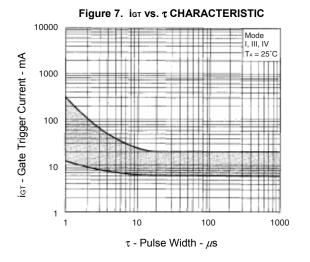


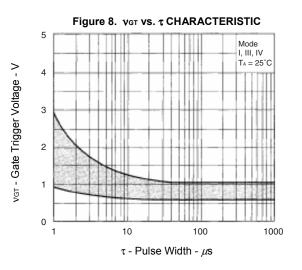
Figure 3. GATE RATING 10 $T_1 = -40 \text{ to } + 125^{\circ}\text{C}$ V_G - Gate Voltage - V 8 6 $\begin{array}{l} P_{\text{GM}}=5 \ W \\ f \geq 50 \ Hz \\ Duty \leq 10\% \end{array}$ 4 a(AV) = 0.5 W 2 0 0 0.5 1.0 1.5 2.0 2.5 3.0 IG - Gate Current - A

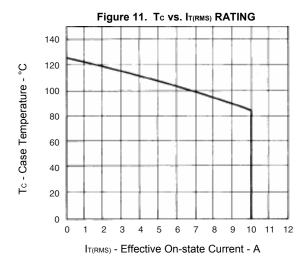
Figure 4. GATE CHARACTERISTIC 2.0 Mode I, III, IV V_{GT} - Gate Trigger Voltage - V 1.5 25°C . -40°C 125°C 1.0 0.5 0 0 10 20 30 40 IGT - Gate Trigger Current - mA

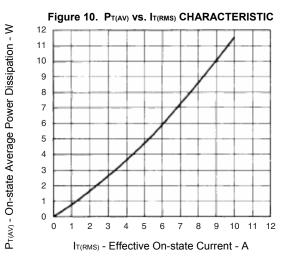
Figure 6. VGT vs. TA CHARACTERISTIC

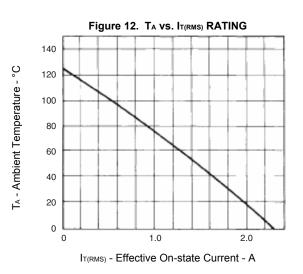


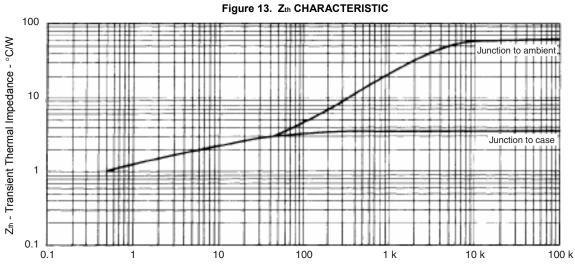












Cycles (50 Hz)

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