- Designed for Complementary Use with BDW53, BDW53A, BDW53B, BDW53C and BDW53D
- 40 W at 25°C Case Temperature
- 4 A Continuous Collector Current
- Minimum h_{FE} of 750 at 3 V, 1.5 A

TO-220 PACKAGE (TOP VIEW) B 1 C E 3

Pin 2 is in electrical contact with the mounting base.

MDTRACA

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING	SYMBOL	VALUE	UNIT	
	BDW54		-45	
	BDW54A		-60	
Collector-base voltage (I _E = 0)	BDW54B	V_{CBO}	-80	V
	BDW54C		-100	
	BDW54D		-120	
	BDW54		-45	
	BDW54A		-60	
Collector-emitter voltage (I _B = 0) (see Note 1)	BDW54B	V_{CEO}	-80	V
	BDW54C		-100	
	BDW54D		-120	
Emitter-base voltage		V _{EBO}	-5	V
Continuous collector current		I _C	-4	Α
Continuous base current	I _B	-50	mA	
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)	P _{tot}	40	W	
Continuous device dissipation at (or below) 25°C free air temperature (see Note	P _{tot}	2	W	
Unclamped inductive load energy (see Note 4)	½Ll _C ²	25	mJ	
Operating junction temperature range	T _j	-65 to +150	°C	
Operating temperature range	T _{stg}	-65 to +150	°C	
Operating free-air temperature range	T _A	-65 to +150	°C	

NOTES: 1. These values apply when the base-emitter diode is open circuited.

- 2. Derate linearly to 150°C case temperature at the rate of 0.32 W/°C.
- 3. Derate linearly to 150°C free air temperature at the rate of 16 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH, $I_{B(on)}$ = -5 mA, R_{BE} = 100 Ω , $V_{BE(off)}$ = 0, R_S = 0.1 Ω , V_{CC} = -20 V.



BDW54, BDW54A, BDW54B, BDW54C, BDW54D PNP SILICON POWER DARLINGTONS

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electrical characteristics at 25°C case temperature (unless otherwise noted)

	PARAMETER		TEST	CONDITIONS		MIN	TYP	MAX	UNIT
V	Collector-emitter	1 20 mA	1 0	(see Note 5)	BDW54 BDW54A BDW54B	-45 -60 -80			V
V _{(BR)CEO} brea	breakdown voltage	I _C = -30 mA	ι _B = 0	(see Note 5)	BDW54C BDW54D	-100 -120			V
I _{CEO}	Collector-emitter	V _{CE} = -30 V			BDW54			-0.5	
			$I_B = 0$		BDW54A BDW54B			-0.5 -0.5	mA
		$V_{CE} = -50 \text{ V}$ $V_{CE} = -60 \text{ V}$	$I_B = 0$		BDW54C BDW54D			-0.5 -0.5	
	Collector cut-off current		$I_E = 0$		BDW54A			-0.2 -0.2	
		V _{CB} = -100 V	=		BDW54B BDW54C			-0.2 -0.2	
I _{CBO}		V _{CB} = -45 V	$I_{E} = 0$ $I_{E} = 0$	T _C = 150°C	BDW54D BDW54			-0.2 -5	mA
		$V_{CB} = -60 \text{ V}$ $V_{CB} = -80 \text{ V}$ $V_{CB} = -100 \text{ V}$	$I_E = 0$ $I_E = 0$	T _C = 150°C T _C = 150°C	BDW54A BDW54B BDW54C			-5 -5 -5	
I _{EBO}	Emitter cut-off current	$V_{CB} = -120 \text{ V}$ $V_{EB} = -5 \text{ V}$		T _C = 150°C	BDW54D			-5 -2	mA
h _{FE}	Forward current transfer ratio	$V_{CE} = -3 V$ $V_{CE} = -3 V$	•	(see Notes 5 and 6)		750 100		20000	
V _{BE(on)}	Base-emitter voltage	V _{CE} = -3 V	I _C = -1.5 A	(see Notes 5 and 6)				-2.5	V
V _{CE(sat)}	Collector-emitter saturation voltage	$I_B = -30 \text{ mA}$ $I_B = -40 \text{ mA}$	•	(see Notes 5 and 6)				-2.5 -4	V
V _{EC}	Parallel diode forward voltage	I _E = -4 A	I _B = 0					-3.5	V

NOTES: 5. These parameters must be measured using pulse techniques, $t_p = 300 \ \mu s$, duty cycle $\leq 2\%$.

thermal characteristics

	PARAMETER	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			3.125	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			62.5	°C/W

resistive-load-switching characteristics at 25°C case temperature

P	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t _{on}	Turn-on time	I _C = -2 A	$I_{B(on)} = -8 \text{ mA}$	$I_{B(off)} = 8 \text{ mA}$		1		μs
t _{off}	Turn-off time	$V_{BE(off)} = 5 V$	$R_L = 15 \Omega$	$t_{p} = 20 \ \mu s, \ dc \le 2\%$		4.5		μs

[†] Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

PRODUCT INFORMATION

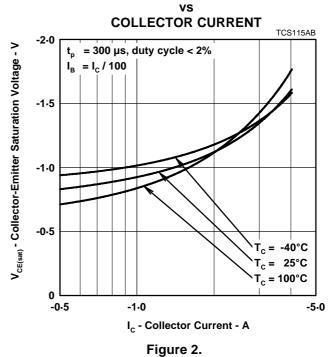
^{6.} These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

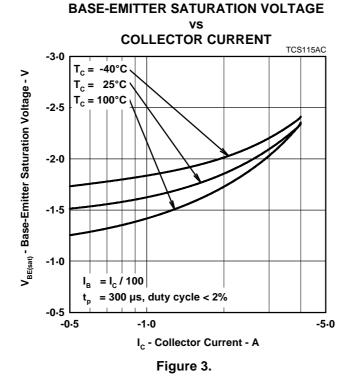
TYPICAL CHARACTERISTICS

TYPICAL DC CURRENT GAIN COLLECTOR CURRENT TCS115AD 20000 $T_c = -40^{\circ}C$ 25°C 10000 $T_c = 100$ °C h_{FE} - Typical DC Current Gain 1000 -3 V = 300 μ s, duty cycle < 2% 100 -0-5 -1-0 -5-0 I_C - Collector Current - A

Figure 1.

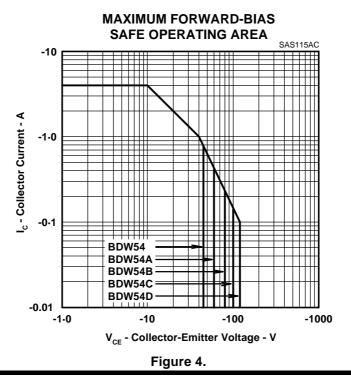
COLLECTOR-EMITTER SATURATION VOLTAGE





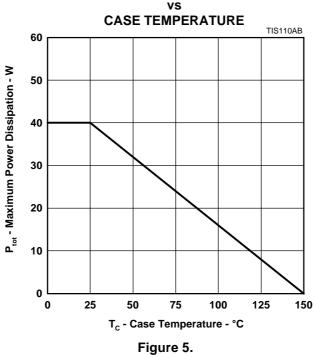


MAXIMUM SAFE OPERATING REGIONS



THERMAL INFORMATION

MAXIMUM POWER DISSIPATION



PRODUCT INFORMATION

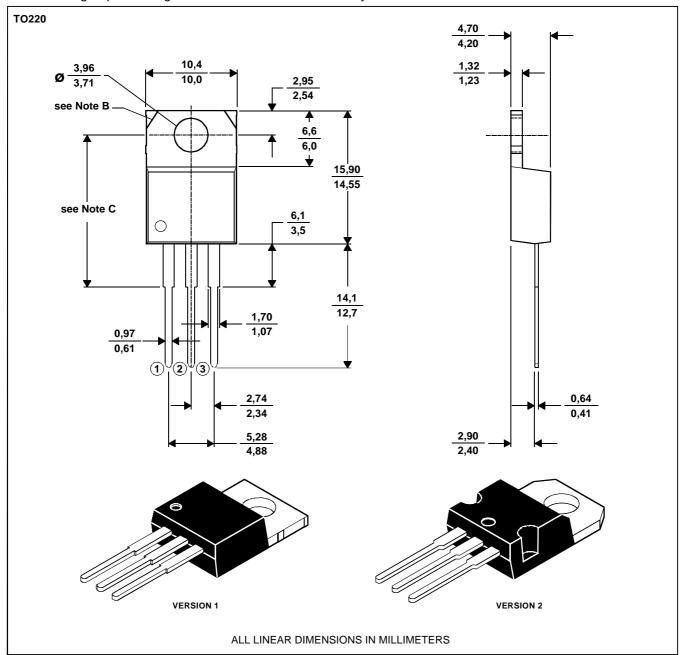
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MECHANICAL DATA

TO-220

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTES: A. The centre pin is in electrical contact with the mounting tab.

- B. Mounting tab corner profile according to package version.
- C. Typical fixing hole centre stand off height according to package version. Version 1, 18.0 mm. Version 2, 17.6 mm.

MDXXBE

PRODUCT INFORMATION

BDW54, BDW54A, BDW54B, BDW54C, BDW54D PNP SILICON POWER DARLINGTONS

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