

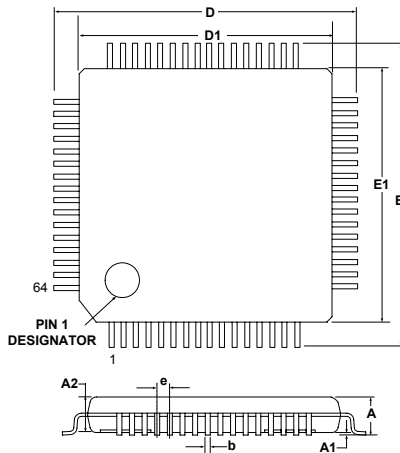


### Selected Electrical Specifications

( $T_A = -40$  to  $+85$  C°,  $V_{DD} = 2.7$  V unless otherwise specified)

Parameter	Conditions	Min	Typ	Max	Units
<b>Global Characteristics</b>					
Supply Voltage		2.7	—	3.6	V
Supply Current with CPU active	Clock = 25 MHz Clock = 1 MHz Clock = 32 kHz; $V_{DD}$ Monitor Enabled	—	10 0.5 20	—	mA mA $\mu$ A
Supply Current (shutdown)	Oscillator off; $V_{DD}$ Monitor Disabled	—	0.1	—	$\mu$ A
Clock Frequency Range		DC	—	25	MHz
<b>A/D Converter</b>					
Resolution		—	10	—	bits
Integral Nonlinearity		—	—	$\pm 1$	LSB
Differential Nonlinearity	Guaranteed Monotonic			$\pm 1$	LSB
Signal-to-Noise Plus Distortion		59	—	—	dB
Throughput Rate		—	—	100	kSPS
Input Voltage Range		0	—	$V_{REF}$	V
<b>Comparators</b>					
Supply Current	(each Comparator)	—	1.5	—	$\mu$ A
Response Time	(CP+ – CP-) = 100 mV	—	4	—	$\mu$ s

### Package Information



	MIN (mm)	NOM (mm)	MAX (mm)
A	-	-	1.20
A1	0.05	-	0.15
A2	0.95	-	1.05
b	0.17	0.22	0.27
D	-	12.00	-
D1	-	10.00	-
e	-	0.50	-
E	-	12.00	-
E1	-	10.00	-

### C8051F040DK Development Kit

