

GENERAL DESCRIPTION

The CM2862 family is a positive voltage linear regulator developed utilizing CMOS technology featured low quiescent current (30 μ A typ.), low dropout voltage, and high output voltage accuracy. Built-in low on-resistance transistor provides low dropout voltage and large output current. A 2.2 μ F or greater can be used as an output capacitor.

The SOT-89 packages are attractive for "Pocket" and "Hand Held" applications.

These robust devices are designed to prevent device failure under the worst operation condition with both Thermal Shutdown and Current Fold-back.

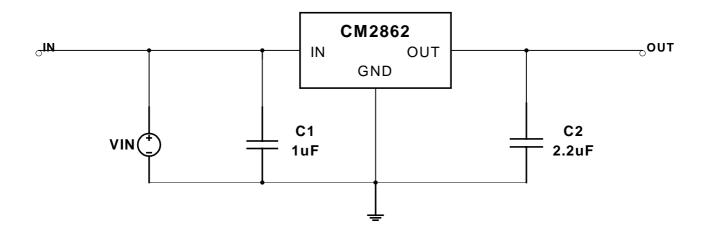
FEATURES

- Very Low Dropout Voltage
- ♦ Low Current Consumption: Typ. 30μA, Max. 35μA
- ♦ High Accuracy Output Voltage: +/- 1.5%
- Guaranteed 600mA Output
- ♦ Thermal Shutdown
- ♦ Current Limiting
- ♦ Compact Package: SOT-89
- ♦ Factory Pre-set Output Voltages
- ♦ Short Circuit Current Fold-Back
- ♦ Low Temperature Coefficient

APPLICATIONS

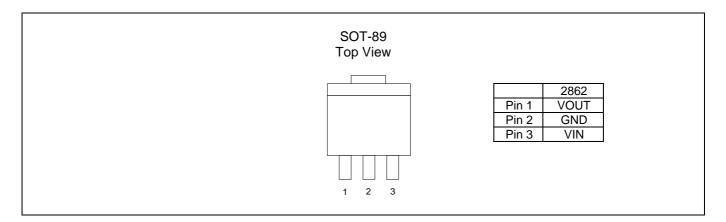
- Battery-powered devices
- Personal communication devices
- ♦ Home electric/electronic appliances
- PC peripherals

TYPICAL APPLICATIONS

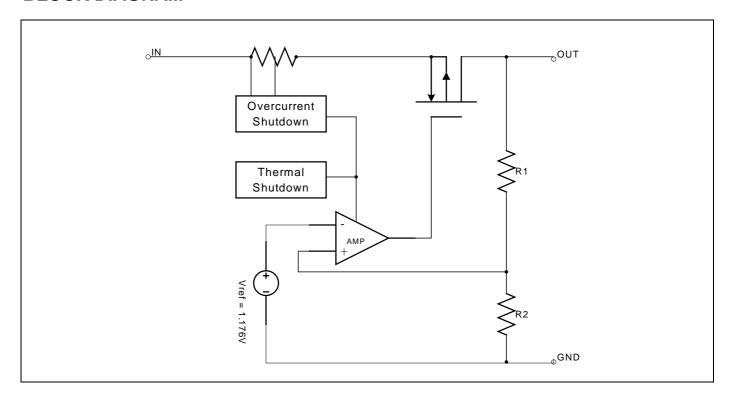




PIN CONFIGURATION



BLOCK DIAGRAM



ORDERING INFORMATION

Part Number	Output Voltage	Temperature Range	Package
CM2862KIM89	2.5V	-40℃ ~ +85℃	SOT-89
CM2862SIM89	3.3V	-40℃ ~ +85℃	SOT-89

Note: For other pre-set output voltage, please contact Champion Sales office.



ABSOLUTE MAXIMUM RATINGS

OPERATING RATINGS

Input Voltage	+7V	Supply Voltage	4.5V to 5.5V
Output Current	1A	Ambient Temperature Range (T	A)40°C to +85°C
Output Voltage	GND-0.3V to V _{IN} +0.3V	Junction Temperature Range	40°C to +125°C
ESD Classification	B		

THERMAL INFORMATION

Parameter		Maximum	Unit
Thermal Resistance (Θ_{jc})	SOT-89	100	°C/W
Thermal Resistance (⊖ _{ja})	SOT-89	180	°C/W
Internal Power Dissipation (P_D) ($\Delta T = 100^{\circ}C$, No Heatsink)	SOT-89	400	mW
Maximum Junction Temperature		150	°C
Maximum Lead Temperature (10 Sec)		300	$^{\circ}\mathbb{C}$

ELECTRICAL CHARACTERISTICS

 $T_{A}=+25^{\circ}\!\!\!\mathrm{C}\,;\,V_{IN}=V_{IN(MIN)}$ unless otherwise noted

Danamatan	Cumha	Test Conditions		CM2862			T	
Parameter	Symbol			Min.	Тур.	Max.	Unit	
Input Voltage	V_{IN}			Note 1		7	V	
Output Voltage Accuracy	Vout	I _O =	1mA		-1.5		1.5	%
-		1.5	1.5V	/ <v<sub>O(NOM)<=2.0V</v<sub>			1000	.,
Dropout Voltage	V _{DROPOUT}	$I_0 = 600 \text{mA},$	2.0V	' <v<sub>O(NOM)<=2.8V</v<sub>			800	mV
		$V_{OUT}=V_{O(NOM)}-1.5\%$	2.8\	/ <v<sub>O(NOM)<3.8V</v<sub>			600	mV
Output Current	lo	V _{OUT} > 1.2V		600			mA	
Current Limit	I _{LIM}	$V_{OUT} > 1.2V, V_{IN} = V_{IN(MIN)}$		600	1000		mA	
Short Circuit Current	I _{SC}	V _{OUT} < 0.8V			250		mA	
Quiescent Current	IQ	I _O = 0mA			30	50	μ A	
Ground Pin Current	I _{GND}	I _O = 1mA to 600mA			30	50	μ A	
Line Degulation	Line Regulation REG _{LINE}	I _{OUT} =5mA, V _{IN} =V _{OUT} +1	1 to	V _{OUT} <= 2.0V			0.15	%
Line Regulation		V _{OUT} +2		$V_{OUT} > 2.0V$		0.02	0.1	%
Load Regulation	REG _{LOAD}	I _O =1mA to 600mA			0.2	1	%	
Over Temperature Shutdown	OTS					150		$^{\circ}\mathbb{C}$
Over Temperature Hystersis	OTH					30		$^{\circ}\mathbb{C}$
V _{OUT} Temperature Coefficient	TC					30		ppm/°C
Power Supply Rejection		$I_0 = 100$ mA $C_0=2.2\mu F$ ceramic		f=1kHz		50		
	PSRR C		_ [f=10kHz		20		dB
			<i>.</i>	f=100kHz		15		
Outset Valta va Nai	- N1	f=10Hz to 100kHz	<u>.</u>	C _O =2.2µF		30		\ /
Output Voltage Noise eN	$I_{O} = 10 \text{mA}, C_{VBG} = 0$	лЕ	C _O =100µF		20		μ Vrms	

Note 1. $V_{IN(MIN)} = V_{OUT} + V_{DROPOUT}$

Note 2. As V_{IN} is larger than $V_{IN(MIN)}$, the Current Limit and output short current Spec value will increase



DETAILED DESCRIPTION

The CM2862 family of CMOS regulators contain a PMOS pass transistor, voltage reference, error amplifier, over-current protection, thermal shutdown, and short circuit protection.

The P-channel pass transistor receives data from the error amplifier, over-current shutdown, short output protection, and thermal protection circuits. During normal operation, the error amplifier compares the output voltage to a precision reference. Over-current and Thermal shutdown circuits become active when the junction temperature exceeds 150° C, or the current exceeds 600mA. During thermal shutdown, the output voltage remains low. Normal operation is restored when the junction temperature drops below 120° C.

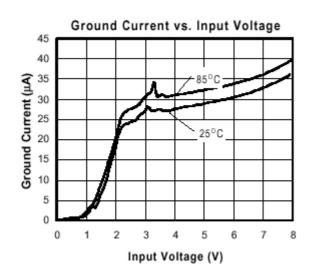
The CM2862 switches from voltage mode to current mode when the load exceeds the rated output current. This prevents over-stress. The CM2862 also incorporates current fold-back to reduce power dissipation when the output is short-circuited. This feature becomes active when the output drops below 0.8V, and reduces the current flow by 65%. Full current is restored when the voltage exceeds 0.8V.

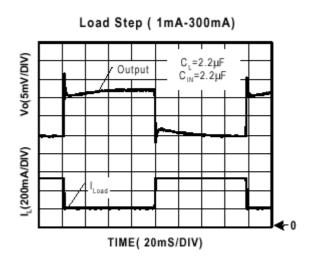
EXTERNAL CAPACITOR

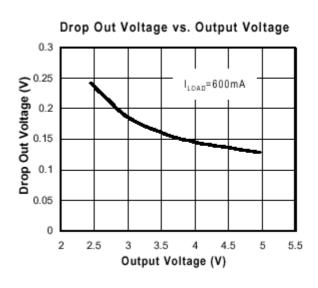
The CM2862 is stable with an output capacitor to ground of 2.2µF or greater. It can keep stable even with higher or poor ESR capacitors. A second capacitor is recommended between the input and ground to stabilize VIN. The input capacitor should be larger than 0.1µF to have a beneficial effect. All capacitors should be placed in close proximity to the pins. A "quiet" ground termination is desirable.

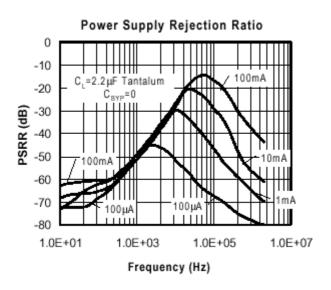


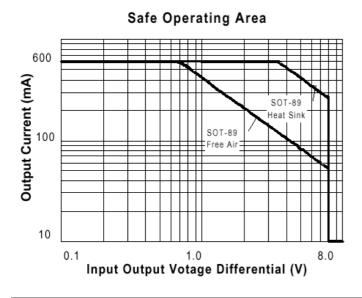
TYPICAL ELECTRICAL CHARACTERISTICS

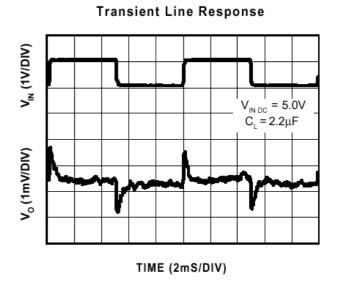




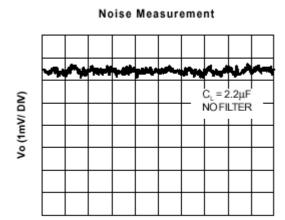




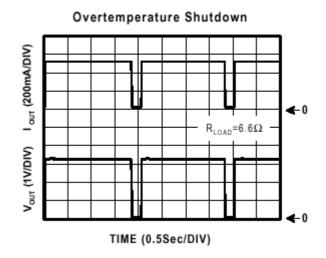


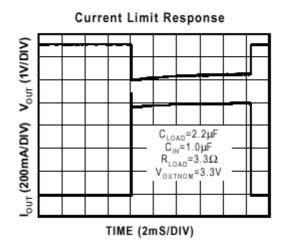


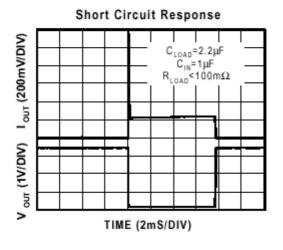




TIME (20mS/DIV)

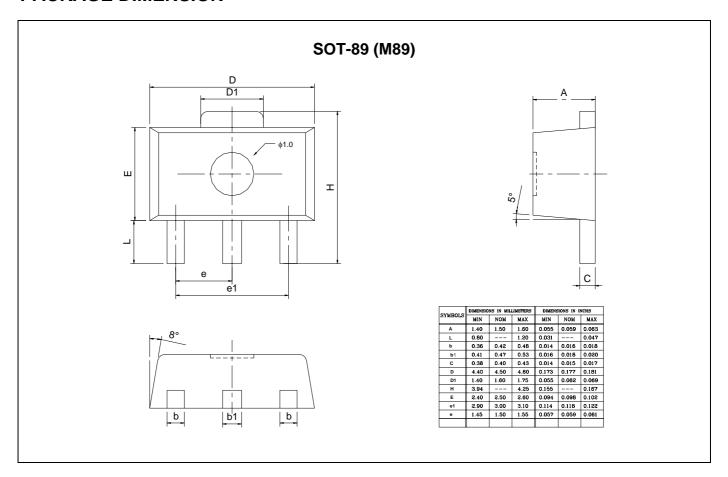








PACKAGE DIMENSION





NUMBERING SCHEME

Ordering Number: CM2862XYZ (note1)

note1:

CM2862: 600mA CMOS LDO \underline{X} : Suffix for voltage output (note 2) \underline{Y} : Suffix for Temperature Range (note 3) \underline{Z} : Suffix for Package Type (note 4)

note 2: see CMOS LDO Voltage Suffix Table

note 3:

Y= I : -40 $^{\circ}$ C ~+85 $^{\circ}$ C (only I grade support for all CMOS LDOs)

note 4:

Z is single alphabet with or without digits

M89: SOT-89 (TR only)

CMOS LDO Voltage Suffix Table

Output Voltage	Suffix	Output Voltage	Suffix
1.5V	A	3.0V	Р
1.6V	В	3.1V	Q
1.7V	С	3.2V	R
1.8V	D	3.3V	S
1.9V	E	3.4V	Т
2.0V	F	3.5V	U
2.1V	G	3.6V	V
2.2V	Н	3.7V	W
2.3V	I	3.8V	X
2.4V	J	3.9V	Y
2.5V	K	4.0V	Z
2.6V	L		
2.7V	M		
2.8V	N		
2.9V	0		



IMPORTANT NOTICE

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