

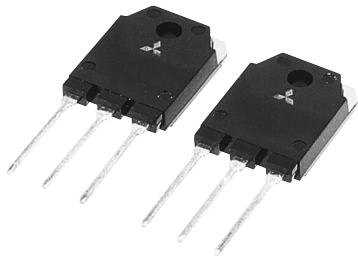
**PRELIMINARY**  
 Notice: This is not a final specification.  
 Some parametric limits are subject to change.

MITSUBISHI Pch POWER MOSFET

# FX50SMJ-03

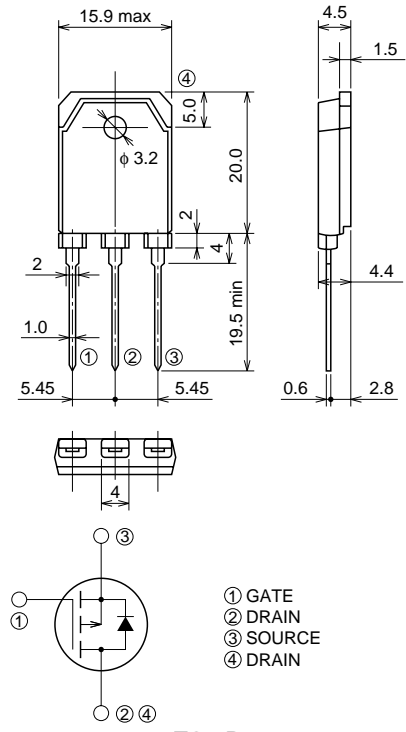
HIGH-SPEED SWITCHING USE

**FX50SMJ-03**



- 4V DRIVE
- $V_{DSS}$  ..... -30V
- $r_{DS(ON)}$  (MAX) .....  $35m\Omega$
- $I_D$  ..... -50A
- Integrated Fast Recovery Diode (TYP.) ..... 55ns

**OUTLINE DRAWING** Dimensions in mm



① GATE  
 ② DRAIN  
 ③ SOURCE  
 ④ DRAIN

**TO-3P**

## APPLICATION

Motor control, Lamp control, Solenoid control  
 DC-DC converter, etc.

## MAXIMUM RATINGS (Tc = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit
$V_{DSS}$	Drain-source voltage	$V_{GS} = 0V$	-30	V
$V_{GSS}$	Gate-source voltage	$V_{DS} = 0V$	$\pm 20$	V
$I_D$	Drain current		-50	A
$I_{DM}$	Drain current (Pulsed)		-200	A
$I_{DA}$	Avalanche drain current (Pulsed)	$L = 10\mu H$	-50	A
$I_S$	Source current		-50	A
$I_{SM}$	Source current (Pulsed)		-200	A
$P_D$	Maximum power dissipation		70	W
$T_{ch}$	Channel temperature		-55 ~ +150	°C
$T_{stg}$	Storage temperature		-55 ~ +150	°C
—	Weight	Typical value	4.8	g

Jan.1999

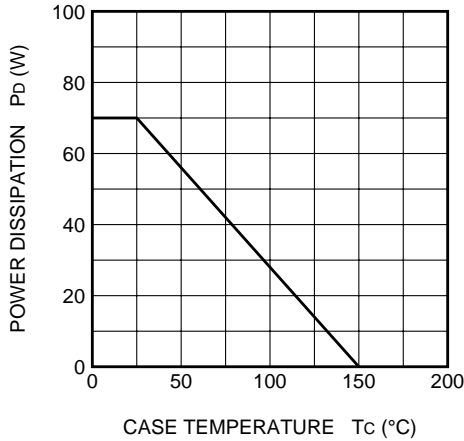
**PRELIMINARY**  
 Notice: This is not a final specification.  
 Some parametric limits are subject to change.

**ELECTRICAL CHARACTERISTICS** (Tch = 25°C)

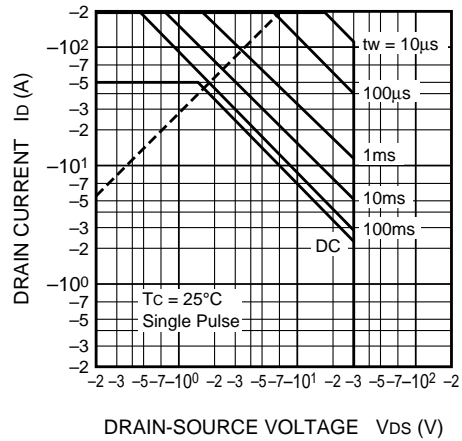
Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V(BR)DSS	Drain-source breakdown voltage	ID = -1mA, VDS = 0V	-30	—	—	V
IGSS	Gate-source leakage current	VGS = ±20V, VDS = 0V	—	—	±0.1	μA
IDSS	Drain-source leakage current	VDS = -30V, VGS = 0V	—	—	-0.1	mA
VGS(th)	Gate-source threshold voltage	ID = -1mA, VDS = -10V	-1.3	-1.8	-2.3	V
rDS(ON)	Drain-source on-state resistance	ID = -25A, VGS = -10V	—	28	35	mΩ
rDS(ON)	Drain-source on-state resistance	ID = -9A, VGS = -4V	—	54	72	mΩ
VDS(ON)	Drain-source on-state voltage	ID = -25A, VGS = -10V	—	-0.70	-0.88	V
yfs	Forward transfer admittance	ID = -25A, VDS = -10V	—	23	—	S
Ciss	Input capacitance	VDS = -10V, VGS = 0V, f = 1MHz	—	4270	—	pF
Coss	Output capacitance		—	695	—	pF
Crss	Reverse transfer capacitance		—	342	—	pF
td(on)	Turn-on delay time		—	21	—	ns
tr	Rise time	VDD = -15V, ID = -25A, VGS = -10V, RGEN = RGS = 50Ω	—	103	—	ns
td(off)	Turn-off delay time		—	223	—	ns
tf	Fall time		—	122	—	ns
VSD	Source-drain voltage		IS = -25A, VGS = 0V	—	-1.0	-1.5
Rth(ch-c)	Thermal resistance	Channel to case	—	—	1.79	°C/W
trr	Reverse recovery time	IS = -25A, dis/dt = 50A/μs	—	55	—	ns

**PERFORMANCE CURVES**

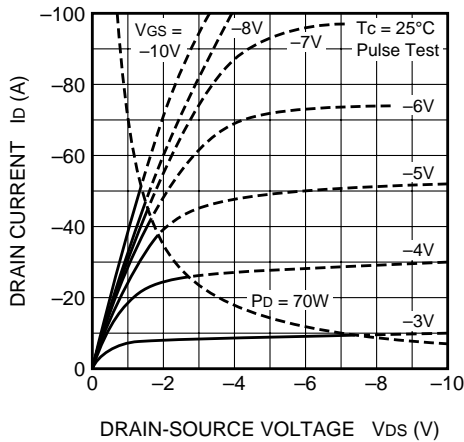
**POWER DISSIPATION DERATING CURVE**



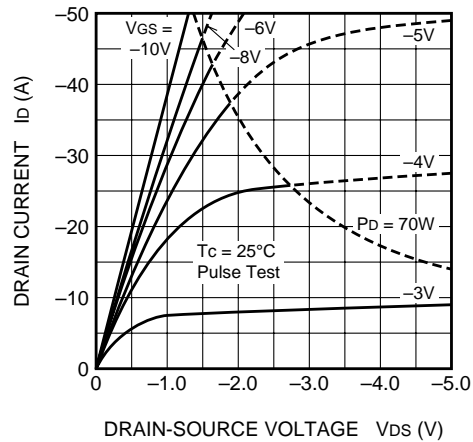
**MAXIMUM SAFE OPERATING AREA**



**OUTPUT CHARACTERISTICS (TYPICAL)**

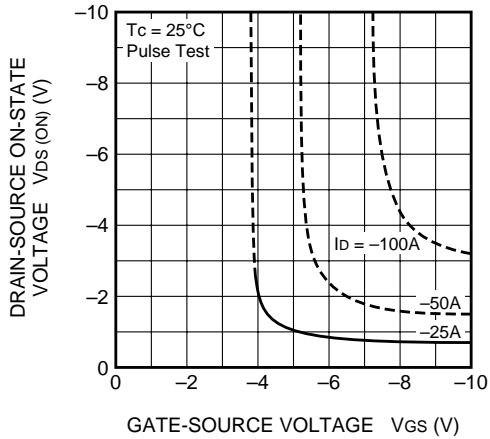


**OUTPUT CHARACTERISTICS (TYPICAL)**

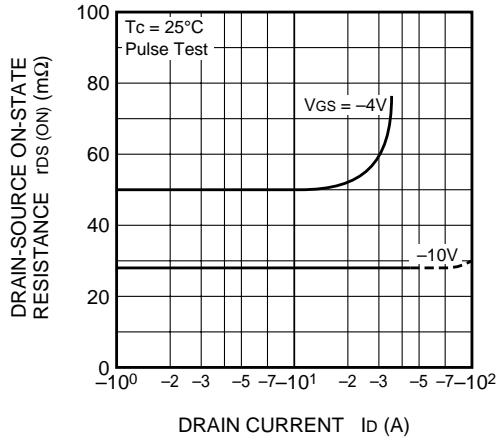


**PRELIMINARY**  
 Notice: This is not a final specification.  
 Some parametric limits are subject to change.

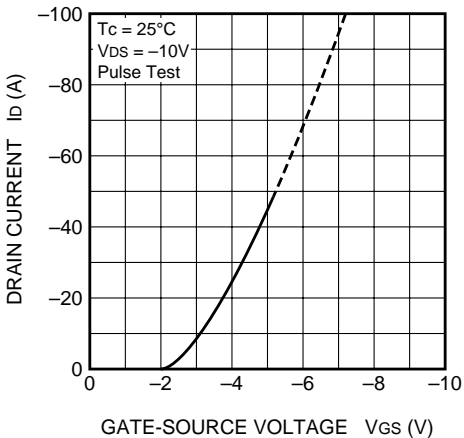
**ON-STATE VOLTAGE VS. GATE-SOURCE VOLTAGE (TYPICAL)**



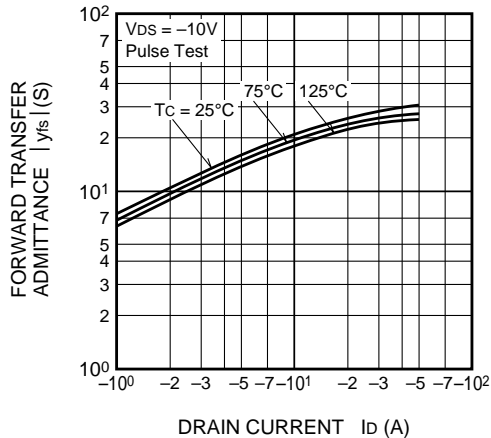
**ON-STATE RESISTANCE VS. DRAIN CURRENT (TYPICAL)**



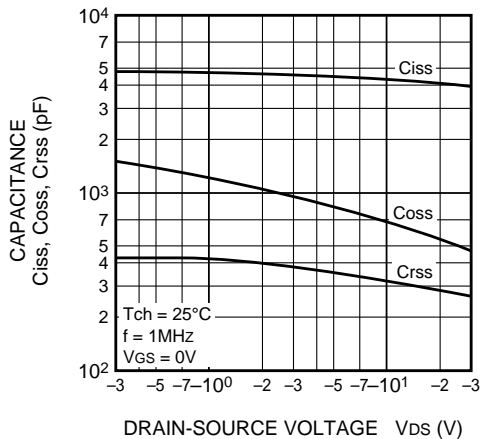
**TRANSFER CHARACTERISTICS (TYPICAL)**



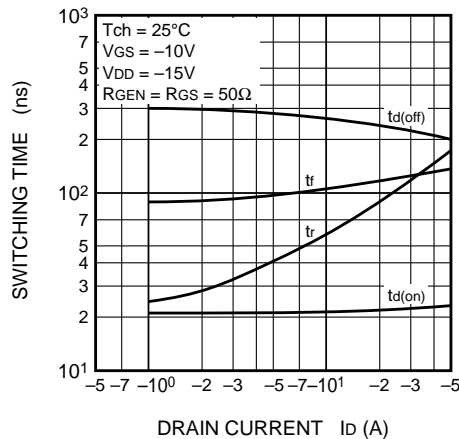
**FORWARD TRANSFER ADMITTANCE VS. DRAIN CURRENT (TYPICAL)**



**CAPACITANCE VS. DRAIN-SOURCE VOLTAGE (TYPICAL)**

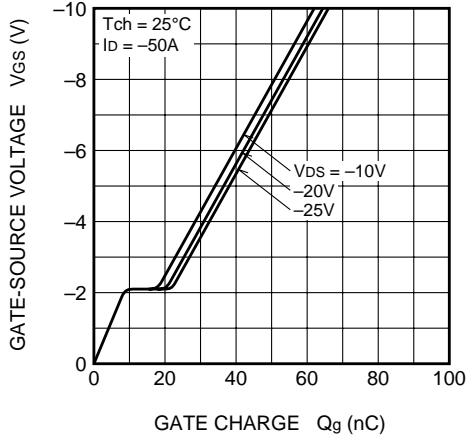


**SWITCHING CHARACTERISTICS (TYPICAL)**

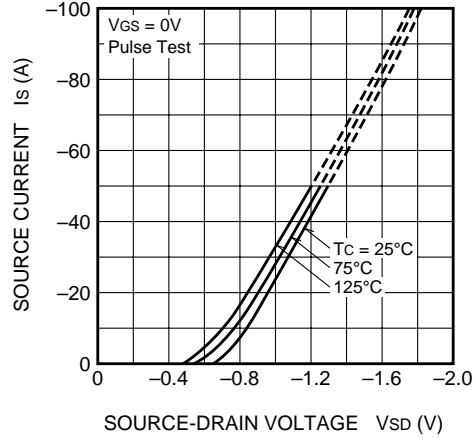


**PRELIMINARY**  
 Notice: This is not a final specification.  
 Some parametric limits are subject to change.

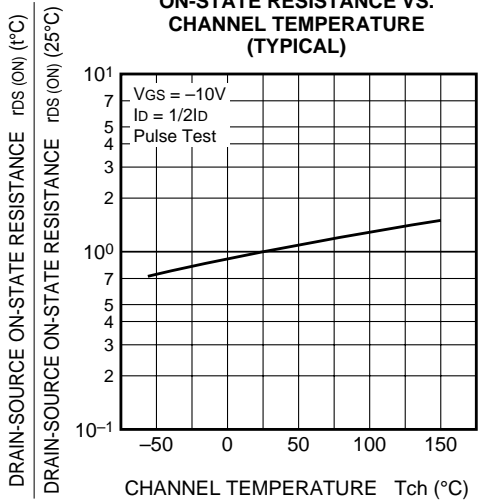
**GATE-SOURCE VOLTAGE VS. GATE CHARGE (TYPICAL)**



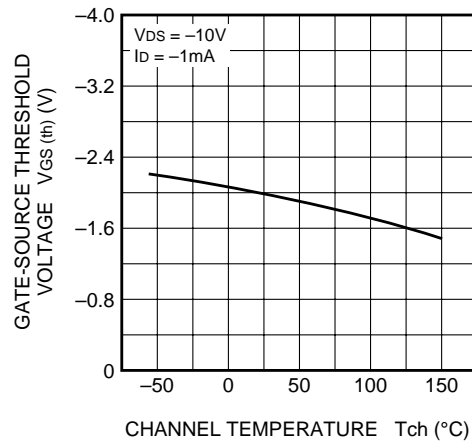
**SOURCE-DRAIN DIODE FORWARD CHARACTERISTICS (TYPICAL)**



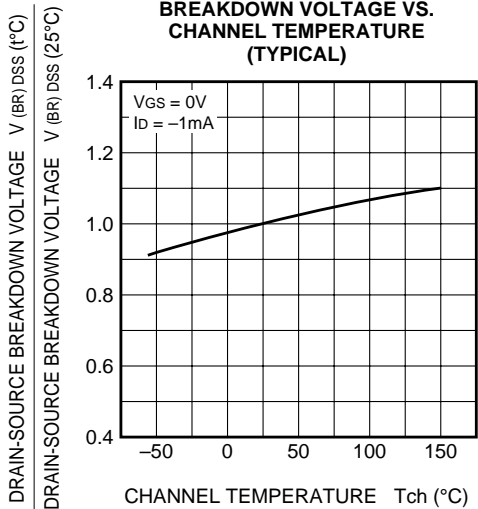
**ON-STATE RESISTANCE VS. CHANNEL TEMPERATURE (TYPICAL)**



**THRESHOLD VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)**



**BREAKDOWN VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)**



**TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS**

