

## Wideband, Fast Settling, Unity Gain Stable, Video Operational Amplifier

July 1994

### Features

- This Circuit is Processed in Accordance to MIL-STD-883 and is Fully Conformant Under the Provisions of Paragraph 1.2.1
- Low AC Variability Over Process and Temperature
- Low Supply Current. . . . . 11mA (Max)
- Unity Gain Bandwidth . . . . . 50MHz (Typ)
- High Slew Rate. . . . . 290V/μs (Typ)
- Low Offset Voltage. . . . . 1mV (Typ)
- Full Power Bandwidth . . . . . 4.6MHz (Typ)
- Low Differential Gain/Phase . . . . . 0.03%/0.03° (Typ)

### Applications

- Pulse and Video Amplifiers
- Wideband Amplifiers
- Fast Sample and Hold Circuits
- Fast, Precise D/A Converters
- High Speed A/D Input Buffer

### Description

The HA-2841/883 is a wideband, unity gain stable, operational amplifier featuring a 50MHz unity gain bandwidth, and excellent DC specifications. This amplifier's performance is further enhanced through stable operation down to closed loop gains of +1, the inclusion of offset null controls, and by its excellent video performance.

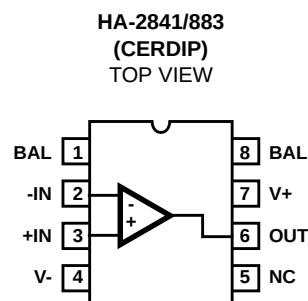
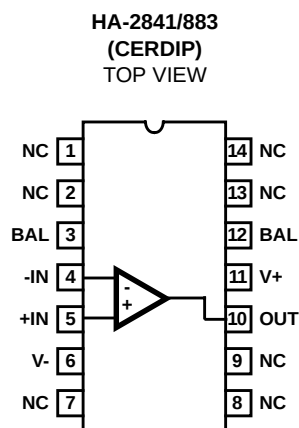
The capabilities of the HA-2841/883 are ideally suited for high speed pulse and video amplifier circuits, where high slew rates and wide bandwidth are required. Gain flatness of 0.05dB, combined with differential gain and phase specifications of 0.03%, and 0.03 degrees, respectively, make the HA-2841/883 ideal for component and composite video applications.

A zener/nichrome based reference circuit, coupled with advanced laser trimming techniques, yields a supply current with a low temperature coefficient and low lot-to-lot variability. Tighter I<sub>CC</sub> control translates to more consistent AC parameters ensuring that units from each lot perform the same way, and easing the task of designing systems for wide temperature ranges.

### Ordering Information

| PART NUMBER  | TEMPERATURE RANGE | PACKAGE        |
|--------------|-------------------|----------------|
| HA1-2841/883 | -55°C to +125°C   | 14 Lead CerDIP |
| HA7-2841/883 | -55°C to +125°C   | 8 Lead CerDIP  |

### Pinouts



NOTE: (NC) No Connection pins may be tied to a ground plane for better isolation and heat dissipation.

## Specifications HA-2841/883

### Absolute Maximum Ratings

Voltage between V+ and V- Terminals. . . . . +35V  
Differential Input Voltage . . . . . 6V  
Voltage at Either Input Terminal . . . . . V+ to V-  
Peak Output Current ( $\leq 10\%$  Duty Cycle) . . . . . 50mA  
Junction Temperature ( $T_J$ ) . . . . . +175°C  
Storage Temperature Range . . . . . -65°C to +150°C  
ESD Rating . . . . . <2000V  
Lead Temperature (Soldering 10s) . . . . . +300°C

### Thermal Information

Thermal Resistance  $\theta_{JA}$   $\theta_{JC}$   
14 Pin CerDIP Package. . . . . 73°C/W 18°C/W  
8 Pin CerDIP Package. . . . . 110°C/W 27°C/W  
Package Power Dissipation Limit at +75°C for  $T_J \leq +175^\circ\text{C}$   
14 Pin CerDIP Package. . . . . 1.05W  
8 Pin CerDIP Package. . . . . 0.9W  
Package Power Dissipation Derating Factor Above +75°C  
14 Pin CerDIP Package. . . . . 10.5mW/°C  
8 Pin CerDIP Package. . . . . 9mW/°C

*CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.*

### Operating Conditions

Operating Temperature Range. . . . . -55°C to +125°C  $V_{INCM} \leq 1/2(V+ - V-)$   
Operating Supply Voltage. . . . .  $\pm 12\text{V}$  to  $\pm 15\text{V}$   $R_L \geq 1\text{k}\Omega$

**TABLE 1. DC ELECTRICAL PERFORMANCE CHARACTERISTICS**

Device Tested at:  $V_{SUPPLY} = \pm 15\text{V}$ ,  $R_{SOURCE} = 100\Omega$ ,  $R_{LOAD} = 100\text{k}\Omega$ ,  $V_{OUT} = 0\text{V}$ , Unless Otherwise Specified.

| PARAMETERS                  | SYMBOL     | CONDITIONS                                                                                         | GROUP A<br>SUBGROUP | TEMPERATURE   | LIMITS |     | UNITS         |
|-----------------------------|------------|----------------------------------------------------------------------------------------------------|---------------------|---------------|--------|-----|---------------|
|                             |            |                                                                                                    |                     |               | MIN    | MAX |               |
| Input Offset Voltage        | $V_{IO}$   | $V_{CM} = 0\text{V}$                                                                               | 1                   | +25°C         | -4     | 4   | mV            |
|                             |            |                                                                                                    | 2, 3                | +125°C, -55°C | -8     | 8   | mV            |
| Input Bias Current          | $+I_B$     | $V_{CM} = 0\text{V}$ , $+R_S = 1.1\text{k}\Omega$<br>$-R_S = 100\Omega$                            | 1                   | +25°C         | -10    | 10  | $\mu\text{A}$ |
|                             |            |                                                                                                    | 2, 3                | +125°C, -55°C | -20    | 20  | $\mu\text{A}$ |
|                             | $-I_B$     | $V_{CM} = 0\text{V}$ , $+R_S = 100\Omega$<br>$-R_S = 1.1\text{k}\Omega$                            | 1                   | +25°C         | -10    | 10  | $\mu\text{A}$ |
|                             |            |                                                                                                    | 2, 3                | +125°C, -55°C | -20    | 20  | $\mu\text{A}$ |
| Input Offset Current        | $I_{IO}$   | $V_{CM} = 0\text{V}$ , $+R_S = 1.1\text{k}\Omega$<br>$-R_S = 1.1\text{k}\Omega$                    | 1                   | +25°C         | -1     | 1   | $\mu\text{A}$ |
|                             |            |                                                                                                    | 2, 3                | +125°C, -55°C | -2     | 2   | $\mu\text{A}$ |
| Common Mode Range           | +CMR       | $V+ = 5\text{V}$<br>$V- = -25\text{V}$                                                             | 1                   | +25°C         | 10     | -   | V             |
|                             |            |                                                                                                    | 2, 3                | +125°C, -55°C | 10     | -   | V             |
|                             | -CMR       | $V+ = 25\text{V}$<br>$V- = -5\text{V}$                                                             | 1                   | +25°C         | -      | -10 | V             |
|                             |            |                                                                                                    | 2, 3                | +125°C, -55°C | -      | -10 | V             |
| Large Signal Voltage Gain   | $+A_{VOL}$ | $V_{OUT} = 0\text{V}$ and +10V<br>$R_L = 1\text{k}\Omega$                                          | 4                   | +25°C         | 10     | -   | kV/V          |
|                             |            |                                                                                                    | 5, 6                | +125°C, -55°C | 5      | -   | kV/V          |
|                             | $-A_{VOL}$ | $V_{OUT} = 0\text{V}$ and -10V<br>$R_L = 1\text{k}\Omega$                                          | 4                   | +25°C         | 10     | -   | kV/V          |
|                             |            |                                                                                                    | 5, 6                | +125°C, -55°C | 5      | -   | kV/V          |
| Common Mode Rejection Ratio | +CMRR      | $\Delta V_{CM} = 10\text{V}$ ,<br>$V_{OUT} = -10\text{V}$<br>$V+ = 5\text{V}$ , $V- = -25\text{V}$ | 1                   | +25°C         | 86     | -   | dB            |
|                             |            |                                                                                                    | 2, 3                | +125°C, -55°C | 80     | -   | dB            |
|                             | -CMRR      | $\Delta V_{CM} = -10\text{V}$ ,<br>$V_{OUT} = 10\text{V}$<br>$V+ = 25\text{V}$ , $V- = -5\text{V}$ | 1                   | +25°C         | 86     | -   | dB            |
|                             |            |                                                                                                    | 2, 3                | +125°C, -55°C | 80     | -   | dB            |

## Specifications HA-2841/883

**TABLE 1. DC ELECTRICAL PERFORMANCE CHARACTERISTICS (Continued)**

Device Tested at:  $V_{SUPPLY} = \pm 15V$ ,  $R_{SOURCE} = 100\Omega$ ,  $R_{LOAD} = 100k\Omega$ ,  $V_{OUT} = 0V$ , Unless Otherwise Specified.

| PARAMETERS                        | SYMBOL       | CONDITIONS                                                                            | GROUP A<br>SUBGROUP | TEMPERATURE   | LIMITS     |     | UNITS |
|-----------------------------------|--------------|---------------------------------------------------------------------------------------|---------------------|---------------|------------|-----|-------|
|                                   |              |                                                                                       |                     |               | MIN        | MAX |       |
| Output Voltage Swing              | $+V_{OUT}$   | $R_L = 1k\Omega$                                                                      | 1                   | +25°C         | 10         | -   | V     |
|                                   |              |                                                                                       | 2, 3                | +125°C, -55°C | 10         | -   | V     |
|                                   | $-V_{OUT}$   | $R_L = 1k\Omega$                                                                      | 1                   | +25°C         | -          | -10 | V     |
|                                   |              |                                                                                       | 2, 3                | +125°C, -55°C | -          | -10 | V     |
| Output Current                    | $+I_{OUT}$   | $V_{OUT} = -5V$ , (Note 1)                                                            | 1                   | +25°C         | 25         | -   | mA    |
|                                   |              |                                                                                       | 2, 3                | +125°C, -55°C | 15         | -   | mA    |
|                                   | $-I_{OUT}$   | $V_{OUT} = 5V$ , (Note 1)                                                             | 1                   | +25°C         | -          | -25 | mA    |
|                                   |              |                                                                                       | 2, 3                | +125°C, -55°C | -          | -15 | mA    |
| Quiescent Power<br>Supply Current | $+I_{CC}$    | $V_{OUT} = 0V$<br>$I_{OUT} = 0mA$                                                     | 1                   | +25°C         | -          | 11  | mA    |
|                                   |              |                                                                                       | 2, 3                | +125°C, -55°C | -          | 11  | mA    |
|                                   | $-I_{CC}$    | $V_{OUT} = 0V$<br>$I_{OUT} = 0mA$                                                     | 1                   | +25°C         | -11        | -   | mA    |
|                                   |              |                                                                                       | 2, 3                | +125°C, -55°C | -11        | -   | mA    |
| Power Supply<br>Rejection Ratio   | +PSRR        | $\Delta V_{SUPPLY} = 10V$<br>$V_+ = 10V$ , $V_- = -15V$<br>$V_+ = 20V$ , $V_- = -15V$ | 1                   | +25°C         | 70         | -   | dB    |
|                                   |              |                                                                                       | 2, 3                | +125°C, -55°C | 70         | -   | dB    |
|                                   | -PSRR        | $\Delta V_{SUPPLY} = 10V$<br>$V_+ = 15V$ , $V_- = -10V$<br>$V_+ = 15V$ , $V_- = -20V$ | 1                   | +25°C         | 70         | -   | dB    |
|                                   |              |                                                                                       | 2, 3                | +125°C, -55°C | 70         | -   | dB    |
| Offset Voltage<br>Adjustment      | $+V_{IOAdj}$ | (Note 2)                                                                              | 1                   | +25°C         | $V_{IO}-1$ | -   | mV    |
|                                   | $-V_{IOAdj}$ | (Note 2)                                                                              | 1                   | +25°C         | $V_{IO}+1$ | -   | mV    |

**NOTES:**

1. The output metal is sized to handle  $I_{OUT} = 10mA$  at a 50% duty cycle, for  $T_J = +175^\circ C$ . For  $I_{OUT} = 15mA$  and  $T_J = +175^\circ C$ , a duty cycle  $\leq 33\%$  is required.
2. Offset Adjustment range is  $|V_{IO} \text{ (measured)} \pm 1mV|$  minimum referred to output. This test is for functionality only, to assure adjustment through 0V.

**TABLE 2. AC ELECTRICAL PERFORMANCE CHARACTERISTICS**

Table 2 Intentionally Left Blank. See A.C. Specifications in Table 3

## Specifications HA-2841/883

**TABLE 3. ELECTRICAL PERFORMANCE CHARACTERISTICS**

Device Characterized at:  $V_{SUPPLY} = \pm 15V$ ,  $R_{SOURCE} = 50\Omega$ ,  $R_{LOAD} = 1k\Omega$ ,  $V_{OUT} = 0V$ ,  $A_V = +1V/V$ , Unless Otherwise Specified.

| PARAMETERS                    | SYMBOL    | CONDITIONS                                   | NOTES | TEMPERATURE     | LIMITS |     | UNITS      |
|-------------------------------|-----------|----------------------------------------------|-------|-----------------|--------|-----|------------|
|                               |           |                                              |       |                 | MIN    | MAX |            |
| Gain Bandwidth Product        | GBWP      | $V_O = 200mV$ ,<br>$f_O = 0.1MHz$            | 1     | +25°C           | 42     | -   | MHz        |
|                               |           | $V_O = 200mV$ ,<br>$f_O = 10MHz$             | 1     | +25°C           | 44     | -   | MHz        |
| Slew Rate                     | SR        | $V_O = -3V$ to +3V                           | 1, 3  | +25°C           | 200    | -   | V/ $\mu s$ |
|                               | SR        | $V_O = -3V$ to +3V                           | 1, 3  | -55°C to +125°C | 187    | -   | V/ $\mu s$ |
| Rise Time                     | $T_R$     | $V_O = 0V$ to +200mV<br>$C_L < 10pF$         | 1, 3  | +25°C           | -      | 6   | ns         |
|                               |           |                                              | 1, 3  | -55°C to +125°C | -      | 6   | ns         |
| Fall Time                     | $T_F$     | $V_O = 0V$ to -200mV<br>$C_L < 10pF$         | 1, 3  | +25°C           | -      | 5   | ns         |
|                               |           |                                              | 1, 3  | -55°C to +125°C | -      | 6   | ns         |
| Full Power Bandwidth          | FPBW      | $V_{PEAK} = +10V$                            | 1, 2  | +25°C           | 3.1    | -   | MHz        |
|                               |           |                                              | 1, 2  | -55°C to +125°C | 3.0    | -   | MHz        |
| Overshoot                     | +OS       | $V_O = 0V$ to +200mV                         | 1     | +25°C           | -      | 60  | %          |
|                               |           |                                              | 1     | -55°C to +125°C | -      | 65  | %          |
|                               | -OS       | $V_O = 0V$ to -200mV                         | 1     | +25°C           | -      | 60  | %          |
|                               |           |                                              | 1     | -55°C to +125°C | -      | 70  | %          |
| Closed Loop Output Resistance | $R_{OUT}$ | $A_V > +1$<br>$V_M = 10V$ , $\Delta I = 9mA$ | 1     | +25°C           | -      | 1   | $\Omega$   |

**NOTES:**

- Parameters listed in Table 3 are controlled via design or process parameters and are not directly tested at final production. These parameters are lab characterized upon initial design release, or upon design changes. These parameters are guaranteed by characterization based upon data from multiple production runs which reflect lot to lot and within lot variations.
- Full Power Bandwidth guarantee based on Slew Rate measurement using  $FPBW = \text{Slew Rate} / (2\pi V_{PEAK})$ .
- Measured between 10% and 90% points.

**TABLE 4. ELECTRICAL TEST REQUIREMENTS**

| MIL-STD-883 TEST REQUIREMENTS               | SUBGROUPS (SEE TABLE 1)   |
|---------------------------------------------|---------------------------|
| Interim Electrical Parameters (Pre Burn-In) | 1                         |
| Final Electrical Test Parameters            | 1 (Note 1), 2, 3, 4, 5, 6 |
| Group A Test Requirements                   | 1, 2, 3, 4, 5, 6          |
| Groups C & D Endpoints                      | 1                         |

**NOTE:**

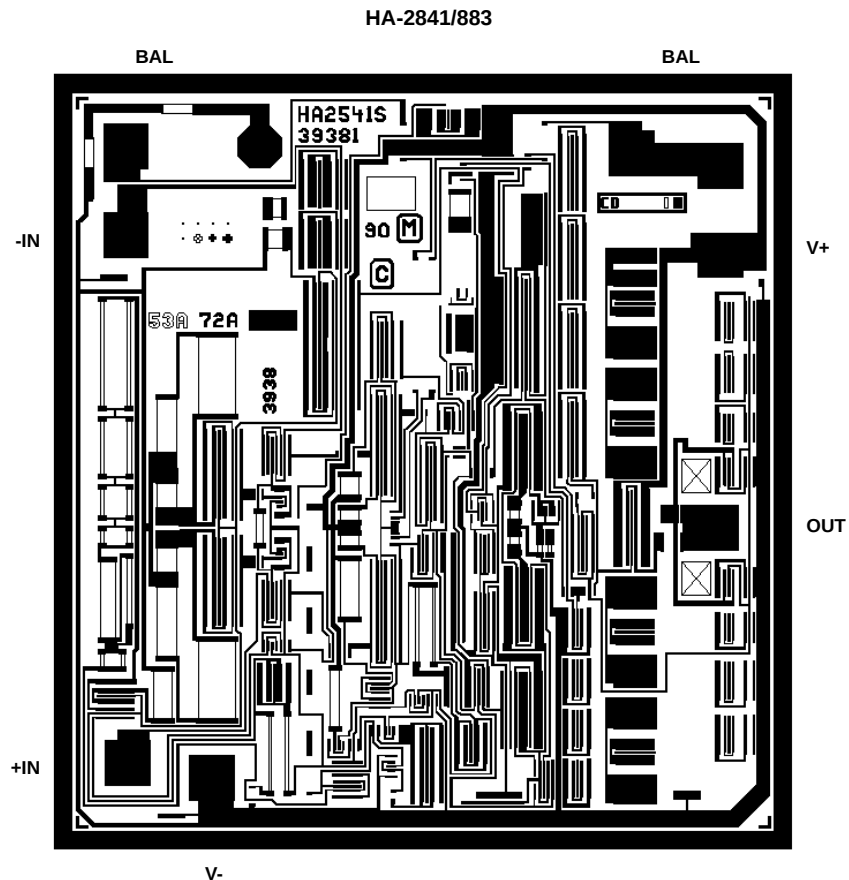
- PDA applies to Subgroup 1 only.

**Die Characteristics****DIE DIMENSIONS:**77 x 81 x 19 mils  $\pm$  1 mils1960 x 2060 x 483 $\mu$ m  $\pm$  25.4 $\mu$ m**METALLIZATION:**

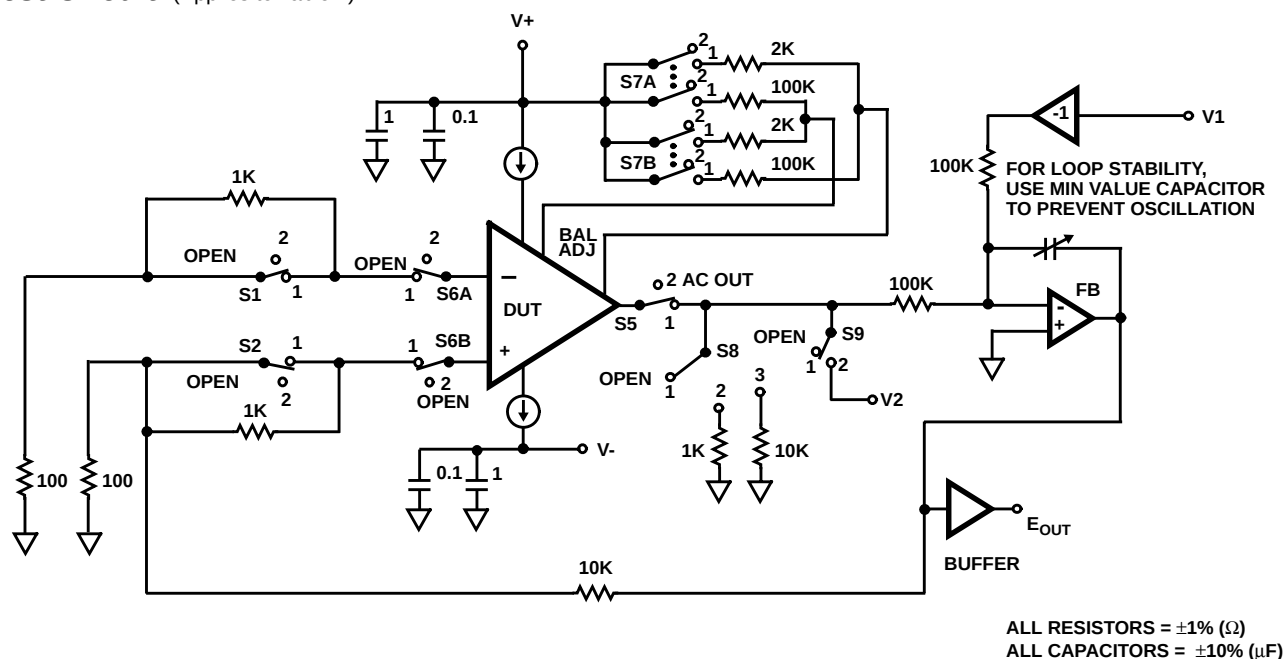
Type: Al, 1% Cu

Thickness: 16k $\text{\AA}$   $\pm$  2k $\text{\AA}$ **GLASSIVATION:**

Type: Nitride over Silox

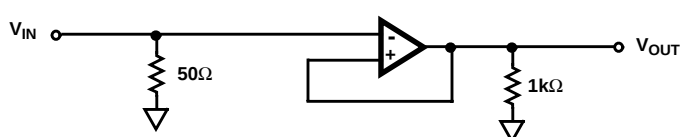
Silox Thickness: 12k $\text{\AA}$   $\pm$  2k $\text{\AA}$ Nitride thickness: 3.5k $\text{\AA}$   $\pm$  1k $\text{\AA}$ **WORST CASE CURRENT DENSITY:**1.2 x 10<sup>5</sup> A/cm<sup>2</sup> at 9.7mA**SUBSTRATE POTENTIAL (Powered Up): V-****TRANSISTOR COUNT: 43****PROCESS: Bipolar Dielectric Isolation****Metallization Mask Layout**

**Test Circuit** (Applies to Table 1)



## Test Waveforms

**SIMPLIFIED TEST CIRCUIT FOR LARGE AND SMALL SIGNAL RESPONSE** (Applies to Table 3)

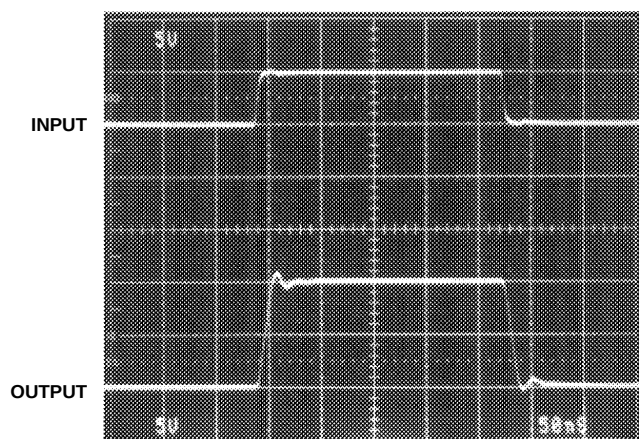

$$V_S = \pm 15V$$
$$A_V = +1$$
$$C_l \leq 10\text{pF}$$

NOTE: No Capacitive Load,  
Maintain Parasitics  
Less Than 10pF.

### MEASURED LARGE SIGNAL RESPONSE

Vertical Scale: Input = 5V/Div., Output = 5V/Div.

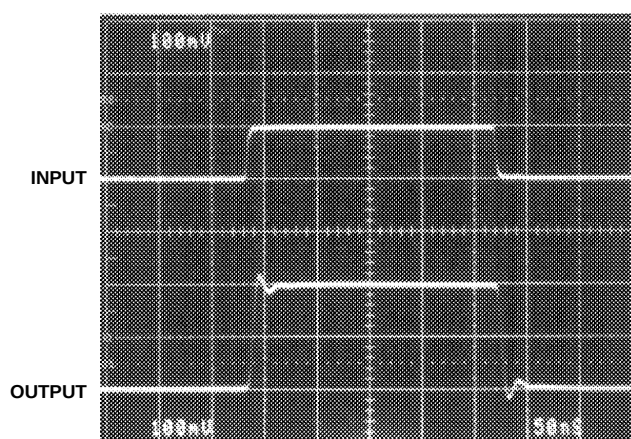
Horizontal Scale: 50ns/Div.



## MEASURED SMALL SIGNAL RESPONSE

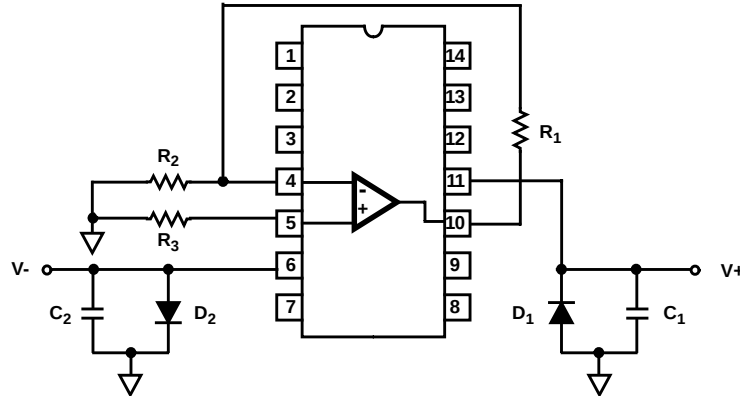
Vertical Scale: Input = 100mV/Div., Output = 100mV/Div.

Horizontal Scale: 50ns/Div.

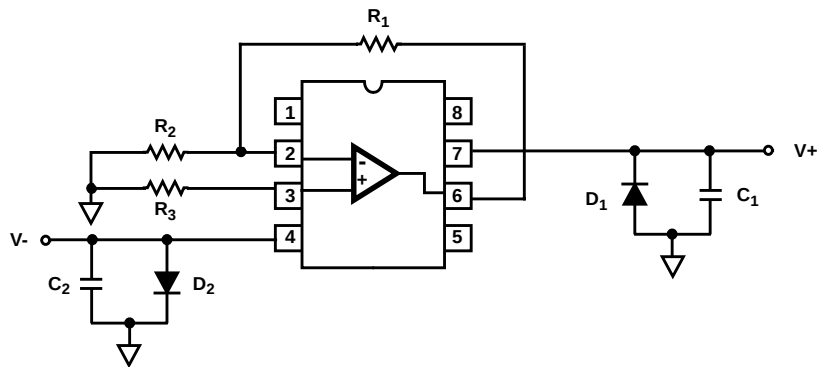


## Burn-In Circuits

HA1-2841/883 CERAMIC DIP



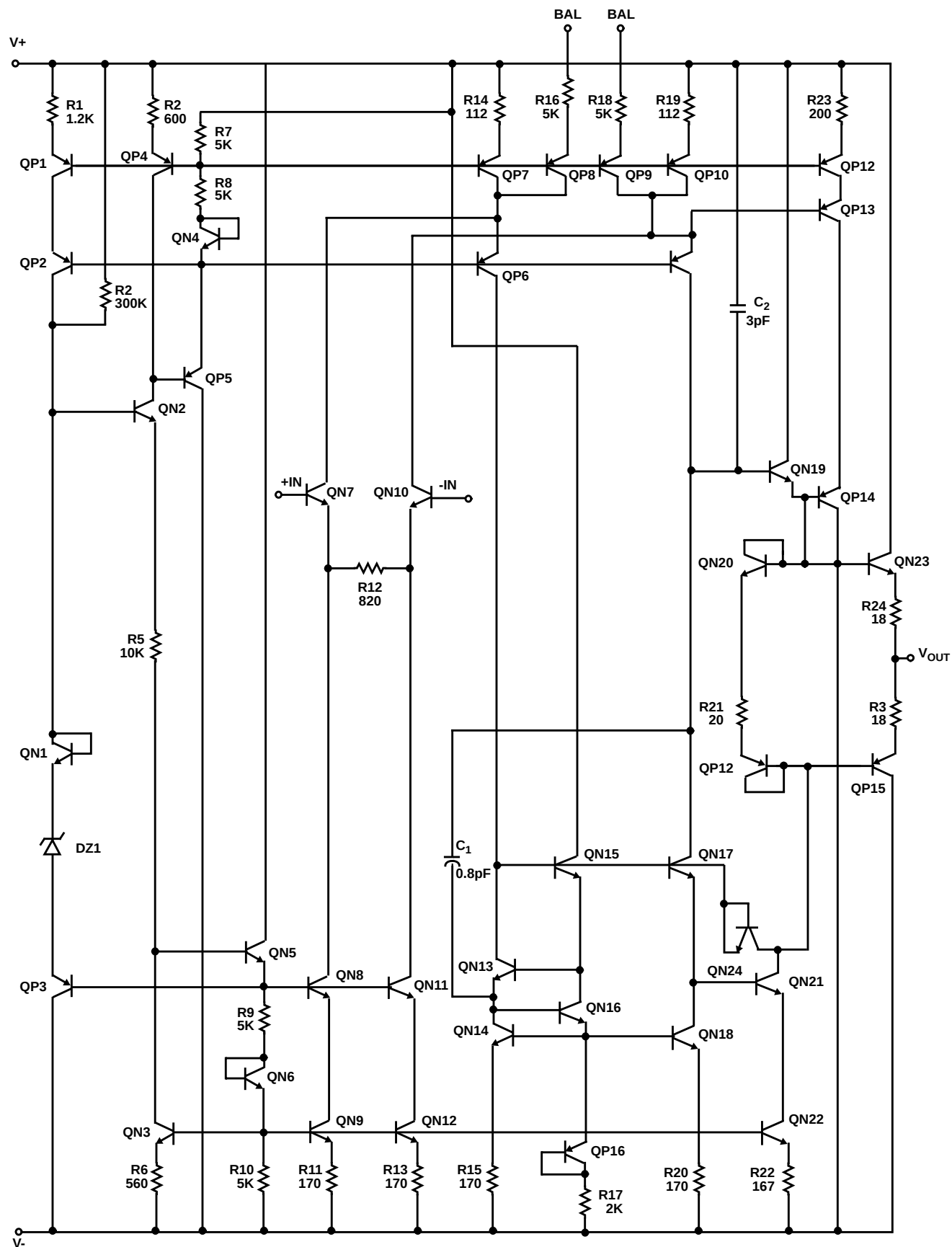
HA7-2841/883 CERAMIC DIP



### NOTES:

1.  $R_1 = 1M\Omega, \pm 5\%, 1/4W$  (Min)
2.  $R_2 = 100k\Omega, \pm 5\%, 1/4W$  (Min),  $= R_3$
3.  $C_1 = C_2 = 0.01\mu F/\text{Socket}$  (Min) or  $0.1\mu F/\text{Row}$ , (Min)
4.  $D_1 = D_2 = 1N4002$  or Equivalent/Board
5.  $|V^+ - V^-| = 31V \pm 1V$

## Schematic Diagram

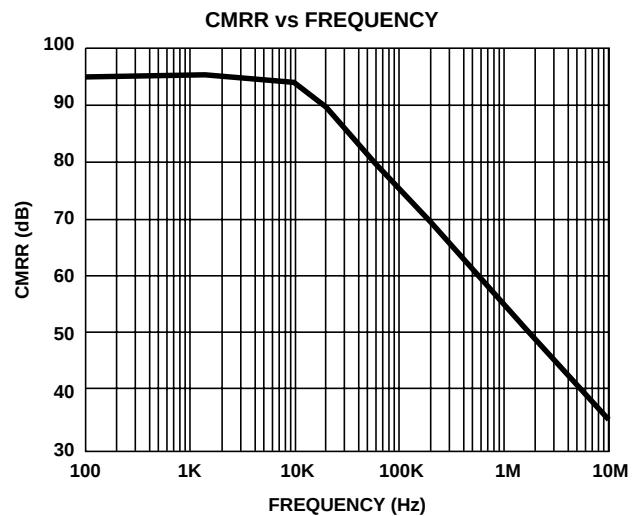
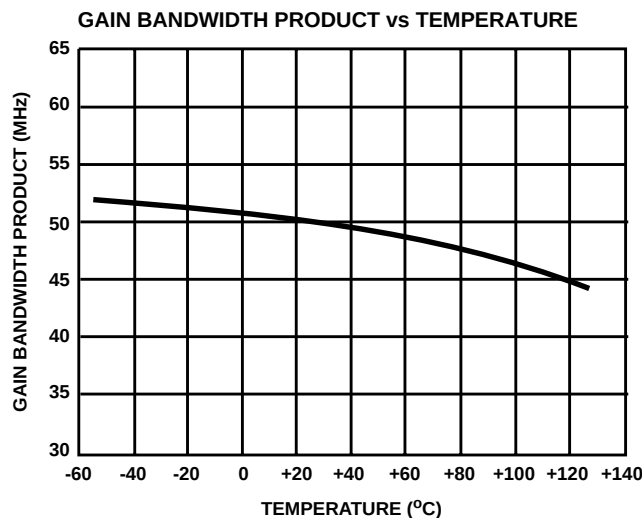
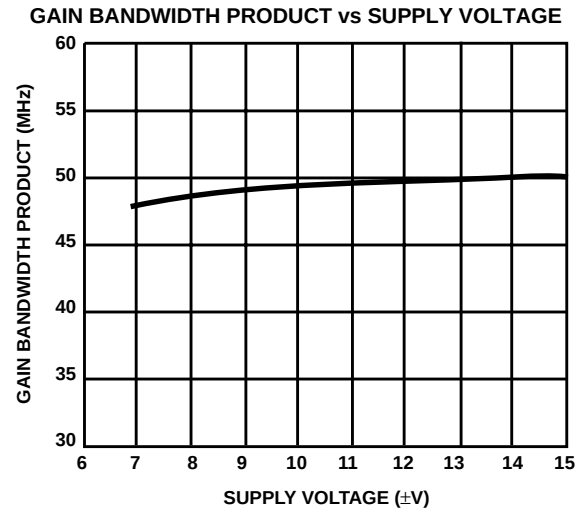
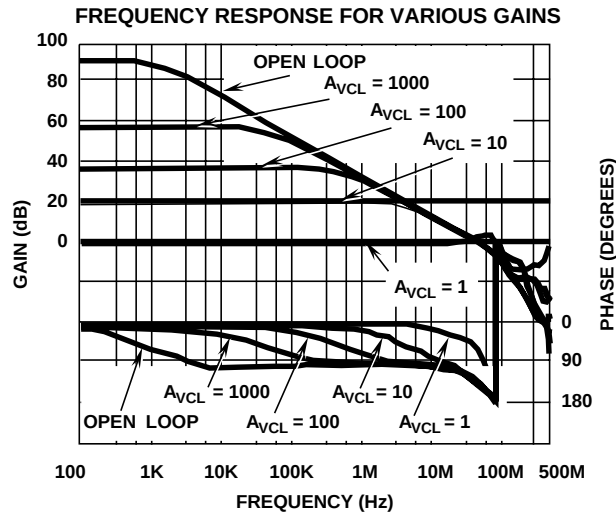


## DESIGN INFORMATION

## Wideband, Fast Settling, Unity Gain Stable, Video Operational Amplifier

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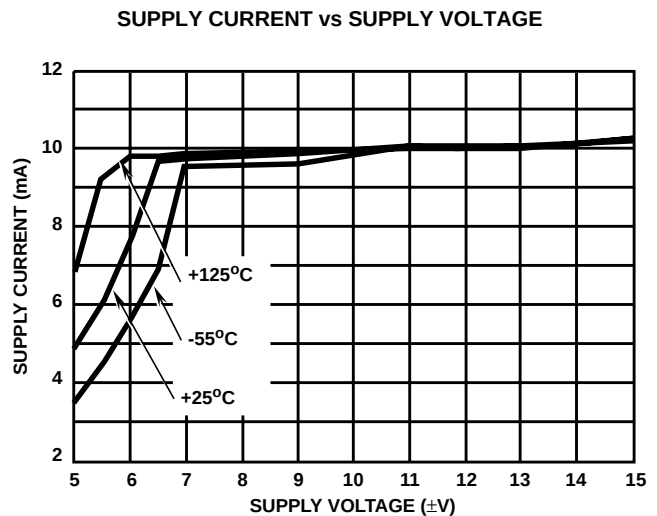
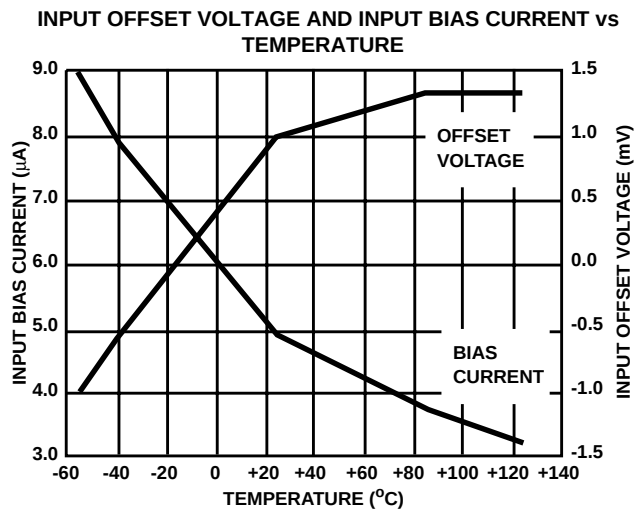
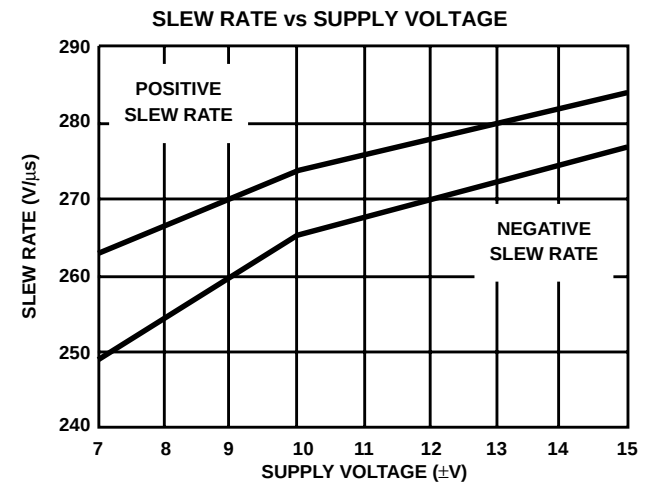
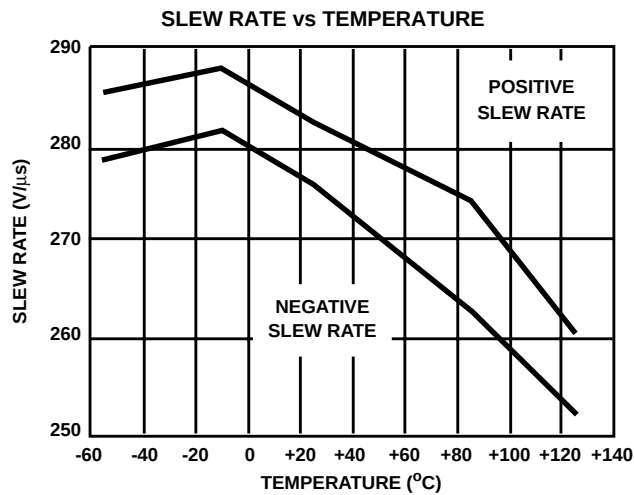
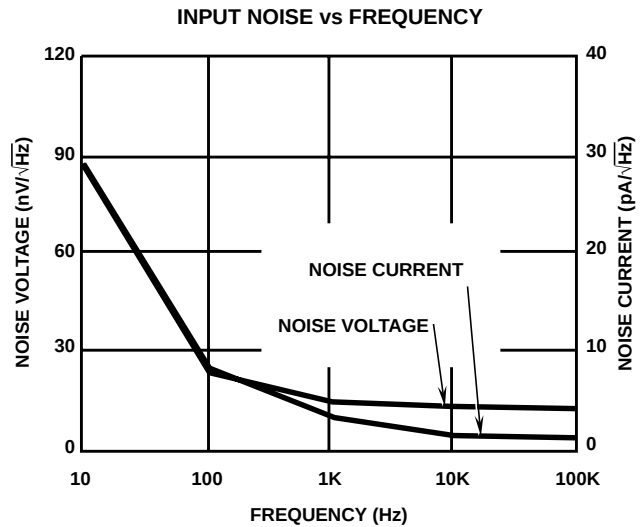
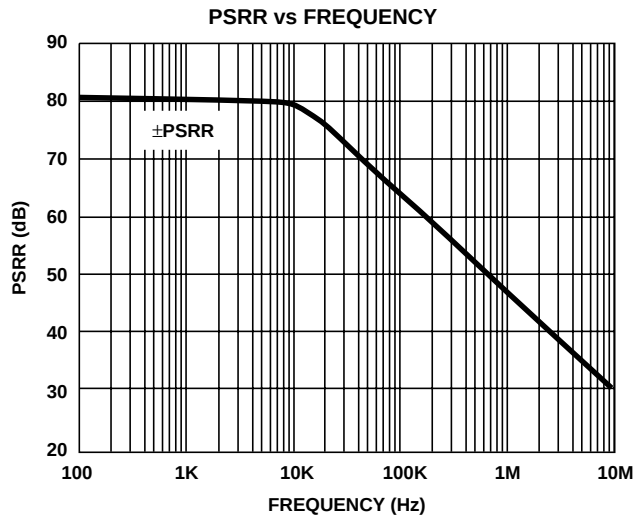
**Typical Performance Curves**  $V_{\text{SUPPLY}} = \pm 15\text{V}$ ,  $A_V = +1$ ,  $R_L = 1\text{k}\Omega$ ,  $C_L \leq 10\text{pF}$ ,  $T_A = +25^\circ\text{C}$ , Unless Otherwise Specified.



## DESIGN INFORMATION (Continued)

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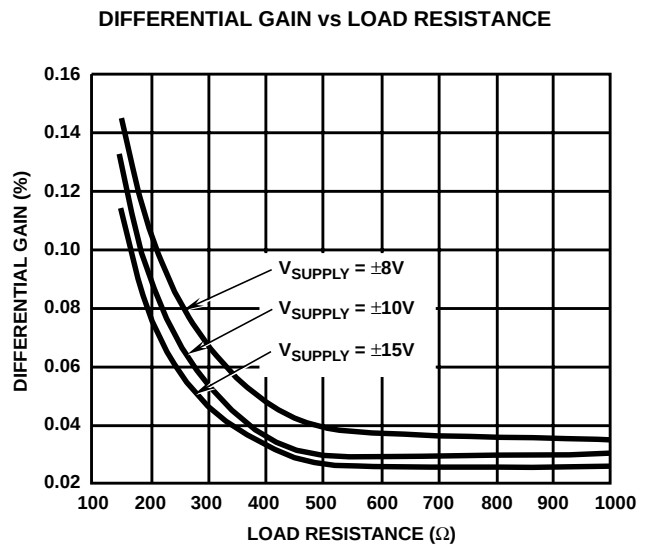
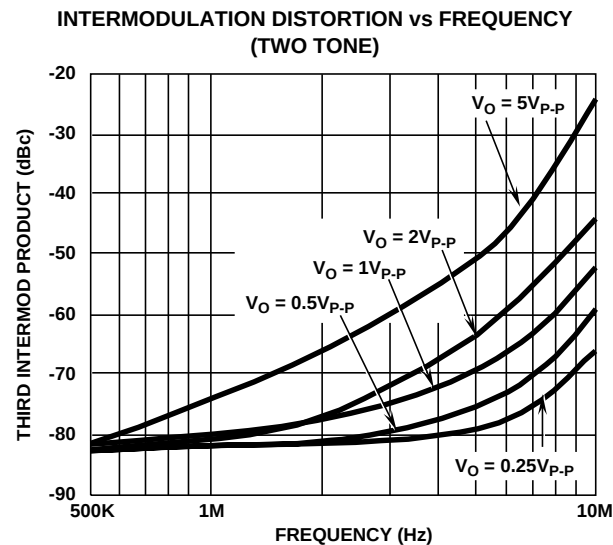
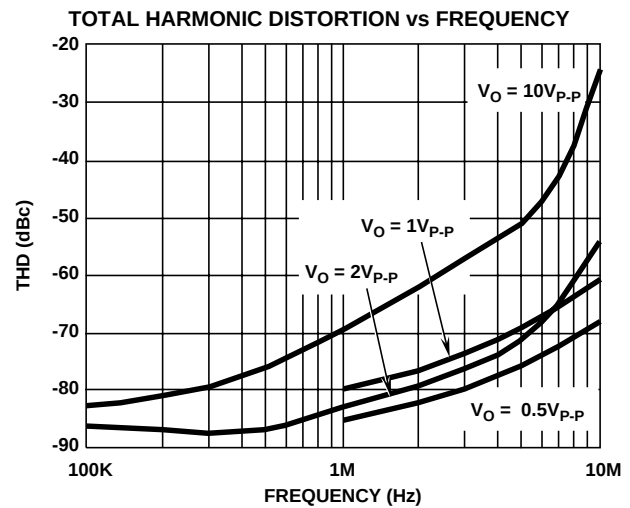
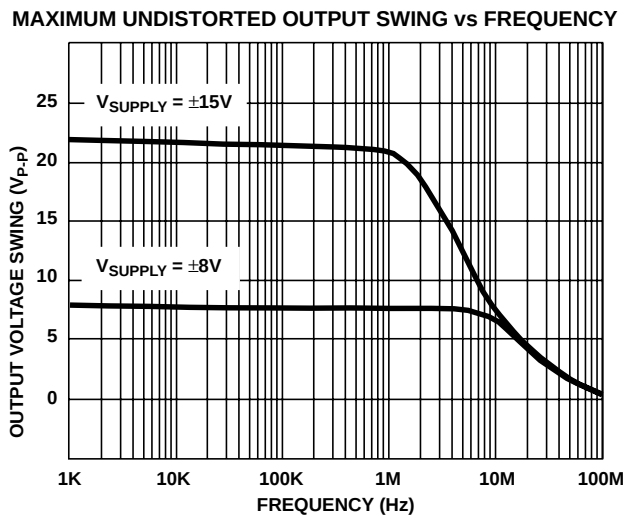
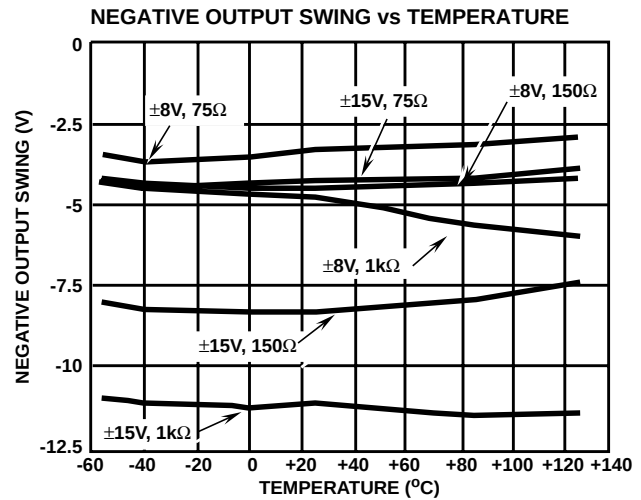
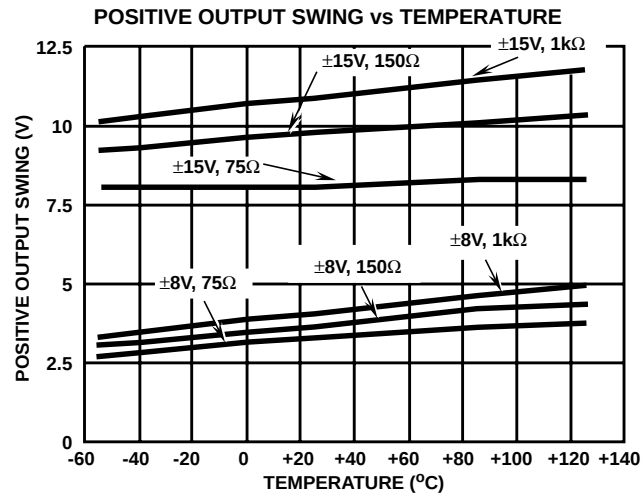
**Typical Performance Curves**  $V_{\text{SUPPLY}} = \pm 15\text{V}$ ,  $A_V = +1$ ,  $R_L = 1\text{k}\Omega$ ,  $C_L \leq 10\text{pF}$ ,  $T_A = +25^\circ\text{C}$ , Unless Otherwise Specified.



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**Typical Performance Curves**  $V_{\text{SUPPLY}} = \pm 15\text{V}$ ,  $A_v = +1$ ,  $R_L = 1\text{k}\Omega$ ,  $C_L \leq 10\text{pF}$ ,  $T_A = +25^\circ\text{C}$ , Unless Otherwise Specified.

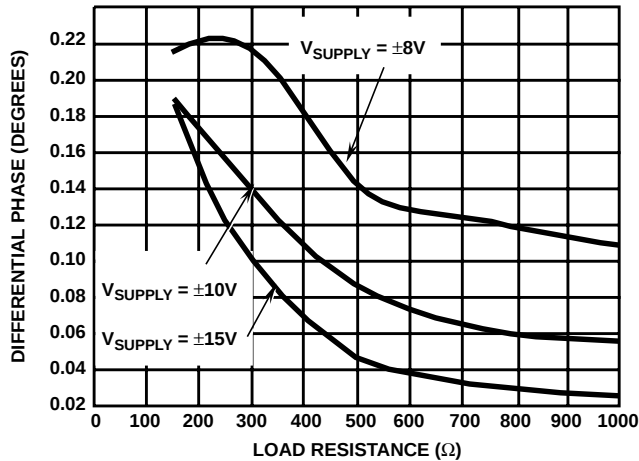


## DESIGN INFORMATION (Continued)

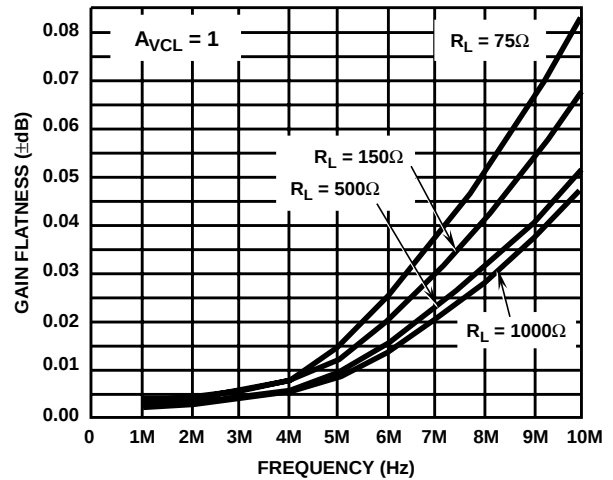
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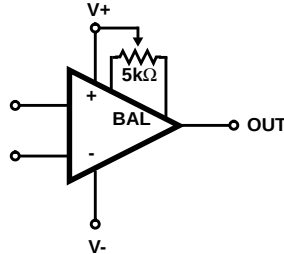
DIFFERENTIAL PHASE vs LOAD RESISTANCE



GAIN FLATNESS vs FREQUENCY



### Suggested Offset Voltage Adjustment



## TYPICAL PERFORMANCE CHARACTERISTICS

Device Characterized at:  $V_{\text{SUPPLY}} = \pm 15\text{V}$ ,  $R_L = 1\text{k}\Omega$ ,  $C_L \leq 10\text{pF}$ , Unless Otherwise Specified.

| PARAMETERS                    | CONDITIONS                                              | TEMPERATURE         | TYPICAL     | UNITS                        |
|-------------------------------|---------------------------------------------------------|---------------------|-------------|------------------------------|
| Input Offset Voltage          | $V_{\text{CM}} = 0\text{V}$                             | $+25^\circ\text{C}$ | 1           | mV                           |
| Average Offset Voltage Drift  | Versus Temperature                                      | Full                | 14          | $\mu\text{V}/^\circ\text{C}$ |
| Input Bias Current            | $V_{\text{CM}} = 0\text{V}$                             | $+25^\circ\text{C}$ | 5.0         | $\mu\text{A}$                |
|                               |                                                         | Full                | 8.0         | $\mu\text{A}$                |
| Input Offset Current          | $V_{\text{CM}} = 0\text{V}$                             | $+25^\circ\text{C}$ | 0.5         | $\mu\text{A}$                |
| Differential Input Resistance |                                                         | $+25^\circ\text{C}$ | 170         | $\text{k}\Omega$             |
| Input Noise Voltage           | $f_O = 10\text{Hz}$ to $1\text{MHz}$                    | $+25^\circ\text{C}$ | 16          | $\mu\text{V}_{\text{RMS}}$   |
| Input Noise Voltage Density   | $f_O = 1000\text{Hz}$                                   | $+25^\circ\text{C}$ | 16          | $\text{nV}/\sqrt{\text{Hz}}$ |
| Input Noise Current Density   | $f_O = 1000\text{Hz}$                                   | $+25^\circ\text{C}$ | 2           | $\text{pA}/\sqrt{\text{Hz}}$ |
| Large Signal Voltage Gain     | $V_{\text{OUT}} = \pm 10\text{V}$                       | $+25^\circ\text{C}$ | 50          | $\text{kV/V}$                |
|                               |                                                         | Full                | 30          | $\text{kV/V}$                |
| CMRR                          | $V_{\text{CM}} = \pm 2\text{V}$                         | Full                | 95          | dB                           |
| Gain Bandwidth Product        | $f = 10\text{MHz}$                                      | $+25^\circ\text{C}$ | 50          | MHz                          |
| Output Voltage Swing          |                                                         | Full                | $\pm 10.5$  | V                            |
| Output Current                | $V_{\text{OUT}} > 10\text{V}$                           | Full                | 30          | mA                           |
| Output Resistance             | Open Loop                                               | $+25^\circ\text{C}$ | 8.5         | $\Omega$                     |
| Full Power Bandwidth          | $\text{FPBW} = \text{SR}/2\pi V_P$ , $V_P = 10\text{V}$ | $+25^\circ\text{C}$ | 4.6         | MHz                          |
| Slew Rate                     | $V_{\text{OUT}} = \pm 5\text{V}$ , $A_V = +2$           | $+25^\circ\text{C}$ | 290         | $\text{V}/\mu\text{s}$       |
| Rise Time                     | $V_{\text{OUT}} = \pm 100\text{mV}$ , $A_V = +2$        | $+25^\circ\text{C}$ | 3.5         | ns                           |
| Overshoot                     | $V_{\text{OUT}} = \pm 100\text{mV}$ , $A_V = +2$        | $+25^\circ\text{C}$ | 47          | %                            |
| Settling Time                 | 0.1%, 10V Step, $A_V = +2$                              | $+25^\circ\text{C}$ | 145         | ns                           |
| PSRR                          | Delta $V_S = \pm 10\text{V}$ to $\pm 20\text{V}$        | Full                | 80          | dB                           |
| Supply Current                | No Load                                                 | Full                | 10          | mA                           |
| Differential Gain             | $R_L = 700\Omega$                                       | $+25^\circ\text{C}$ | 0.03        | %                            |
| Differential Phase            | $R_L = 700\Omega$                                       | $+25^\circ\text{C}$ | 0.03        | Degrees                      |
| Gain Flatness to 5MHz         | $R_L = 75\Omega$                                        | $+25^\circ\text{C}$ | $\pm 0.015$ | dB                           |
| Gain Flatness to 10MHz        | $R_L = 75\Omega$                                        | $+25^\circ\text{C}$ | $\pm 0.05$  | dB                           |

All Intersil semiconductor products are manufactured, assembled and tested under **ISO9000** quality systems certification.

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