HMC351S8

GaAs MMIC HIGH IP3 DOUBLE-BALANCED MIXER, 0.7 - 1.2 GHz

Typical Applications

The HMC351S8 is ideal for:

- Cellular Basestations
- Cable Modems
- Fixed Wireless Access Systems

Features

Conversion Loss: 9.0 dB

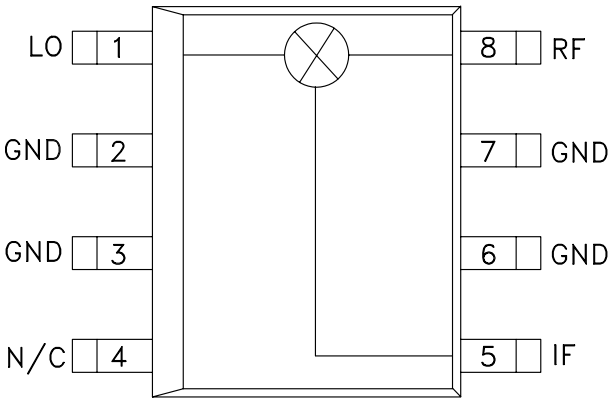
LO/IF Isolation: 35 dB

LO/RF Isolation: 42 dB

Input IP3: +25 dBm

Input IP2: +48 dBm

Functional Diagram



General Description

The HMC351S8 is a double balanced mixer in an 8 lead plastic surface mount package. The passive GaAs schottky diode mixer implements planar on chip baluns and requires no external components. The mixer can be used as an upconverter, down converter, or modulator. The mixer provides 9 dB conversion loss and +25 dBm IIP3 with LO drive levels of +19 dBm. The design was optimized for low cost high volume applications where high converter linearity is required. The high LO suppression of 42 dB yields excellent carrier suppression for modulator applications.

Electrical Specifications, T_A = +25° C

Parameter	LO = +19 dBm, IF = 100 MHz			Units
	Min.	Typ.	Max.	
Frequency Range, RF & LO		0.7 - 1.2		GHz
Frequency Range, IF		DC - 0.3		GHz
Conversion Loss		9	11.5	dB
Noise Figure (SSB)		9	11.5	dB
LO to RF Isolation	36	42		dB
LO to IF Isolation	31	35		dB
RF to IF Isolation	9	13		dB
IP3 (Input)	22	25		dBm
IP2 (Input)	40	48		dBm
1 dB Compression (Input)	12	16		dBm

**Unless otherwise noted, all measurements performed as downconverter, IF= 100 MHz.*

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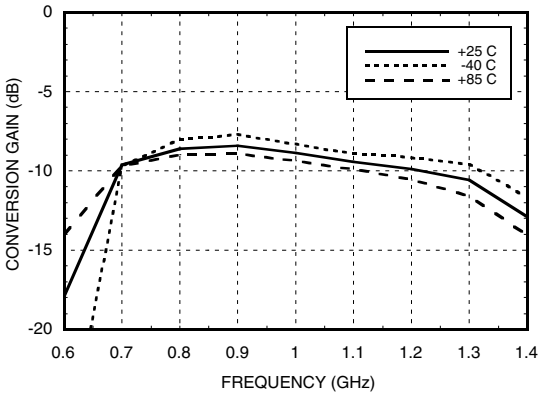
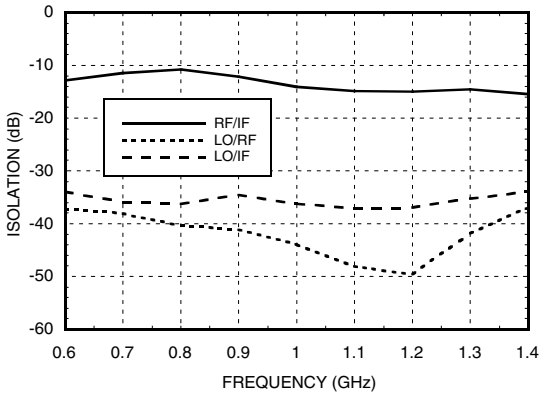
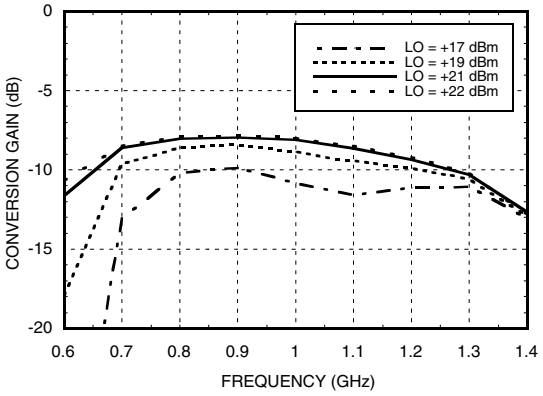
Conversion Gain vs. Temperature @ LO = +19 dBm



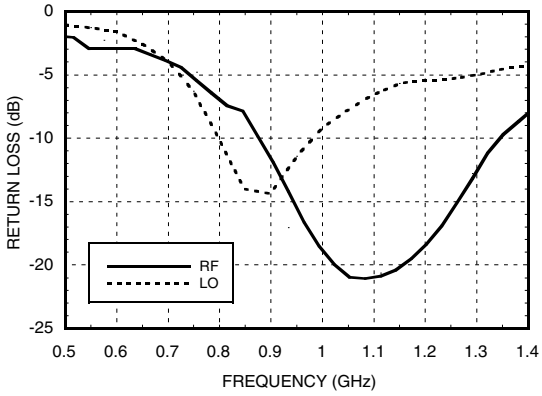
Isolation @ LO = +19 dBm



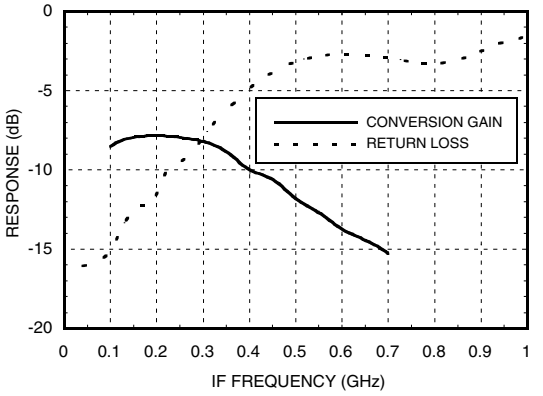
Conversion Gain vs. LO Drive



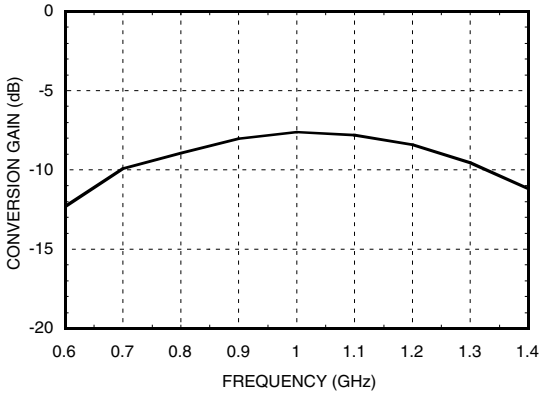
Return Loss @ LO = +19 dBm



IF Bandwidth @ LO = +19 dBm



Upconverter Performance, Conversion Gain @ LO = +19 dBm



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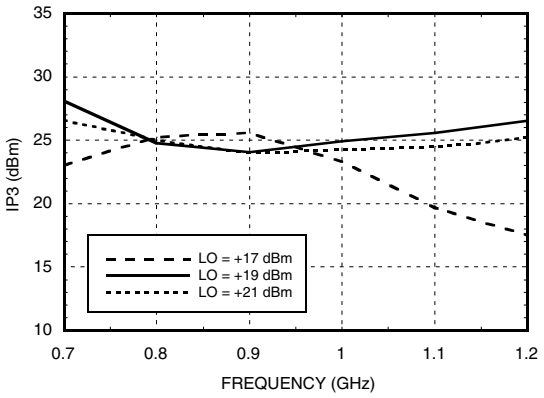
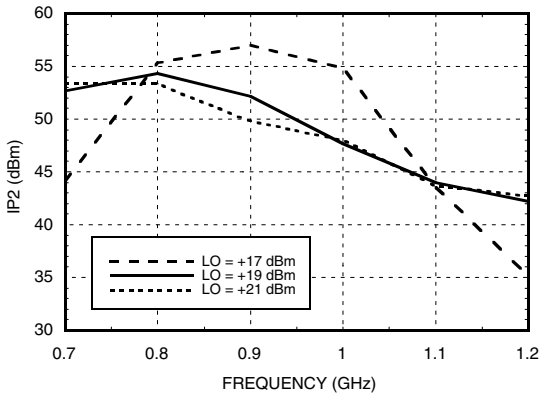
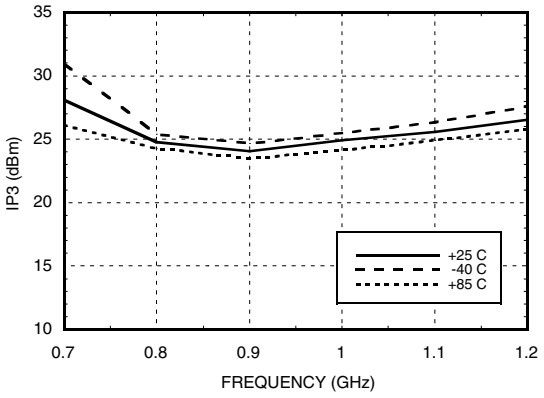
Input IP3 vs. LO Drive *



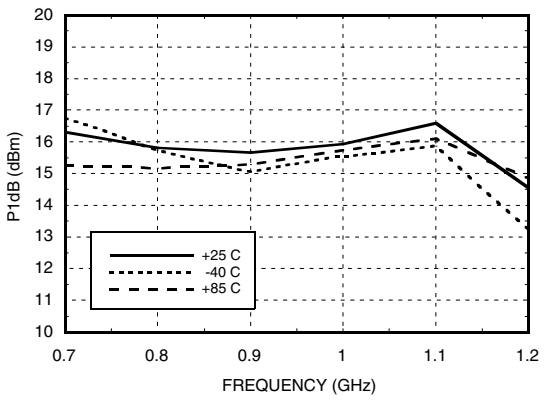
Input IP2 vs. LO Drive *



Input IP3 vs. Temperature * @ LO Drive = +19 dBm



P1dB vs. Temperature @ LO = +19 dBm



MxN Spurious Outputs

	nLO				
mRF	0	1	2	3	4
0	xx	-2	21	19	40
1	4	0	19	39	53
2	69	68	84	76	84
3	83	93	93	86	89
4	>96	>96	>96	>96	87

RF = 1.0 GHz @ -10 dBm
LO = 0.9 GHz @ +19 dBm
All values in dBc relative to the IF output power level.

Harmonics of LO

	nLO Spur at RF Port			
LO Frequency (GHz)	1	2	3	4
0.6	37	42	65	78
0.75	39	50	63	83
0.9	40	51	59	69
1.05	45	59	55	70
1.2	49	70	53	79
1.35	37	72	63	73

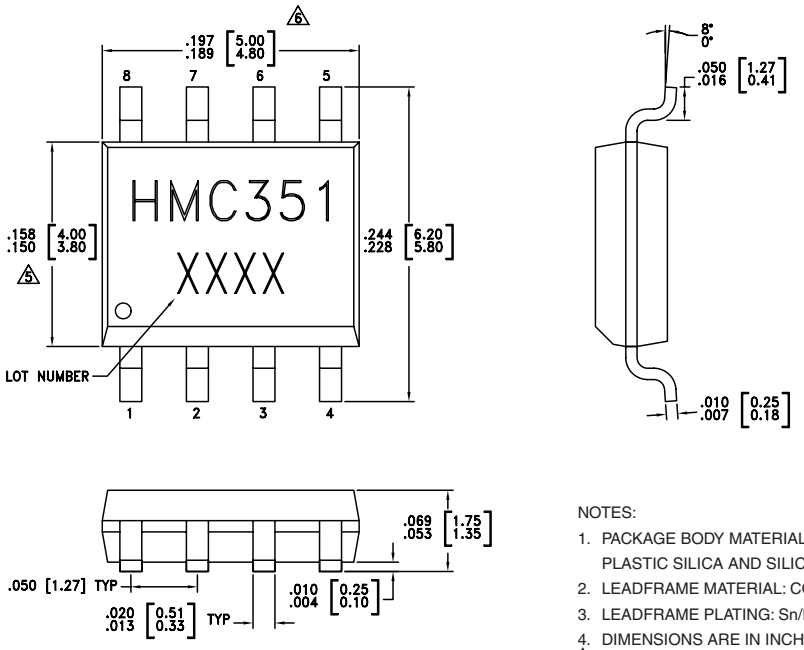
LO = +19 dBm
Values in dBc below input LO level measured at the RF port.

** Two-tone input power = 0 dBm each tone, 1 MHz spacing.*

Absolute Maximum Ratings

RF / IF Input	+27 dBm
LO Drive	+27 dBm
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
IF DC Current	±26 mA

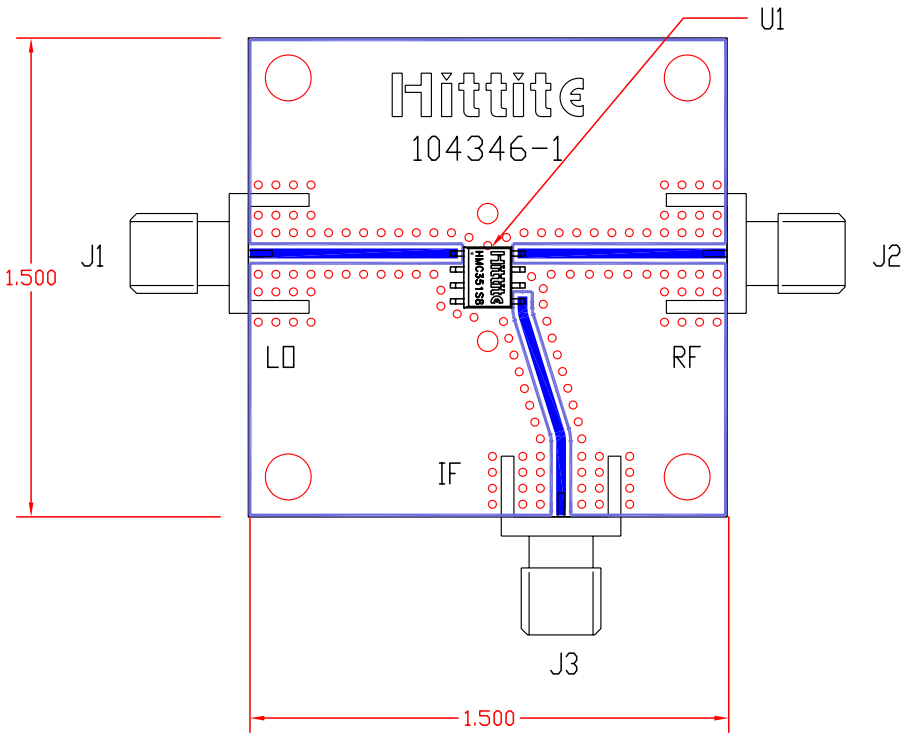
Outline Drawing



NOTES:

1. PACKAGE BODY MATERIAL: LOW STRESS INJECTION MOLDED PLASTIC SILICA AND SILICON IMPREGNATED.
2. LEADFRAME MATERIAL: COPPER ALLOY
3. LEADFRAME PLATING: Sn/Pb SOLDER
4. DIMENSIONS ARE IN INCHES [MILLIMETERS].
5. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
6. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.
7. ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND.

Evaluation PCB



List of Material

Item	Description
J1 - J3	PC Mount SMA RF Connector
U1	HMC351S8 Mixer
PCB*	104346 Eval Board
* Circuit Board Material: Rogers 4350	

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board as shown is available from Hittite upon request.

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Notes: