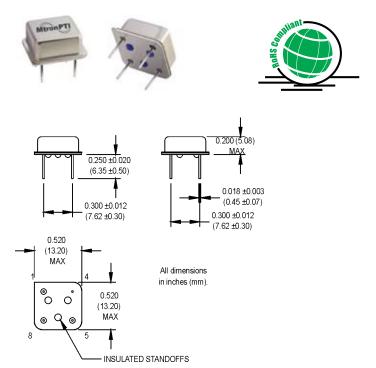
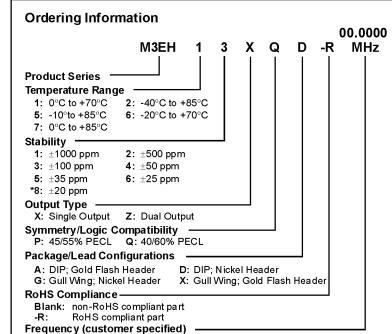
8 pin DIP, 3.3 Volt, ECL/PECL, Clock Oscillator







^{*}Contact factory for availability.

Pin Connections

PIN	FUNCTION(S) (Model Dependent)				
1	N/C, Output #2				
4	-Vee, Ground				
5	Output #1				
8	+Vcc				

Electrical Specifications	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition
	Frequency Range	F	1.5		155.52	MHz	
	Frequency Stability	△F/F (See Ordering Information)				See Note 1	
	Operating Temperature	TA	(See Ordering Information)				
	Storage Temperature	Ts	-55		+125	°C	
	Input Voltage	Vcc	3.15	3.3	3.45	٧	
	Input Current	lee/lcc			100	mA	
	Symmetry (Duty Cycle)	(See Ordering Information)				Vcc -1.3 V level	
	Load		50 Ω to Vcc -2V or Thevenin Equiv			/alent	See Note 2
	Rise/Fall Time	Tr/Tf			2.5	ns	See Note 3
	Logic "1" Level	Voh	Vcc -1.02			٧	
	Logic "0" Level	Vol			Vcc -1.63	٧	
	Cycle to Cycle Jitter			13	25	ps RMS	1 Sigma
nvironmental	Mechanical Shock	Per MIL-STD-202, Method 213, Condition C					
	Vibration	Per MIL-STD-202, Method 201 & 204					
	Wave Solder Conditions	260°C for 10 s max.					
	Hermeticity	Per MIL-STD-202, Method 112 (1 x 10° atm.cc/s of helium)					
ᆸ	Solderability	Per EIAJ-STD-002					

^{1.} Calibration, deviation over temperature, shock, vibration, and aging.

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.

^{2.} Internally terminated outputs. See load circuit diagram #5.

^{3.} Rise/Fall times are measured between Vcc -1.02 V and Vcc -1.63 V.