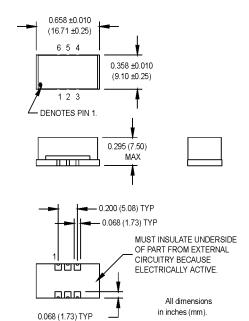
M6005 Series 9x14 mm FR-4, 3.3 Volt, HCMOS/TTL, TCXO

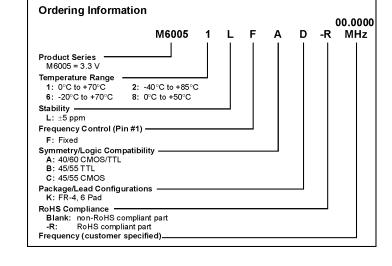






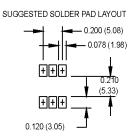
- · Stratum 3 compliant stability and aging
- Ideal for WLL/DWDM/ATM, and SONET/ SDH applications





Pin Connections

| FUNCTION | SMT | | |
|-------------|-----|--|--|
| N/C | 1 | | |
| Tristate | 2 | | |
| Ground/Case | 3 | | |
| Output | 4 | | |
| N/C | 5 | | |
| +Vdd | 6 | | |



| | PARAMETER | Symbol | Min. | Тур. | Max. | Units | Condition/Notes |
|---------------------------|-----------------------|--------|----------------------------------|-------|--------|---------|---------------------|
| Electrical Specifications | Frequency Range | F | 60 | | 170 | MHz | |
| | Operating Temperature | TA | (See Ordering Information) | | | | |
| | Storage Temperature | Ts | -55 | | +105 | °C | |
| | Frequency Stability | ÄF/F | (See Ordering Information) | | | | See Note 1 |
| | Aging | | | | | | |
| | 1st Year | | | | 1.5 | ppm | |
| | Thereafter (per year) | | | | 0.5 | ppm | |
| | Input Voltage | Vdd | 3.15 | 3.3 | 3.45 | V | |
| | Input Current | ldd | | | 50 | mA | |
| | Output Type | | | | | | CMOS/TTL |
| | Load | | 2 TTL or 15 pF max. | | | | |
| | Symmetry (Duty Cycle) | | (See Ordering Information) | | | | |
| | Logic "1" Level | Voh | 2.5 | | | V | |
| | Logic "0" Level | Vol | | | 0.5 | V | |
| | Rise/Fall Time | Tr/Tf | | | 10 | ns | |
| | Tristate Function | | Input Logic "1": output active | | | | |
| | | | Input Logic "0": output disables | | | | |
| | Start up Time | | 10 | | | ms | |
| | Phase Noise (Typical) | 10 Hz | 100 Hz | 1 kHz | 10 kHz | 100 kHz | Offset from carrier |
| | @155.52 MHz | -60 | -90 | -110 | -120 | -120 | dBc/Hz |

1. Stability is inclusive of five year aging at 25°C.

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.