

MAS6240

Piezo Driver with Multi-Mode Charge Pump

This is preliminary information on a new product under development. Micro Analog Systems Oy reserves the right to make any changes without notice.



- Both Single Ended and Differential Output
- Three-Step Volume Adjusting
- Up to 18Vpp Output from 3V Supply
- One Wire Audio & Shutdown Control
- High Efficiency
- Solution without Inductors
- Low External Part Count

DESCRIPTION

MAS6240 is a piezo driver device that can drive outputs up to 18Vpp from 3V supply. An internal three-mode charge pump generates boosted supply voltage for piezo driver. For adjusting the piezo element sound volume, the charge pump can operate in either of a 1x, 2x or 3x mode. In 1x mode the input voltage is bypassed to the output, in 2x or 3x mode the input voltage is boosted up accordingly 2 or 3 times. Charge pump mode is selected by control pins EN1 and EN2 (see Table1 at page 2).

MAS6240 is an easy and low-cost solution for piezo driver, since only 4 small value capacitors are needed in addition to sound element - the use of inductors can be avoided. The inductorless design also causes significantly less disturbance to the surrounding circuits making it an ideal choice for sensitive designs. Its charge pump switches at 1MHz, allowing to use as small as 100nF external capacitors.

FEATURES

Thin 0.75 mm QFN 12 package

Piezo Driver

- Three-Step Volume Adjusting
- Both Single Ended and Differential Output
- Up to 18Vpp Output from 3V Supply
- One Wire Audio & Shutdown Control

Charge Pump

- Low External Part Count
- Solution without Inductors
- 1 MHz Switching Frequency
- Multi-Mode Charge Pump (1x/2x/3x)

Control logic is switching the charge pump on at first rising signal of digital input (DIN) pin. The switch-off signal will be generated while the signal at DIN has been low at least for 10ms. The piezo driver is enabled at a second rising edge of a pulse at DIN and the signal is transferred to piezo output VO1. The same signal is inverted into output VO2 for using differential output.

Continuous logic high level at DIN input causes the charge pump to be turned ON but leaves the audio amplifier disabled. In that state the charge pump output can be used to power the external LEDs or any other external circuit. The output voltage is still selectable at three steps.

In "disabled" mode (DIN has been low for 10ms) all functional blocks are switched off to achieve the quiescent current less than $1\mu A$.

See more information in the chapter Detailed Description.

APPLICATIONS

- Wrist Watches
- Alarm Clocks
- Handheld GPS devices
- PDAs
- •Portable Device with Sound Feature



BLOCK & APPLICATION DIAGRAM



Figure 1: Charge Pump + Single End Piezo Driver (max 9Vpp)



Figure 2: Charge Pump + Differential Piezo Driver (max 18Vpp)

DIN	EN1	EN2	Charge Pump
0	-	-	OFF
1	0	0	OFF
1	0	1	1x Mode (VIN)
1	1	0	2x Mode (2xVIN)
1	1	1	3x Mode (3xVIN)

Note: Pulsed signal at digital input DIN is taken as "1" if pulse low time is less than 10 ms!



PIN DESCRIPTION

Pin Description	Symbol	Туре	X-coordinate	Y-coordinate
Control signal input for setting charge pump mode	EN1	DI	152 μm	989 µm
Control signal input for setting charge pump mode	EN2	DI	152 μm	804 µm
Enable signal + Digital signal input	DIN	DI	152 μm	588 µm
Flying capacitor negative terminal	CN1	I/O	672 μm	200 µm
Supply ground	GND	G	808 µm	202 µm
Digital audio signal output	VO2	DO	980 μm	225 μm
Digital audio signal output	VO1	DO	980 µm	378 μm
Flying capacitor negative terminal	CN2	I/O	980 µm	558 µm
Flying capacitor positive terminal	CP1	I/O	980 µm	711 μm
Charge pump output	VOUT	AO	765 μm	1000 µm
Flying capacitor positive terminal	CP2	I/O	612 μm	1000 µm
Power supply	VIN	Р	447 μm	999 µm

G = Ground, P = Power, D = Digital, A = Analog, I = Input, O = Output.

Note: Because the substrate of the die is internally connected to GND, the die has to be connected to GND or left floating. Please make sure that GND is the first pad to be bonded. Pick-and-place and all component assembly are recommended to be performed in ESD protected area.

Note: Pad coordinates are measured from the left bottom corner of the chip to the center of the pads. The coordinates may vary depending on sawing width and location, however, distances between pads are accurate.

Note: See IC outlines on a page 6 for IC layout.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Conditions	Min	Мах	Unit
Supply Voltage	VIN		-0.3	5.5	V
Outputs and Flying Capacitors Pins Voltages	VOUT, CP1, CP2, CN1, CN2, VO1, VO2		-0.3	10	V
Voltage Range for Input Pins	DIN, EN1, EN2		-0.3	VIN + 0.3	V
VOUT Short-Circuit Duration	t _{sc}	Note 1		Indefinite	
Storage Temperature			-55	+150	°C
ESD Rating		Human Body Model (HBM)		1	kV

NOTE 1. SHORT CIRCUIT CURRENT INTERNALLY LIMITED.

RECOMMENDED OPERATING CONDITIONS

			AI	voltages with	respect to	ground.
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Operating Junction Temperature	TJ		-40		+125	°C
Operating Ambient Temperature	T _A		-40	+27	+85	S
Operating Supply Voltage	V _{IN}		2.5	3.0	3.3	V



ELECTRICAL CHARACTERISTICS

 $T_A = -40 \,^{\circ}\text{C}$ to +85 °C, typical values at $T_A = 27 \,^{\circ}\text{C}$, $V_{IN} = 3.0 \,^{\circ}\text{V}$, $C_1 = 100 \,^{\circ}\text{nF}$, $C_2 = 100 \,^{\circ}\text{nF}$, $C_{OUT} = 100 \,^{\circ}\text{nF}$, $C_{IN} = 100 \,^{\circ}\text{nF}$, $C_{piezo} = 15 \,^{\circ}\text{nF}$, digital values at $T_A = 27 \,^{\circ}\text{C}$, $V_{IN} = 3.0 \,^{\circ}\text{V}$, $C_1 = 100 \,^{\circ}\text{nF}$, $C_{OUT} = 100 \,^{\circ}\text{nF}$, $C_{IN} = 100 \,^{\circ}\text{nF}$, $C_{piezo} = 15 \,^{\circ}\text{nF}$, digital values at $T_A = 27 \,^{\circ}\text{C}$, $V_{IN} = 3.0 \,^{\circ}\text{V}$, $C_1 = 100 \,^{\circ}\text{nF}$, $C_{OUT} = 100 \,^{\circ}\text{nF}$, $C_{IN} = 100 \,^{\circ}\text{nF}$, $C_{piezo} = 15 \,^{\circ}\text{nF}$, digital values at $T_A = 27 \,^{\circ}\text{C}$, $V_{IN} = 3.0 \,^{\circ}\text{V}$, $C_1 = 100 \,^{\circ}\text{nF}$, $C_{OUT} = 100 \,^{\circ}\text{nF}$, $C_{IN} = 100 \,^{\circ}\text{nF}$

Parameter	Symbol	Conditions	Min		Max	Unit
		VOLIT pip voltage towards ground		- 76		•
Oulput Voltage	0001	at VDD = $3 V$				
		1x Mode (no load)			3	v
		2x Mode (no load)			6	·
		3x Mode (no load)			9	
Current Consumption	lcc	Charge Pump (no load):				
	00	1x Mode		50		μA
		2x Mode		490		•
		3x Mode		820		
		Single ended application				
		$(C_{piezo} = 15nF; f=4kHz):$				
		1x Mode		0.27		mA
		2x Mode		1.3		
		3x Mode		2.7		
		Differential application				
		(C _{piezo} = 15 nF; f=4kHz):				
		1x Mode		0.88		mA
		2x Mode		3.5		
	_	3x Mode		7.6		
Signal Frequency	FAUDIO		0.2	4	8	kHz
Shutdown Current	I _{SD}	DIN = 0V,			1	μA
Internal Switching	Fosc		0.6	1	1.8	MHz
Frequency (Charge Pump)						
VOUT Turn-ON Time	t _{ON}	1x Mode		10		μs
(From DIN signal HIGH to		2x Mode		60		
90% VOUT steady state)		3x Mode		120		
Shut Down delay	t _{OFF}	Time before device shutdown after	10	20		ms
		DIN signal goes to LOW				
Short Circuit Current Limit	I _{SC}	From VIN pin	10	30		mA
Control Input Threshold	V _{IH}	EN1, EN2, DIN pins	1.6			V
	V _{IL}				0.55	V
Control Input Current	IIH	$V_{\text{DIN}} = 3V$, (900k Ω pull down)		3.4	7	μA
	I	$V_{\text{DIN}} = 0V$		0	1	μΑ
		$V_{\text{DIN}} = 3V$				
	I _{IH}	$V_{\text{EN1,EN2}} = 3V$, (900k Ω pull down)		3.4	7	μA
	IL	$V_{\text{EN1,EN2}} = 0V$		0	1	μΑ
		$V_{\text{DIN}} = 0V$				
	I _{IH}	$V_{\text{EN1,EN2}} = 3V$		0	1	μA
		$V_{\text{EN1,EN2}} = 0V$		0	1	μA



DETAILED DESCRIPTION



Figure 3: Enabling output VO1

The piezo driver is enabled at the second rising edge of the pulse at DIN, thus the signal is transferred to the piezo output VO1. An output VO2 is enabled at the same time, but it is optional to take it in use. Control logic is switching the charge pump on at first rising signal of digital input DIN pin. If only one continous pulse is fed to the input DIN, the output VO1 is not enabled. This make it possible to control e.g. a white LED or other device through pin VOUT while charge pump is enabled, without enabling the piezo output VO1.



Figure 4: Disabling VO2

Figure 4 is drawn in the case of VO2. The switch-off signal will be generated after the signal at DIN has been low at least for 10 ms. In the figure 4 the switch-off delay is about 25 ms. Again when new pulses are fed into DIN, the charge pump and piezo driver will be enabled.





1130 µm

DIE size = 1.13 x 1.20 mm; PAD size = 80 x 80 µm

DEVICE OUTLINE CONFIGURATION





Top Marking Information: 1234 = Product Number Av = Version Number G = Lead Free, RoHS Compliant Package YWW = Year Week

Pin nr.	Pin Name						
1	EN1	4	CN1	7	VO1	10	VOUT
2	EN2	5	GND	8	CN2	11	CP2
3	DIN	6	VO2	9	CP1	12	VIN



PACKAGE (QFN 3X3x0.75 12Id) OUTLINE



Symbol	Min	Nom	Max	Unit		
PACKAGE DIMENSIONS						
A	0.700	0.750	0.800	mm		
A1	0.000	0.020	0.050	mm		
A3	0.178		0.228	mm		
b	0.180		0.300	mm		
D	2.950	3.000	3.050	mm		
D2 (Exposed.pad)	1.300		1.550	mm		
E	2.950	3.000	3.050	mm		
E2 (Exposed.pad)	1.300		1.550	mm		
е	0.500 BSC			mm		
L	0.300		0.500	mm		

Dimensions do not include mold or interlead flash, protrusions or gate burrs.

2500 Components on Each Reel

Reel Material: Conductive, Plastic Antistatic or Static Dissipative Carrier Tape Material: Conductive Cover Tape Material: Static Dissipative



SOLDERING INFORMATION

◆ For Lead-Free / Green QFN 3mm x 3mm x 0.75mm

Resistance to Soldering Heat	According to RSH test IEC 68-2-58/20
Maximum Temperature	260°C
Maximum Number of Reflow Cycles	3
Reflow profile	Thermal profile parameters stated in IPC/JEDEC J-STD-020
	should not be exceeded. http://www.jedec.org
Lead Finish	7.62 - 25.4 µm, Matte Tin

EMBOSSED TAPE SPECIFICATIONS



Dimension	Min/Max	Unit
Ao	3.30 ±0.10	Mm
Bo	3.30 ±0.10	Mm
Do	1.50 +0.1/-0.0	Mm
E	1.75	Mm
F	5.50 ±0.05	Mm
Ko	1.10 ±0.10	Mm
Po	4.0	Mm
P1	8.0 ±0.10	Mm
P2	2.0 ±0.05	Mm
Т	0.3 ±0.05	Mm
W	12.00 ±0.3	Mm
	All dimensione in millimeters	

All dimensions in millimeters



REEL SPECIFICATIONS





Dimension	Min	Мах	Unit
A		330	mm
В	1.5		mm
С	12.80	13.50	mm
D	20.2		mm
N	100		mm
W ₁ (measured at hub)	12.4	14.4	mm
W ₂ (measured at hub)		18.4	mm
Trailer	160		mm
Leader	390,		mm
	of which minimum 160 mm of empty carrier tape sealed with cover tape		



ORDERING INFORMATION

Product Code	Product	Package	Comments
MAS6240A1HP06	Piezo Driver with Multi-Mode Charge Pump	QFN 3x3x0.75 12ld, Pb Free, RoHS Compliant	Tape and Reel
MAS6240A1TC00	Piezo Driver with Multi-Mode Charge Pump	EWS Tested wafers 400 µm	Die size 1.13 x 1.20 mm

The formation of product code

An example for MAS6240A1HP06:

MAS6240	Α	1	HP	06
Product name	Design version	Product	Package:	Delivery format:
		Version	HP = QFN 3 x 3 x 0.75	06 = T&R
			(Pb free, RoHS compliant)	08 = Loose components

LOCAL DISTRIBUTOR

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