

DATA SHEET

MAX809/MAX810

3-pin microprocessor resets

Product data
Supersedes data of 2002 Oct 21

2003 Aug 08

3-pin microprocessor resets

MAX809/MAX810

DESCRIPTION

The MAX809/MAX810 are single function microprocessor resets used to monitor supply voltages in microprocessor and other logic systems. They provide a reset signal to the microprocessor during power-up, power-down, and brownout conditions. The reset signal is asserted when the supply voltage drops below the preset threshold, and the signal is released a set time after the supply voltage has risen above the preset threshold.

The MAX809 has an active-LOW reset output while the MAX810 has an active-HIGH reset output.

The low supply current of typically 17 μ A makes the MAX809/MAX810 ideal for use in portable, battery operated equipment. They are available in the 3-pin SOT23 and SC70 packages.

FEATURES

- Monitors 5.0 V, 3.3 V and 3 V supplies
- 140 ms min. reset delay time:
 - Active-LOW $\overline{\text{RESET}}$ output (MAX809)
 - Active-HIGH RESET output (MAX810)
- Power supply transient immunity
- Guaranteed reset valid for 1.1 V
- Available in small SOT23 and SC70 3-pin packages
- No external components needed
- Specified over full temperature range -40°C to $+105^{\circ}\text{C}$

APPLICATIONS

- Embedded controllers
- Battery operated systems
- Wireless communication systems
- PDAs and handheld equipment

SIMPLIFIED SYSTEM DIAGRAMS

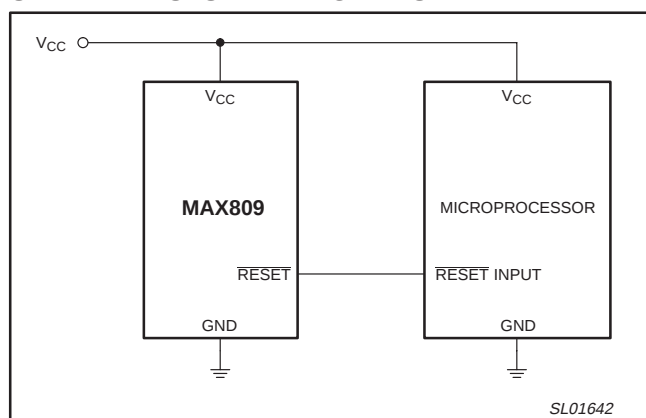


Figure 1. MAX809 simplified system diagram.

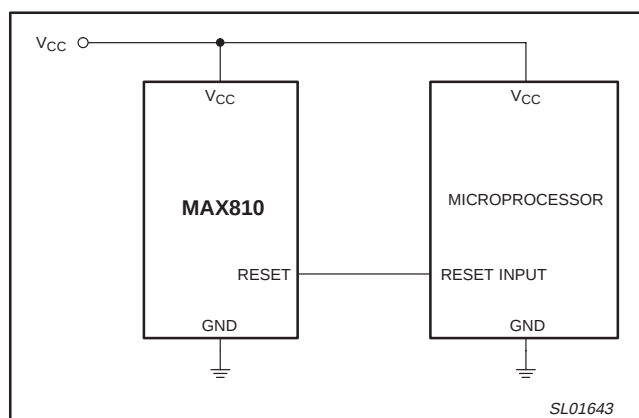


Figure 2. MAX810 simplified system diagram.

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ORDERING INFORMATION

TYPE NUMBER	PACKAGE		TEMPERATURE RANGE
	NAME	DESCRIPTION	
MAX809xD	SOT23-3	plastic small outline package; 3 leads (see dimensional drawing)	–40 to +105 °C
MAX810xD	SOT23-3	plastic small outline package; 3 leads (see dimensional drawing)	–40 to +105 °C
MAX809xW	SC70-3	plastic small outline package; 3 leads; body width 1.15 mm	–40 to +105 °C
MAX810xW	SC70-3	plastic small outline package; 3 leads; body width 1.15 mm	–40 to +105 °C

NOTE:

Each device has 7 voltage options, indicated by the 'x' on the 'Type number'.

Part number	Reset Threshold Voltage (Typical)	Part number	Reset Threshold Voltage (Typical)
MAX809		MAX810	
MAX809ZD, MAX809ZW	2.32 V	MAX810ZD, MAX810ZW	2.32 V
MAX809RD, MAX809RW	2.63 V	MAX810RD, MAX810RW	2.63 V
MAX809SD, MAX809SW	2.93 V	MAX810SD, MAX810SW	2.93 V
MAX809TD, MAX809TW	3.08 V	MAX810TD, MAX810TW	3.08 V
MAX809JD, MAX809JW	4.00 V	MAX810JD, MAX810JW	4.00 V
MAX809MD, MAX809MW	4.38 V	MAX810MD, MAX810MW	4.38 V
MAX809LD, MAX809LW	4.63 V	MAX810LD, MAX810LW	4.63 V

Marking code

Each device is marked with a four letter code. The first three letters designate the product. The fourth, represented by an 'x', designates the date tracking code.

Part	Marking	Part	Marking
MAX809		MAX810	
MAX809JD	AMUx	MAX810LD	ANTx
MAX809LD	AMVx	MAX810RD	ANUx
MAX809MD	AMWx	MAX810SD	ANVx
MAX809RD, W	AMXx	MAX810JD	ANWx
MAX809SD	AMYx	MAX810MD	ANXx
MAX809TD	AMZx	MAX810TD	ANYx
MAX809ZD	AMAx	MAX810ZD	ANZx

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PINNING

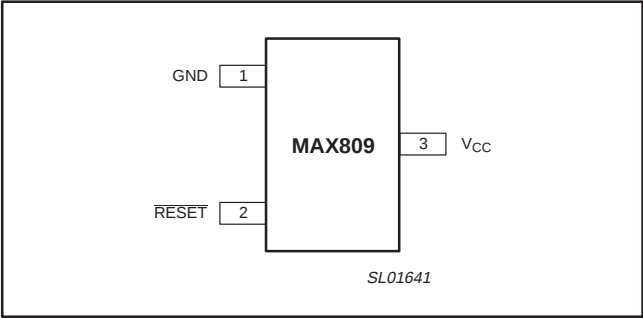


Figure 3. MAX809 pin configuration.

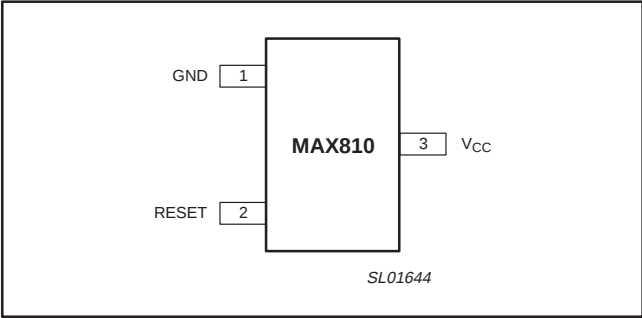


Figure 4. MAX810 pin configuration.

MAX809 pin description

PIN	SYMBOL	DESCRIPTION
1	GND	Device ground.
2	RESET	Active-LOW reset signal.
3	VCC	Supply voltage input.

MAX810 pin description

PIN	SYMBOL	DESCRIPTION
1	GND	Device ground.
2	RESET	Active-HIGH reset signal.
3	VCC	Supply voltage input.

MAXIMUM RATINGS

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CC}	Supply voltage		-0.3	6.0	V
V _{i(RESET)}	Input voltage at RESET pin (MAX809)		-0.3	V _{CC} + 0.3	V
V _{i(RESET)}	Input voltage at RESET pin (MAX810)		-0.3	V _{CC} + 0.3	V
I _{i(VCC)}	Input current at V _{CC} pin		-	20	mA
I _o	Output current		-	20	mA
	Rate of rise at V _{CC}		-	100	V/μs
T _{amb}	Ambient temperature		-40	+105	°C
T _{stg}	Storage temperature		-65	+150	°C
P	Power dissipation	Derate 4 mW/°C above T _{amb} = 70 °C	-	320	mW

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ELECTRICAL CHARACTERISTICS

V_{CC} = full range ($T_{amb} = -40\text{ }^{\circ}\text{C}$ to $+105\text{ }^{\circ}\text{C}$) unless otherwise noted; typical values are at $T_{amb} = +25\text{ }^{\circ}\text{C}$, and $V_{CC} = 5\text{ V}$ (J/L/M versions), $V_{CC} = 3.3\text{ V}$ (T/S versions), $V_{CC} = 3\text{ V}$ (R version), and $V_{CC} = 2.5\text{ V}$ (Z version) (Note 1).

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
	V _{CC} range	T _{amb} = 0 °C to +70 °C		1.0	–	5.5	V
		T _{amb} = –40 °C to +105 °C		1.2	–	5.5	V
I _{CC}	Supply current (SOT23-3)	MAX809J/L/M MAX810J/L/M	V _{CC} < 5.5 V; T _{amb} = –40 °C to +85 °C	–	24	60	μA
		MAX809R/S/T/Z MAX810R/S/T/Z	V _{CC} < 3.6 V; T _{amb} = –40 °C to +85 °C	–	17	50	μA
		MAX809J/L/M MAX810J/L/M	V _{CC} < 5.5 V; T _{amb} = +85 °C to +105 °C	–	–	100	μA
		MAX809R/S/T/Z MAX810R/S/T/Z	V _{CC} < 3.6 V; T _{amb} = +85 °C to +105 °C	–	–	100	μA
I _{CC}	Supply current (SC70-3)	MAX809J/L/M MAX810J/L/M	V _{CC} < 5.5 V; T _{amb} = –40 °C to +85 °C	–	24	35	μA
		MAX809R/S/T/Z MAX810R/S/T/Z	V _{CC} < 3.6 V; T _{amb} = –40 °C to +85 °C	–	17	30	μA
		MAX809J/L/M MAX810J/L/M	V _{CC} < 5.5 V; T _{amb} = +85 °C to +105 °C	–	–	60	μA
		MAX809R/S/T/Z MAX810R/S/T/Z	V _{CC} < 3.6 V; T _{amb} = +85 °C to +105 °C	–	–	60	μA
V _{th}	RESET threshold voltage (SOT23-3 and SC70-3)	MAX809Z MAX810Z	T _{amb} = +25 °C	2.28	2.32	2.35	V
			T _{amb} = –40 °C to +85 °C	2.25	–	2.38	V
			T _{amb} = +85 °C to +105 °C	2.22	–	2.42	V
		MAX809R MAX810R	T _{amb} = +25 °C	2.59	2.63	2.66	V
			T _{amb} = –40 °C to +85 °C	2.55	–	2.70	V
			T _{amb} = +85 °C to +105 °C	2.50	–	2.76	V
		MAX809S MAX810S	T _{amb} = +25 °C	2.89	2.93	2.96	V
			T _{amb} = –40 °C to +85 °C	2.85	–	3.00	V
			T _{amb} = +85 °C to +105 °C	2.78	–	3.08	V
		MAX809T MAX810T	T _{amb} = +25 °C	3.04	3.08	3.11	V
			T _{amb} = –40 °C to +85 °C	3.00	–	3.15	V
			T _{amb} = +85 °C to +105 °C	2.92	–	3.23	V
		MAX809J MAX810J	T _{amb} = +25 °C	3.93	4.00	4.06	V
			T _{amb} = –40 °C to +85 °C	3.89	–	4.10	V
			T _{amb} = +85 °C to +105 °C	3.80	–	4.20	V
		MAX809M MAX810M	T _{amb} = +25 °C	4.31	4.38	4.45	V
			T _{amb} = –40 °C to +85 °C	4.25	–	4.50	V
			T _{amb} = +85 °C to +105 °C	4.16	–	4.56	V
		MAX809L MAX810L	T _{amb} = +25 °C	4.56	4.63	4.70	V
			T _{amb} = –40 °C to +85 °C	4.50	–	4.75	V
			T _{amb} = +85 °C to +105 °C	4.40	–	4.86	V
	Reset threshold temperature coefficient		–	30	–	ppm/°C	
	V _{CC} to RESET delay	V _{CC} = V _{th} to (V _{th} – 100 mV)		–	20	–	μs
	RESET active time-out period (SOT23-3)	T _{amb} = –40 °C to +85 °C		140	240	560	ms
		T _{amb} = +85 °C to +105 °C		100	–	840	ms
	RESET active time-out period (SC70-3)	T _{amb} = –40 °C to +85 °C		140	240	460	ms
		T _{amb} = +85 °C to +105 °C		100	–	840	ms

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SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
$V_{OL(RESET)}$	LOW-level output voltage on RESET pin	MAX809R/S/T/Z	$V_{CC} > V_{th(min)}$; $I_{sink} = 1.2 \text{ mA}$	–	–	0.3	V
		MAX809J/L/M	$V_{CC} > V_{th(min)}$; $I_{sink} = 3.2 \text{ mA}$	–	–	0.4	V
		MAX809	$V_{CC} > 1.0 \text{ V}$; $I_{sink} = 50 \text{ }\mu\text{A}$	–	–	0.3	V
$V_{OH(RESET)}$	HIGH-level output voltage on RESET pin	MAX809R/S/T/Z	$V_{CC} > V_{th(max)}$; $I_{source} = 500 \text{ }\mu\text{A}$	$0.8 \times V_{CC}$	–	–	V
		MAX809J/L/M	$V_{CC} > V_{th(max)}$; $I_{source} = 800 \text{ }\mu\text{A}$	$V_{CC} - 1.5$	–	–	V
$V_{OL(RESET)}$	LOW-level output voltage on RESET pin	MAX810R/S/T/Z	$V_{CC} > V_{th(max)}$; $I_{sink} = 1.2 \text{ mA}$	–	–	0.3	V
		MAX810J/L/M	$V_{CC} > V_{th(max)}$; $I_{sink} = 3.2 \text{ mA}$	–	–	0.4	V
$V_{OH(RESET)}$	HIGH-level output voltage on RESET pin	MAX810	$1.8 \text{ V} < V_{CC} > V_{th(min)}$; $I_{source} = 150 \text{ }\mu\text{A}$	$0.8 \times V_{CC}$	–	–	V

NOTES:

1. Production testing done at $T_{amb} = +25 \text{ }^{\circ}\text{C}$; limits over temperature guaranteed by design.
2. RESET output for MAX809; RESET output for MAX810.

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MAX809/MAX810

APPLICATION INFORMATION

Detailed description

A microprocessor reset ensures that the microprocessor starts in a known and safe state. The reset asserts a reset signal to prevent code execution errors during power up, power down, or brownout conditions.

A reset must assert an output within a predictable range of the supply voltage. The common threshold voltage range is between 5% and 10% of the nominal supply voltage. MAX809/MAX810 have 4.63 V, 3.08 V and 2.63 V options for 5 V, 3.3 V and 3.0 V supplies respectively. They have high accuracy (within $\pm 3\%$) that ensures that the reset thresholds occur within the safe operating range. Whenever the supply voltage falls below the reset threshold, the reset signal is asserted. It remains asserted for at least 140 ms after the supply voltage rises above the threshold. At this point the reset is released. This delay time helps ensure valid reset signals despite erratic changes in supply voltage. The MAX809/MAX810 have a push-pull output stage and do not require a pull-up resistor.

Negative-going V_{CC} transients

The MAX809/MAX810 are relatively immune to short negative-going V_{CC} transients and glitches. Figure 5 shows the maximum pulse width a negative-going transient can have without causing a reset signal. As the magnitude of the transient increases below the reset threshold, the maximum allowable pulse width decreases. Typically, for the 4.0 V, 4.38 V, and 4.63 V versions of the MAX809/MAX810, a V_{CC} transient that goes 100 mV below the reset threshold and lasts 20 μ s or less will not cause a reset signal. To provide additional transient rejection, connect a 10 nF bypass capacitor as close as possible to the V_{CC} pin.

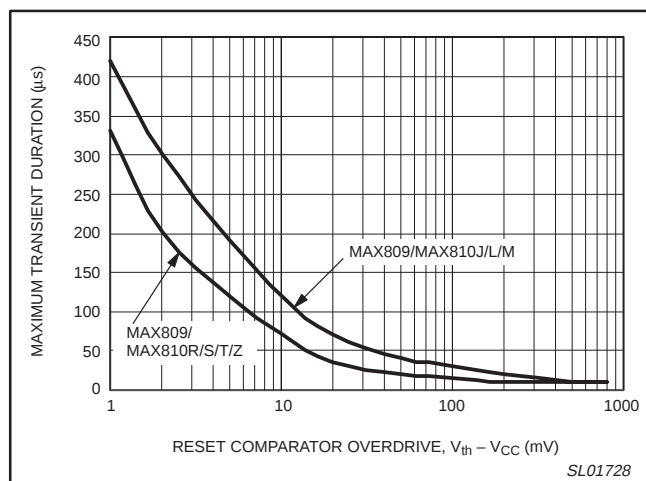


Figure 5. Maximum transient duration without causing a reset pulse versus reset comparator overdrive.

Ensuring a valid reset output down to $V_{CC} = 0$ V

When V_{CC} falls below 1 V, the MAX809 $\overline{\text{RESET}}$ no longer sinks current (i.e., it becomes open circuit). A high impedance CMOS logic input connected to $\overline{\text{RESET}}$ can drift to undetermined voltages. In most applications in which the microprocessor circuitry is inoperative below 1 V, this will not represent a problem. However, in applications in which $\overline{\text{RESET}}$ must be valid down to 0 V, use a

relatively large resistor from $\overline{\text{RESET}}$ to ground as shown in Figure 6. 100 k Ω is small enough to provide a path for any leakage currents to flow to ground (holding $\overline{\text{RESET}}$ LOW); while it is large enough not to load $\overline{\text{RESET}}$. Conversely, a 100 k Ω pull-up resistor is recommended for MAX810 if $\overline{\text{RESET}}$ is required to remain valid for $V_{CC} < 1$ V.

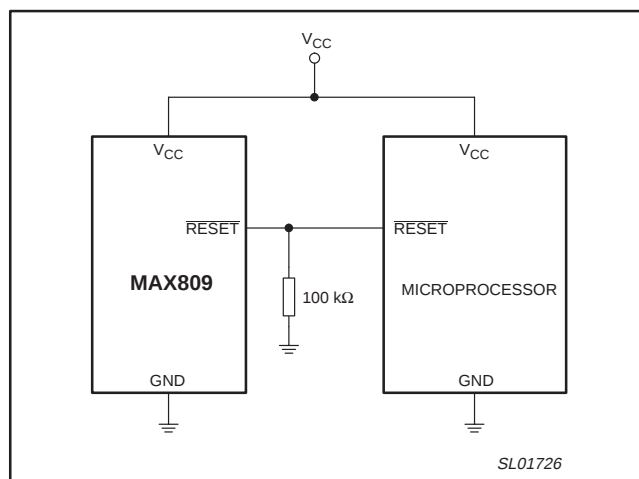


Figure 6. $\overline{\text{RESET}}$ valid to $V_{CC} = 0$ V circuit.

Interfacing to microprocessors with bi-directional reset pins

Microprocessors with bi-directional reset I/Os, such as the Motorola 68HC11 series, can be connected to the MAX809 $\overline{\text{RESET}}$ output. To ensure a correct output on the MAX809, even when the microprocessor reset I/O is in the opposite state, connect a 4.7 k Ω resistor between the reset pins as shown in Figure 7. This allows the microprocessor to issue commands to the system regardless of the state of the $\overline{\text{RESET}}$. The bi-directional microprocessor reset functions both as a driven reset input and as an active reset driver.

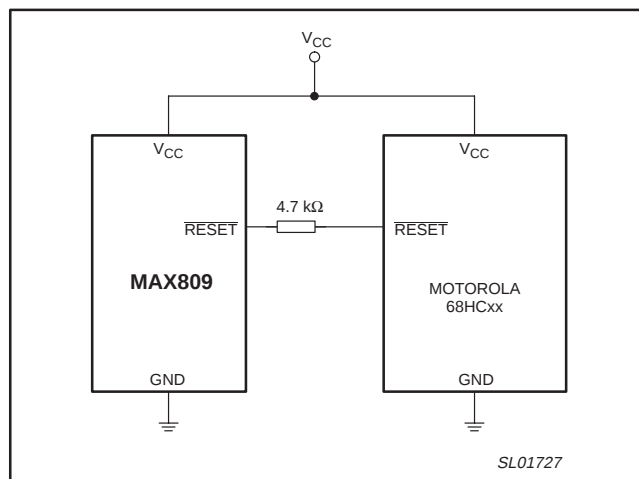


Figure 7. Interfacing to microprocessor with bi-directional reset I/O

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MAX809/MAX810

PACKING METHOD

The MAX809 and MAX810 are packed in reels, as shown in Figure 8.

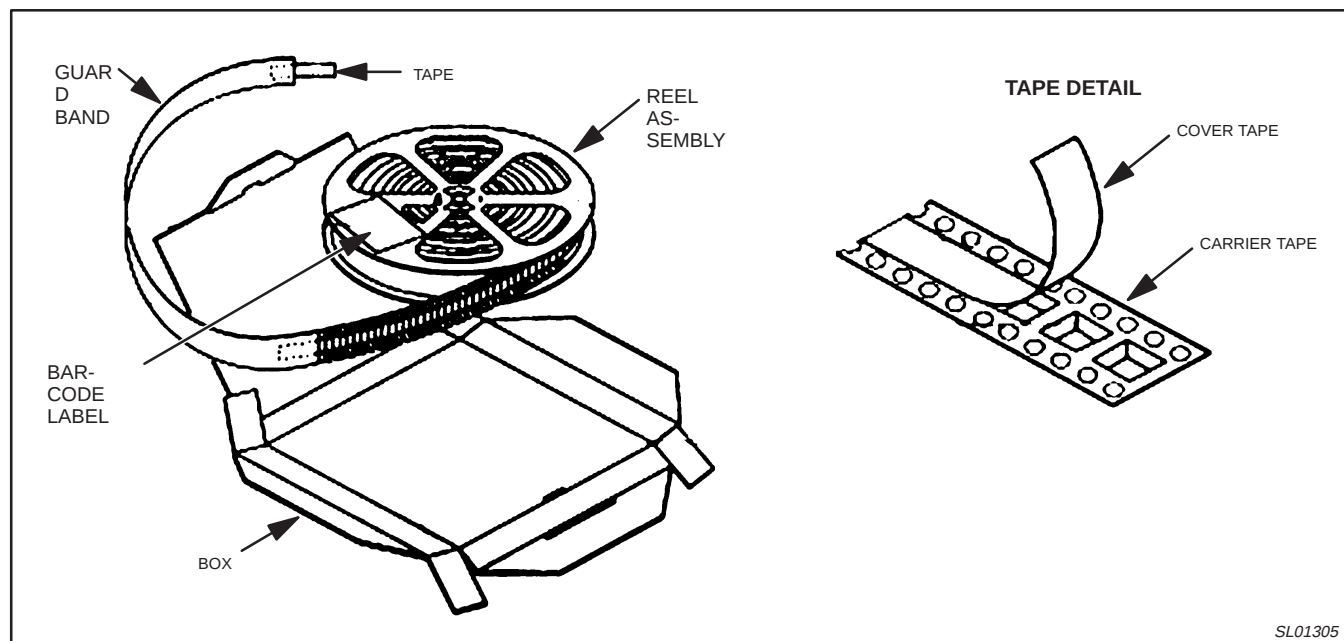
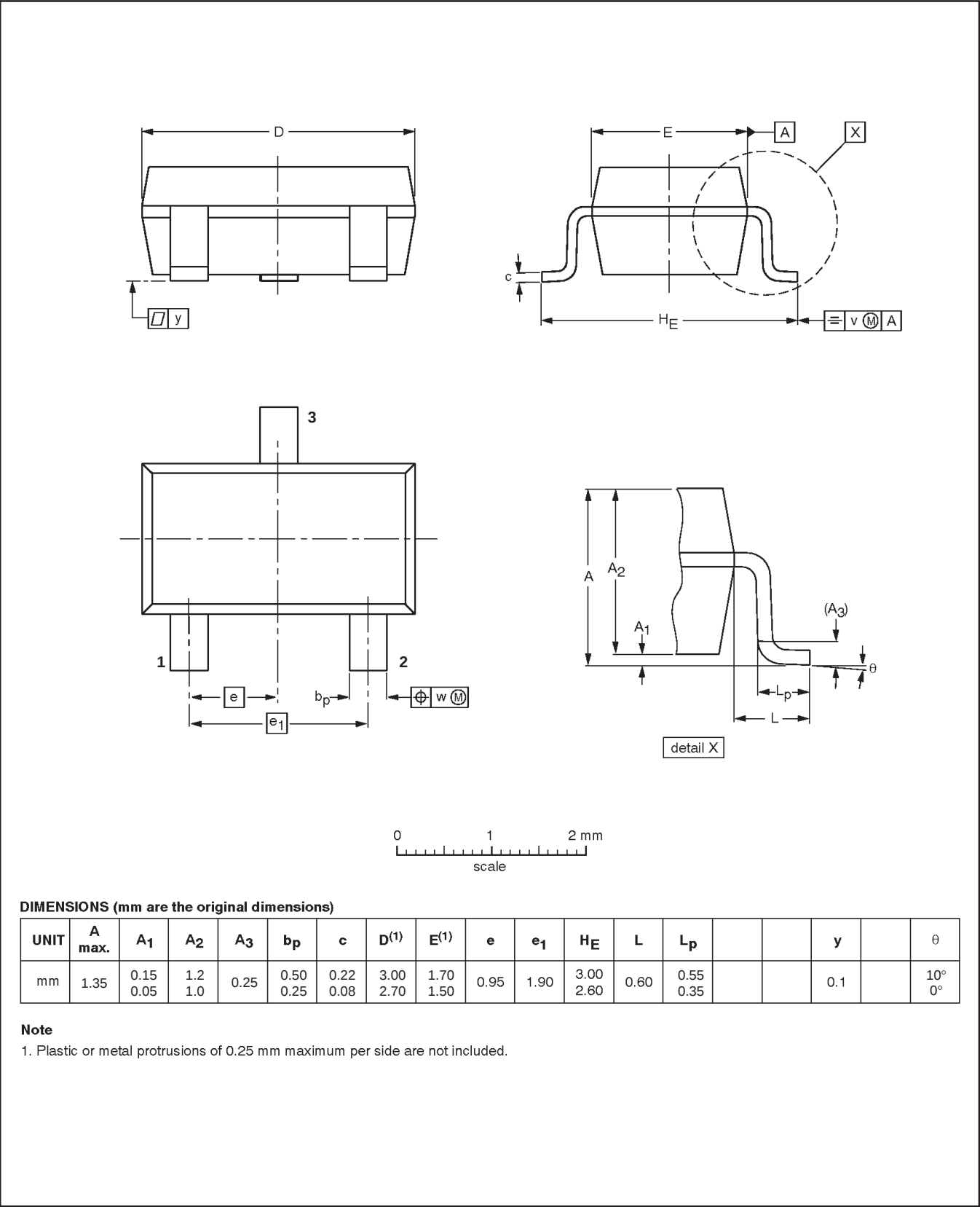


Figure 8. Tape and reel packing method.

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MAX809/MAX810

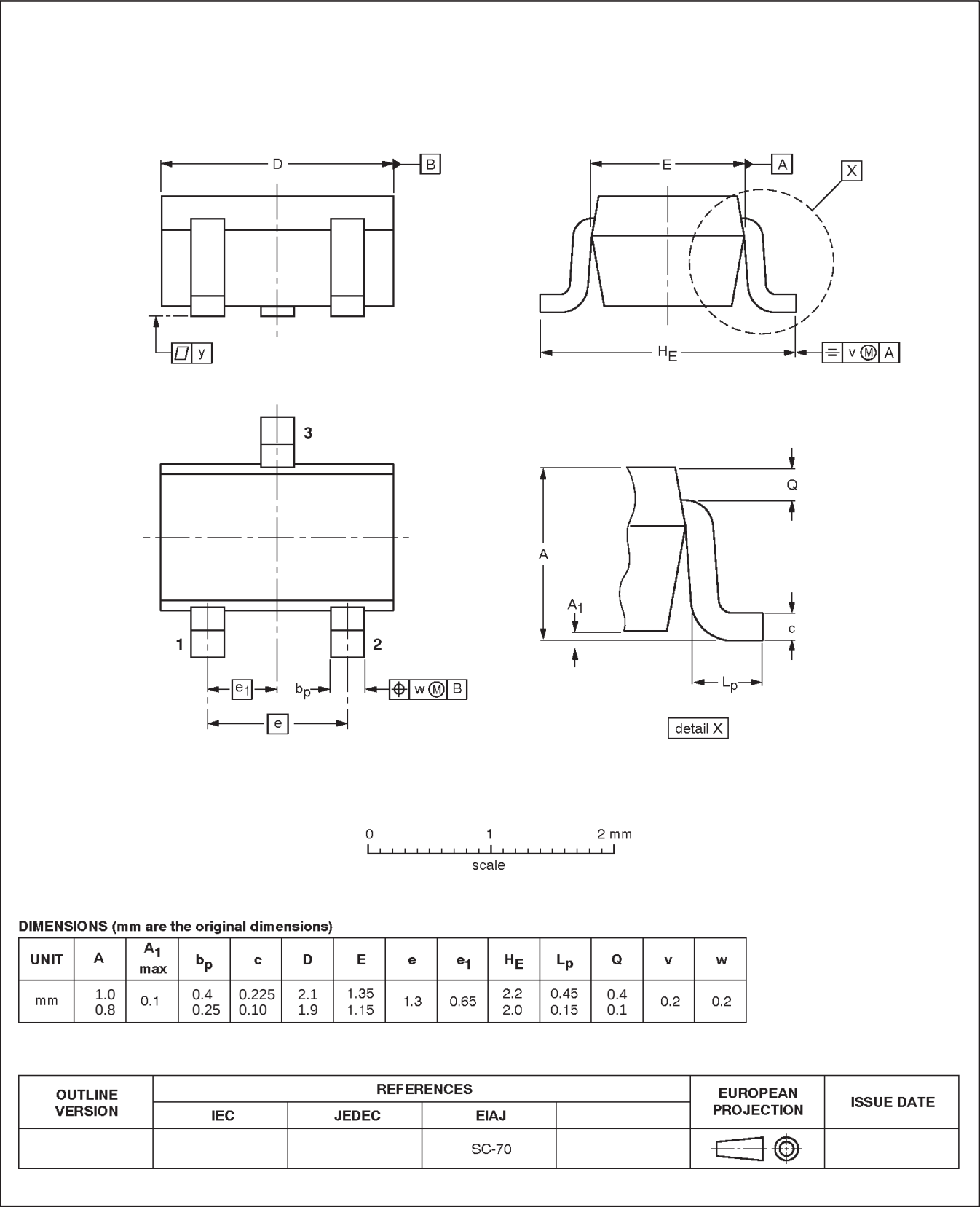
SOT23-3: plastic small outline package; 3 leads; body width 1.5 mm



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SC70-3: plastic small outline package; 3 leads; body width 1.15 mm



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REVISION HISTORY

Rev	Date	Description
_3	20030808	Product data (9397 750 11889); ECN 853–2355 30184 dated 04 August 2003. Supersedes MAX809_MAX810_2 of 2002 Oct 21 (9397 750 10584). Modifications: • Add 'Marking code' table to Ordering information on page 3. • SOT23–3 package outline on page 9 corrected.
_2	20021021	Product data (9397 750 10584); ECN 853–2355 29047 dated 14 October 2002. Supersedes MAX809_MAX810_1 of 2002 June 20 (9397 750 10213).
_1	20020620	Product data (9397 750 10213); ECN 853–2355 28505 dated 20 June 2002.

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Level	Data sheet status ^[1]	Product status ^{[2] [3]}	Definitions
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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