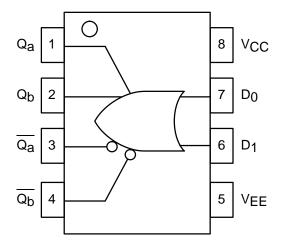
Low Impedance Driver

The MC100LVEL12 is a low impedance drive buffer. With two pairs of OR/NOR outputs the device is ideally suited for high drive applications such as memory addressing. The device is a function equivalent to the EL12 device and operates from a low voltage power supply. With propagation delays equivalent to the EL12, the LVEL12 is ideally suited for those applications which require the ultimate in AC performance in a low voltage environment.

- 445ps Propagation Delay
- Dual Outputs for 25Ω Drive Applications
- $75k\Omega$ Internal Input Pulldown Resistors
- >2000V ESD Protection





Sector Contraction of the sector of the sect								
D SUFFIX 8–LEAD PLASTIC SOIC PACKAGE CASE 751–05								
PIN DESCRIPTION								

MC100LVEL12

PIN	FUNCTION
D0, D1	Data Inputs
Qa, Qb	Data Outputs

DC CHARACTERISTICS (V_{EE} = V_{EE}(min) to V_{EE}(max); V_{CC} = GND)

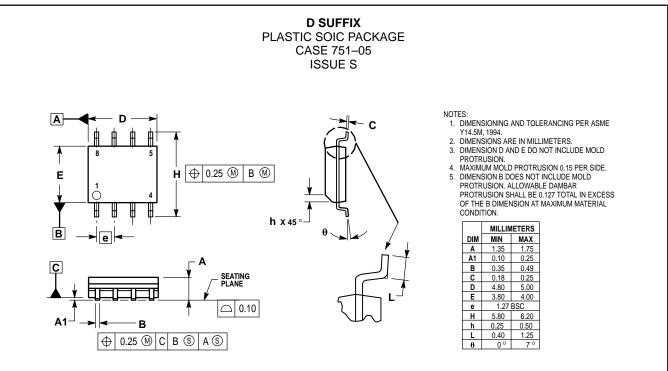
		_40°C			0°C			25°C			85°C			
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit
IEE	Power Supply Current		17	24		17	24		17	24		18	25	mA
V_{EE}	Power Supply Voltage	-3.0		-3.8	-3.0		-3.8	-3.0		-3.8	-3.0		-3.8	V
Iн	Input HIGH Current			150			150			150			150	μΑ

AC CHARACTERISTICS (VEE = VEE(min) to VEE(max); VCC = GND)

		-40°C			0°C			25°C			85°C			
Symbol	Characteristic	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit
^t PLH ^t PHL	Propagation Delay to Output	310		580	310		580	310	445	580	320		590	ps
t _r t _f	Output Rise/Fall Times Q (20% – 80%)	230	400	550	230	400	550	230	400	550	230	400	550	ps

6/97

OUTLINE DIMENSIONS



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