## SAN|NTM MCH3412

## Features

- Low ON-resinstance.
- Ultrahigh-speed switching.
. 4 V drive.


## Package Dimensions

unit: mm
2167


## Specifications

Absolute Maximum Ratings at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Drain-to-Source Voltage | VDSS |  | 30 | V |
| Gate-to-Source Voltage | $\mathrm{V}_{\text {GSS }}$ |  | $\pm 20$ | V |
| Drain Current (DC) | ID |  | 3 | A |
| Drain Current (Pulse) | IDP | PW $\leq 10 \mu \mathrm{~s}$, duty cycle $\leq 1 \%$ | 12 | A |
| Allowable Power Dissipation | PD | Mounted on a ceramic board (900mm $2 \times 0.8 \mathrm{~mm})$ | 1 | W |
| Channel Temperature | Tch |  | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | Tstg |  | -55 to +125 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Drain-to-Source Breakdown Voltage | $\mathrm{V}_{\text {(BR) }}$ DSS | $\mathrm{I}_{\mathrm{D}}=1 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GS}}=0$ | 30 |  |  | V |
| Zero-Gate Voltage Drain Current | IDSS | VDS $=30 \mathrm{~V}$, VGS $=0$ |  |  | 1 | $\mu \mathrm{A}$ |
| Gate-to-Source Leakage Current | IGSS | $\mathrm{V}_{\mathrm{GS}}= \pm 16 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0$ |  |  | $\pm 10$ | $\mu \mathrm{A}$ |
| Cutoff Voltage | $\mathrm{V}_{\mathrm{GS}}$ (off) | VDS $=10 \mathrm{~V}, \mathrm{ID}=1 \mathrm{~mA}$ | 1.2 |  | 2.6 | V |
| Forward Transfer Admittance | $\mid \mathrm{yfs}$ \| | $\mathrm{V} \mathrm{DS}=10 \mathrm{~V}, \mathrm{ID}=1.5 \mathrm{~A}$ | 2.1 | 3 |  | S |

Marking : KM
Continued on next page.

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| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Static Drain-to-Source On-State Resistance | RDS(on)1 | $\mathrm{ID}=1.5 \mathrm{~A}, \mathrm{VGS}=10 \mathrm{~V}$ |  | 64 | 84 | $\mathrm{m} \Omega$ |
|  | RDS(on)2 | $\mathrm{l}=1 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=4 \mathrm{~V}$ |  | 105 | 150 | $\mathrm{m} \Omega$ |
| Input Capacitance | Ciss | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 180 |  | pF |
| Output Capacitance | Coss | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 42 |  | pF |
| Reverse Transfer Capacitance | Crss | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 25 |  | pF |
| Turn-ON Delay Time | $\mathrm{t}_{\mathrm{d}}(\mathrm{on})$ | See specified Test Circuit |  | 7 |  | ns |
| Rise Time | tr | See specified Test Circuit |  | 2.8 |  | ns |
| Turn-OFF Delay Time | $t_{d}$ (off) | See specified Test Circuit |  | 18.5 |  | ns |
| Fall Time | $\mathrm{tf}^{\text {f }}$ | See specified Test Circuit |  | 4.4 |  | ns |
| Total Gate Charge | Qg | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=3 \mathrm{~A}$ |  | 4.9 |  | nC |
| Gate-to-Source Charge | Qgs | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=3 \mathrm{~A}$ |  | 0.93 |  | nC |
| Gate-to-Drain "Miller" Charge | Qgd | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=3 \mathrm{~A}$ |  | 0.63 |  | nC |
| Diode Forward Voltage | $\mathrm{V}_{\text {SD }}$ | $\mathrm{IS}=3 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0$ |  | 0.85 | 1.2 | V |

## Switching Time Test Circuit



MCH3412


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