

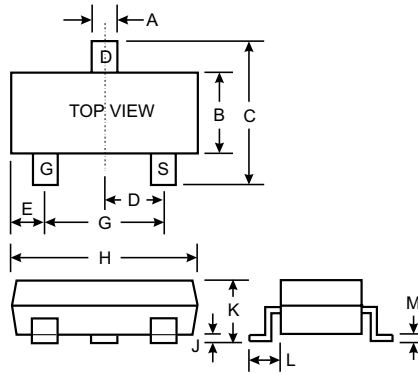
N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage

Mechanical Data

- Case: SOT-23, Molded Plastic
- Terminals: Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking: K6Z
- Weight: 0.008 grams (approx.)



SOT-23		
Dim	Min	Max
A	0.37	0.51
B	1.19	1.40
C	2.10	2.50
D	0.89	1.05
E	0.45	0.61
G	1.78	2.05
H	2.65	3.05
J	0.013	0.15
K	0.89	1.10
L	0.45	0.61
M	0.076	0.178
All Dimensions in mm		

Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	MMBF170	Units
Drain-Source Voltage	V _{DSS}	60	V
Drain-Gate Voltage R _{GS} ≤ 1.0MΩ	V _{DGR}	60	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Pulsed		±40	
Drain Current (Note 1)	I _D	500	mA
Continuous Pulsed		800	
Total Power Dissipation (Note 1) Derating above T _A = 25°C	P _d	225 1.80	mW mW/°C
Thermal Resistance, Junction to Ambient	R _{θJA}	556	K/W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +150	°C

Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 2)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	70	—	V	V _{GS} = 0V, I _D = 100μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1.0	μA	V _{DS} = 60V, V _{GS} = 0V
Gate-Body Leakage	I _{GSS}	—	—	±10	nA	V _{GS} = ±15V, V _{DS} = 0V
ON CHARACTERISTICS (Note 2)						
Gate Threshold Voltage	V _{GS(th)}	0.8	2.1	3.0	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	—	5.0	Ω	V _{GS} = 10V, I _D = 200mA
Forward Transconductance	g _{FS}	80	—	—	mS	V _{DS} = 10V, I _D = 0.2A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	—	22	40	pF	V _{DS} = 10V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	11	30	pF	
Reverse Transfer Capacitance	C _{rss}	—	2.0	5.0	pF	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{D(ON)}	—	—	10	ns	V _{DD} = 25V, I _D = 0.5A, V _{GS} = 10V, R _{GEN} = 50Ω
Turn-Off Delay Time	t _{D(OFF)}	—	—	10	ns	

Note:1. Valid provided that terminals are kept at specified ambient temperature.

2. Pulse width ≤ 300μs, duty cycle ≤ 2%.

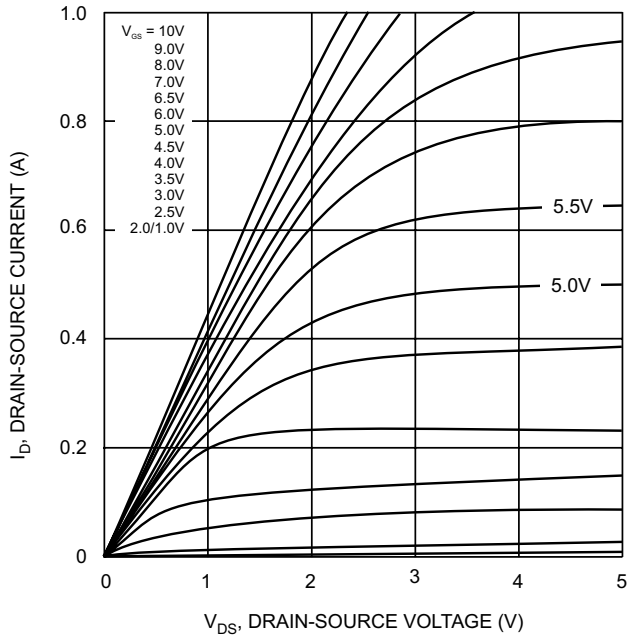


Fig. 1 On-Region Characteristics

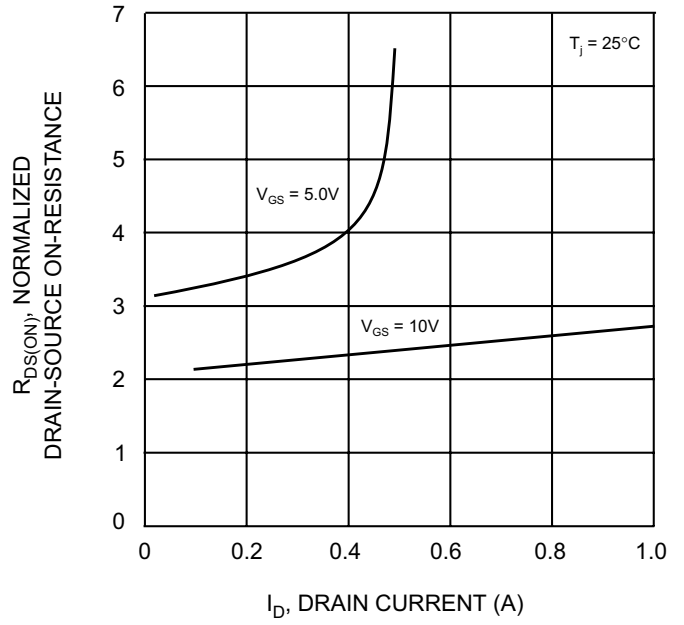


Fig. 2 On-Resistance vs Drain Current

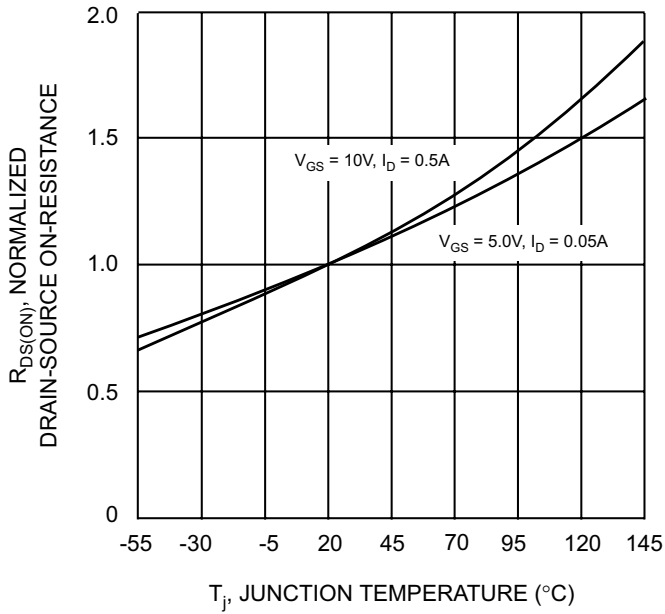


Fig. 3 On-Resistance vs Junction Temperature

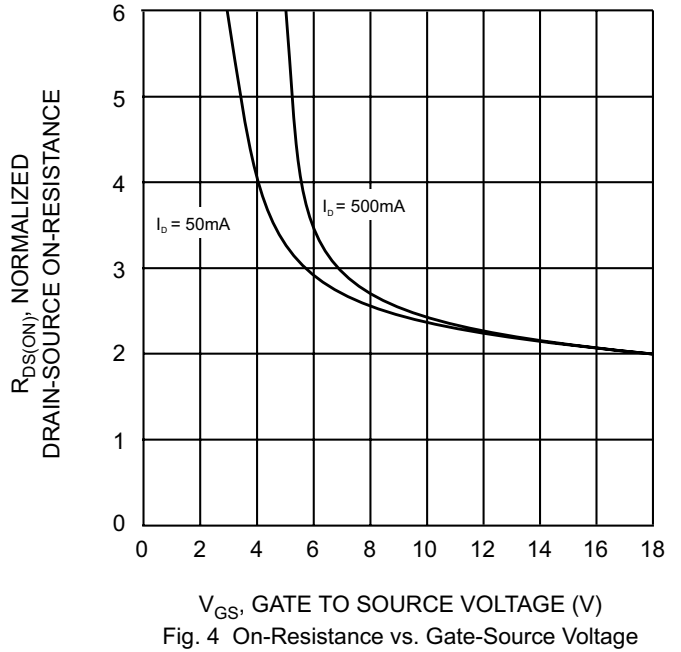


Fig. 4 On-Resistance vs. Gate-Source Voltage