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MMBTA42

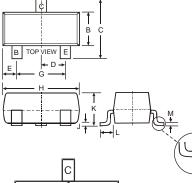
NPN SMALL SIGNAL SURFACE MOUNT TRANSISTOR

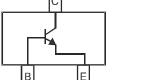
Features

- Epitaxial Planar Die Construction
- Complementary PNP Type Available (MMBTA92)
- Ideal for Low Power Amplification and Switching
- Lead Free/RoHS Compliant (Note 4)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Marking (See Page 2): K3M
- Ordering & Date Code Information: See Page 2
- Weight: 0.008 grams (approximate)





SOT-23									
Dim	Min	Max							
Α	0.37	0.51							
В	1.20	1.40							
С	2.30	2.50 1.03							
D	0.89								
Е	0.45	0.60							
G	1.78	2.05							
н	2.80	3.00							
J	0.013	0.10							
К	0.903	1.10							
L	0.45	0.61							
М	0.085	0.180							
α	0°	8°							
All Dimensions in mm									

Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	300	V
Collector-Emitter Voltage	V _{CEO}	300	V
Emitter-Base Voltage	V _{EBO}	6.0	V
Collector Current (Note 1) (Note 3)	Ι _C	500	mA
Power Dissipation (Note 1)	Pd	300	mW
Thermal Resistance, Junction to Ambient (Note 1)	$R_{ heta JA}$	417	°C/W
Operating and Storage and Temperature Range	T _i , T _{STG}	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 2)				-			
Collector-Base Breakdown Voltage	V _{(BR)CBO}	300		V	$I_{C} = 100 \mu A, I_{E} = 0$		
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	300		V	I _C = 1.0mA, I _B = 0		
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	6.0		V	I _E = 100μA, I _C = 0		
Collector Cutoff Current	I _{CBO}	_	100	nA	V _{CB} = 200V, I _E = 0		
Collector Cutoff Current	I _{EBO}		100	nA	$V_{CE} = 6.0V, I_{C} = 0$		
ON CHARACTERISTICS (Note 2)				-			
DC Current Gain	h _{FE}	25 40 40	_	_	$I_{C} = 1.0 \text{mA}, V_{CE} = 10 \text{V}$ $I_{C} = 10 \text{mA}, V_{CE} = 10 \text{V}$ $I_{C} = 30 \text{mA}, V_{CE} = 10 \text{V}$		
Collector-Emitter Saturation Voltage	V _{CE(SAT)}		0.5	V	$I_{\rm C} = 20 {\rm mA}, I_{\rm B} = 2.0 {\rm mA}$		
Base- Emitter Saturation Voltage	V _{BE(SAT)}		0.9	V	$I_{\rm C} = 20 {\rm mA}, I_{\rm B} = 2.0 {\rm mA}$		
SMALL SIGNAL CHARACTERISTICS							
Output Capacitance	C _{cb}		3.0	pF	$V_{CB} = 20V, f = 1.0MHz, I_E = 0$		
Current Gain-Bandwidth Product	f _T	50		MHz	$V_{CE} = 20V, I_C = 10mA,$ f = 100MHz		

Notes: 1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout

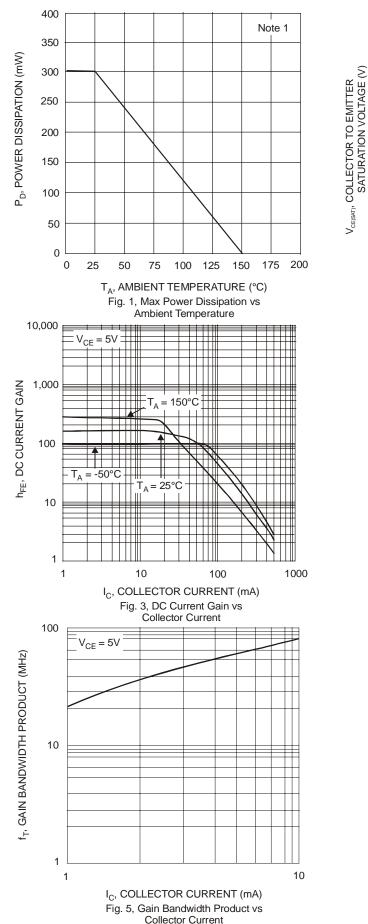
document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

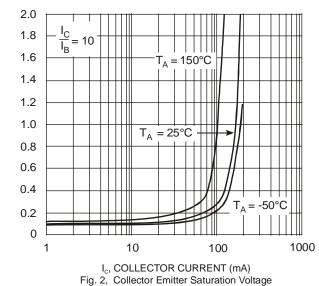
2. Short duration pulse test used to minimize self-heating effect.

 When operated under collector-emitter saturation conditions within the safe operating area defined by the thermal resistance rating (R_{θJA}), power dissipation rating (P_d) and power derating curve (figure 1).

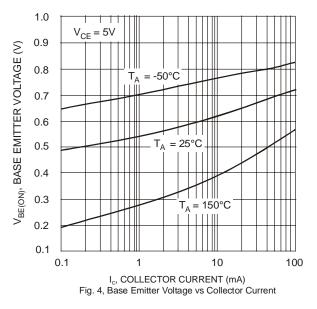
4. No purposefully added lead.







 Collector Emitter Saturation Voltage vs. Collector Current



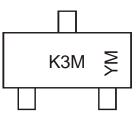


Ordering Information (Note 5)

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Device	Packaging	Shipping		
MMBTA 42-7-F	SOT-23	3000/Tape & Reel		

Notes: 5. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



K3M = Product Type Marking CodeYM = Date Code MarkingY = Year ex: N = 2002M = Month ex: 9 = September

Date Code Key

Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	J	К	L	М	Ν	Р	R	S	Т	U	V	W	Х	Y	Z
N	lonth		Jan	Feb	Mar	Apr	Мау	Jun	Jul	Αι	ıg (Бер	Oct	Nov	Dec
Code			1	2	3	4	5	6	7	8	3	9	0	Ν	D

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