Preferred Device

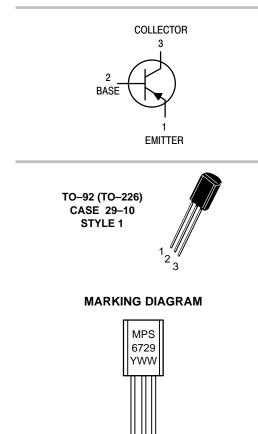
# **One Watt Amplifier Transistor**

**PNP Silicon** 

# ON

### **ON Semiconductor**<sup>™</sup>

http://onsemi.com



Y = Year WW = Work Week

#### ORDERING INFORMATION

Device	Package	Shipping
MPS6729	TO-92	Bulk

**Preferred** devices are recommended choices for future use and best overall value.

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	-80	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	-80	Vdc
Emitter-Base Voltage	VEBO	-4.0	Vdc
Collector Current – Continuous	IC	-500	mAdc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	1.0 8.0	Watt mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	2.5 20	Watts mW/°C
Operating and Storage Junction Temperature Range	TJ, T <sub>stg</sub>	–55 to +150	°C

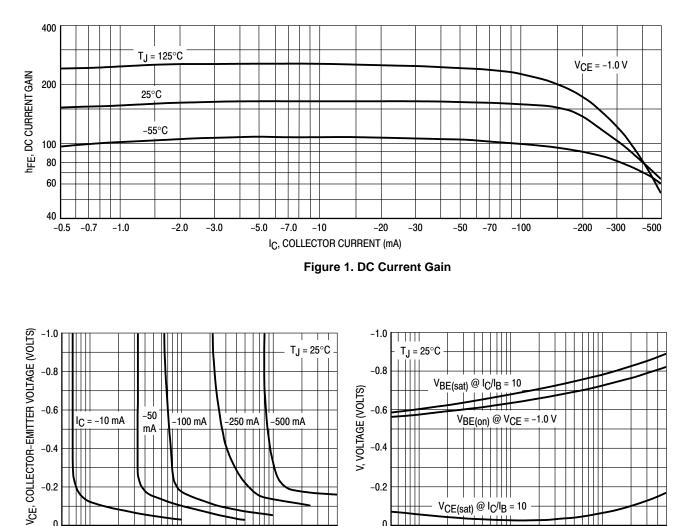
#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta}JA$	125	°C/W
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	50	°C/W

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Breakdown Voltage (Note 1.) $(I_C = -1.0 \text{ mAdc}, I_B = 0)$	V(BR)CEO	-80	_	Vdc
Collector–Base Breakdown Voltage $(I_{C} = 0.1 \text{ mA}, I_{E} = 0)$	V(BR)CBO	-80	-	Vdc
Emitter–Base Breakdown Voltage ( $I_E = -10 \mu Adc, I_C = 0$ )	V(BR)EBO	-5.0	-	Vdc
Collector Cutoff Current ( $V_{CB} = -60 \text{ Vdc}, I_E = 0$ )	Ісво	-	-0.1	μAdc
Emitter Cutoff Current ( $V_{EB} = -5.0 \text{ Vdc}, I_{C} = 0$ )	IEBO	-	-10	μAdc
ON CHARACTERISTICS (Note 1.)				
DC Current Gain ( $I_C = -50 \text{ mAdc}$ , $V_{CE} = -1.0 \text{ Vdc}$ ) ( $I_C = -250 \text{ mAdc}$ , $V_{CE} = -1.0 \text{ Vdc}$ )	hFE	80 50	_ 250	-
Collector–Emitter Saturation Voltage $(I_C = -250 \text{ mAdc}, I_B = -10 \text{ mAdc})$	V <sub>CE(sat)</sub>	-	-0.5	Vdc
Base–Emitter On Voltage ( $I_C = -250 \text{ mAdc}, V_{CE} = -1.0 \text{ Vdc}$ )	V <sub>BE(on)</sub>	-	-1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Collector–Base Capacitance ( $V_{CB} = -10 \text{ Vdc}, f = 1.0 \text{ MHz}$ )	C <sub>cb</sub>	-	30	pF
Small–Signal Current Gain (I <sub>C</sub> = 200 mA, V <sub>CE</sub> = 5.0 V, f = 20 MHz)	h <sub>fe</sub>	2.5	25	

1. Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2.0%.



V<sub>CE(sat)</sub> @ I<sub>C</sub>/I<sub>B</sub> = 10

-5.0 -10

-20

Figure 3. "On" Voltages

IC, COLLECTOR CURRENT (mA)

ΠI

-50 -100 -200

-500

٥L

-50

Ш

-2.0

-0.5 -1.0

IT

-10 -20

-5.0

0

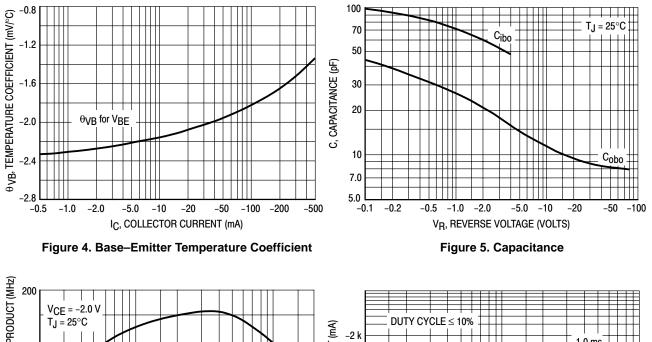
-0.05 -0.1

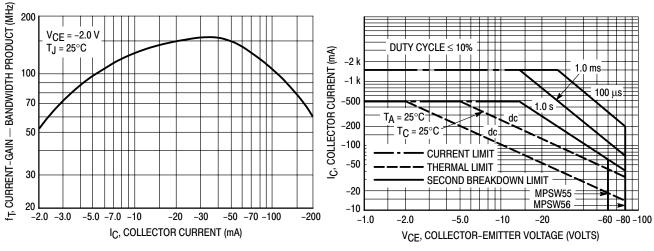
-0.2

-0.5 -1.0 -2.0

IB, BASE CURRENT (mA)

Figure 2. Collector Saturation Region





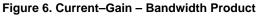
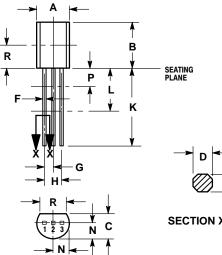


Figure 7. Active Region – Safe Operating Area



**TO-92 (TO-226)** CASE 29-10 ISSUE AL





SECTION X-X

NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. DIMENSION F AND JAPPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.44	5.21	
В	0.290	0.310	7.37	7.87	
С	0.125	0.165	3.18	4.19	
D	0.018	0.021	0.457	0.533	
F	0.016	0.019	0.407	0.482	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.018	0.024	0.46	0.61	
Κ	0.500		12.70		
L	0.250		6.35		
Ν	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.135		3.43		

STYLE 1: PIN 1. EMITTER 2. BASE 3. COLLECTOR

# <u>Notes</u>

# <u>Notes</u>

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