The RF Line UHF Power Transistor

Designed primarily for wideband, large-signal output and driver amplifier stages in the 500 to 1000 MHz frequency range.

- Designed for Class AB Linear Power Amplifiers
- Specified 28 Volt, 1000 MHz Characteristics: Output Power — 50 Watts Power Gain — 7 dB (Min), Class AB
- Built-In Matching Network for Broadband Operation
- Gold Metallization for Improved Reliability
- Diffused Ballast Resistors
- Hermetic Package for Military/Space Applications



7.0 dB, 500 – 1000 MHz 50 W BROADBAND UHF POWER TRANSISTOR



CASE 391-03, STYLE 1 (HLP-42)

MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Collector–Emitter Voltage	VCEO	30	Vdc	
Collector-Base Voltage	VCBO	60	Vdc	
Emitter-Base Voltage	VEBO	4	Vdc	
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	125 0.715	Watts W/°C	
Operating Junction Temperature	Тј	200	°C	
Storage Temperature Range	T _{stg}	-65 to +200	°C	
THERMAL CHARACTERISTICS				
Characteristic	Symbol	Max	Unit	
Thermal Resistance, RF, Junction to Case (T _C = 70°C)	R _{θJC}	1.4	°C/W	

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS (1)					
Collector–Emitter Breakdown Voltage ($I_C = 25 \text{ mA}, V_{BE} = 0$)	V _(BR) CES	60	_	-	Vdc
Collector–Base Breakdown Voltage ($I_C = 25 \text{ mA}, I_E = 0$)	V(BR)CBO	60	_	-	Vdc
Emitter–Base Breakdown Voltage ($I_E = 5 \text{ mA}, I_C = 0$)	V(BR)EBO	4	-	-	Vdc
Collector–Emitter Breakdown Voltage (I _C = 25 mA, R _{BE} = 1 Ω)	V(BR)CER	50	-	-	Vdc
Collector Cutoff Current ($V_{CB} = 30 \text{ V}, I_E = 0$)	Ісво	_	_	25	mAdc

(1) Each transistor chip measured separately.

(continued)



ELECTRICAL CHARACTERISTICS — continued

Characteristic	Symbol	Min	Тур	Max	Unit		
ON CHARACTERISTICS (1)							
DC Current Gain ($I_C = 1 \text{ A}, V_{CE} = 5 \text{ V}$)	h _{FE}	20	_	80	-		
DYNAMIC CHARACTERISTICS (1)							
Output Capacitance (V_{CB} = 28 V, I_E = 0, f = 1 MHz)	C _{ob}	-	_	24	pF		
FUNCTIONAL TESTS (2)							
Common–Emitter Amplifier Power Gain (V _{CE} = 28 V, P _{out} = 50 W, f = 1 GHz, I _{CQ} = 2 x 120 mA)	G _{PE1}	7	_	-	dB		
Load Mismatch (V _{CE} = 28 V, I _{CQ} = 2 x 120 mA, P _{OUt} = 50 W, f = 1 GHz, Load VSWR = 5:1, All Phase Angles)	Ψ	No Degradation in Output Power					
Broadband Power Gain (V _{CE} = 28 V, P _{OUt} = 45 W, f = 500 mHz and 1 GHz, I_{CQ} = 2 x 120 mA)	G _{PE2}	6.5	-	-	dB		

(1) Each transistor chip measured separately.

(2) Both transistor chips operating in push-pull amplifier.

TYPICAL CHARACTERISTICS



Figure 1. Input Impedance versus Frequency





PACKAGE DIMENSIONS



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