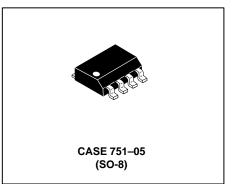
Designed primarily for use in DECT, Japan Personal Handy System (PHS), other wireless Personal Communication Systems (PCS) applications, and 2.4 GHz ISM band applications. The MRFIC1801 is a single pole, double throw reflective antenna switch featuring low insertion loss and high power handling capability in a low-cost SOIC-8 package. The integrated circuit requires no off-chip matching and provides for easy control circuit interface. The high power handling capability allows application in higher power wireless systems than traditional GaAs antenna switches.

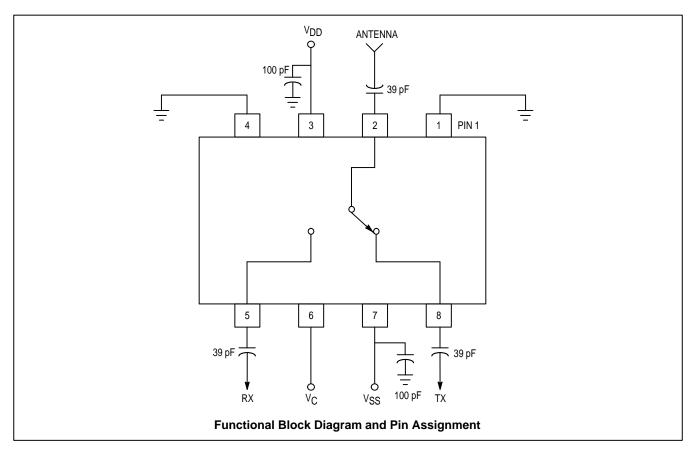
Together with the rest of the MRFIC180X series, this GaAs IC family offers the complete transmit and receive functions, less LO and filters, needed for a typical 1.8 GHz cordless telephone.

- Usable Frequency Range 1.5 to 2.5 GHz
- High 1.0 dB Compression Point = 29 dBm (Typ)
- Low Transmit Insertion Loss = 0.75 dB (Typ)
- High Transmit to Receive Isolation = 22 dB (Typ)
- Single Control Pin for Easy Switching Signal Interface
- Low Current Drain = 300 μA (Typ) in TX, 45 μA (Typ) in RX
- Low Cost Surface Mount Plastic Package
- Order MRFIC1801R2 for Tape and Reel.
- R2 Suffix = 2,500 Units per 12 mm, 13 inch Reel.
- Device Marking = M1801

# **MRFIC1801**

1.8 GHz TRANSMIT/RECEIVE ANTENNA SWITCH GaAs MONOLITHIC INTEGRATED CIRCUIT





### MAXIMUM RATINGS (T<sub>A</sub>= 25°C unless otherwise noted)

Ratings	Symbol	Value	Unit
Supply Voltage	V <sub>DD</sub>	10	Vdc
Supply Voltage Difference	$V_{DD} - V_{SS}$	8	Vdc
RF Input Power	Pin	33	dBm
Switch Control Voltage	٧ <sub>C</sub>	V <sub>DD</sub> +1, V <sub>SS</sub> –1	Vdc
Storage Temperature Range	T <sub>stg</sub>	– 65 to +150	°C
Operating Ambient Temperature	TA	– 30 to + 85	°C

## **RECOMMENDED OPERATING CONDITIONS**

Parameter	Symbol	Value	Unit
Supply Voltage	V <sub>DD</sub>	2.7 to 5.5	Vdc
Supply Voltage Difference	$V_{DD} - V_{SS}$	5.5	Vdc
Switch Control Voltage	٧ <sub>C</sub>	V <sub>DD</sub> to V <sub>SS</sub>	Vdc
Operating Frequency	f	1.5 to 2.5	GHz

# **ELECTRICAL CHARACTERISTICS** ( $V_{DD} - V_{SS} = 5.5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ , f = 1.9 GHz)

Characteristic	Min	Тур	Max	Unit
Antenna to Receive Insertion Loss (RX Mode, PIN = 0 dBm)	—	0.8	1	dB
Transmit to Antenna Insertion Loss (TX Mode, PIN = +27 dBm)	_	0.6	1	dB
Transmit to Receive Isolation in TX Mode (P <sub>IN</sub> = +27 dBm)	-	22	—	dB
Antenna to Transmit Isolation in RX Mode (PIN = 0 dBm)	_	18	—	dB
Input Return Loss, all ports	-	15	—	dB
Transmit to Antenna Input 1.0 dB Compression	-	29	—	dBm
Leakage Current (RX Mode)	-	45	—	μA
Total Supply Current (TX Mode)	_	300	_	μA

### **EVALUATION BOARDS**

Evaluation boards are available for RF Monolithic Integrated Circuits by adding a "TF" suffix to the device type. For a complete list of currently available boards and ones in development for newly introduced product, please contact your local Motorola Distributor or Sales Office.

# **APPLICATIONS INFORMATION**

The MRFIC1801 is a SPDT switch. A series–shunt pair of FETs are used in each path for improved isolation. The power handling capability of the MRFIC1801 is determined by the difference between  $V_{DD}$  and  $V_{SS}$ . Typical operating conditions are  $V_{DD}$  = 3.0 V and  $V_{SS}$  = -2.5 V, but a negative  $V_{SS}$  is not required.  $V_{SS}$  can be grounded.

Mode	v <sub>C</sub>
RX	V <sub>SS</sub>
ТХ	V <sub>DD</sub>

Table 1. Logic Table

TYPICAL CHARACTERISTICS  $(V_{DD} - V_{SS} = 5.5 V)$ 

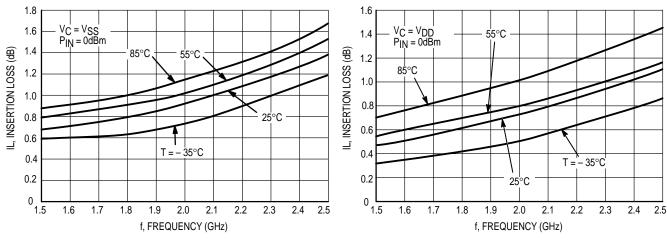


Figure 1. Antenna to Receive Insertion Loss

Figure 2. Transmit to Antenna Insertion Loss (Small Signal)

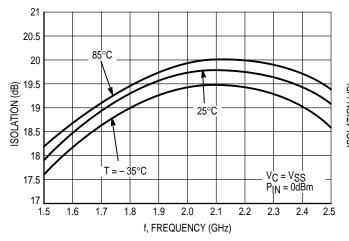


Figure 3. Antenna to Transmit Isolation in RX Mode

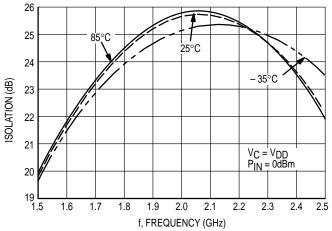
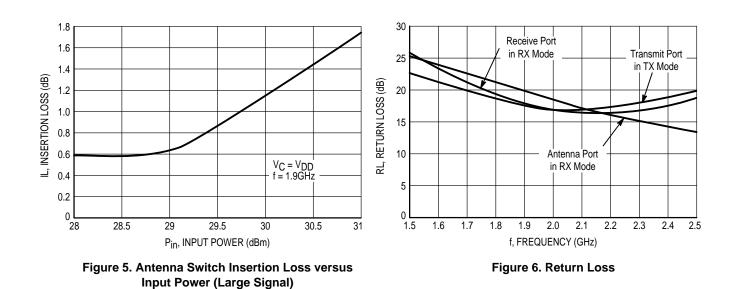
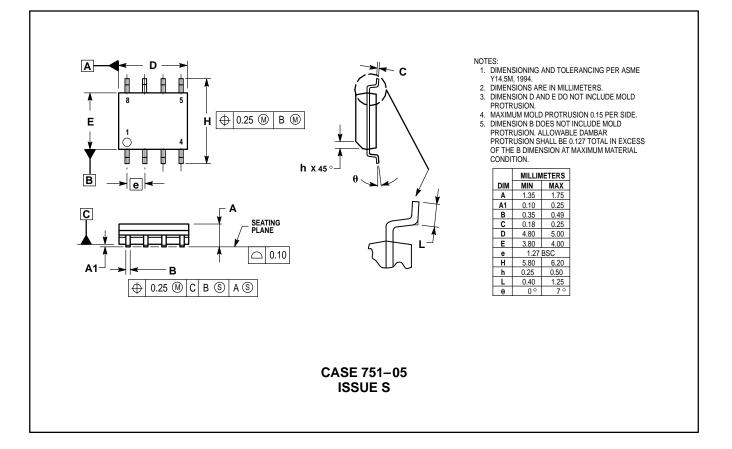


Figure 4. Transmit to Receive Isolation in TX Mode





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