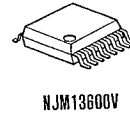
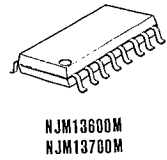
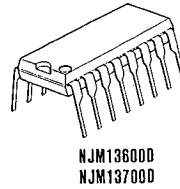


DUAL OPERATIONAL TRANSCONDUCTANCE AMPLIFIER

■ GENERAL DESCRIPTION

The NJM13600/13700 consist of two current controlled transconductance amplifiers each with differential inputs and a push pull output. The two amplifiers share common supplies but otherwise operate independently. Linearizing diodes are provided at the inputs to reduce distortion and allow higher input levels. The results is a 10 dB signal-to-noise improvement referenced to 0.5 percent THD. Controlled impedance buffers are provided which are especially designed to complement the dynamic range of the amplifiers.

■ PACKAGE OUTLINE

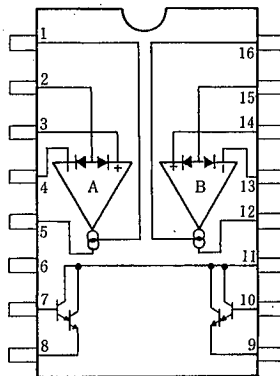


■ FEATURES

- Package Outline
- Bipolar Technology

DIP16, DMP16, (SSOP16)

■ PIN CONFIGURATION

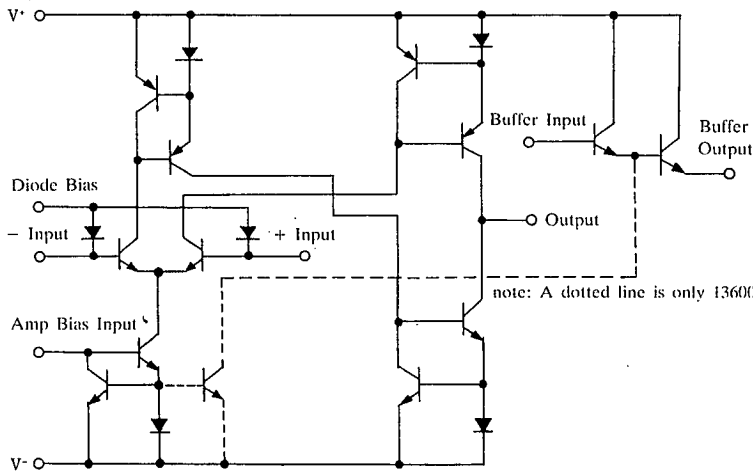


NJM13600D, NJM13600M, NJM13600V  
NJM13700D, NJM13700M,

PIN FUNCTION

- |                     |                      |
|---------------------|----------------------|
| 1. Amp Bias Input A | 9. Buffer Output B   |
| 2. Diode Bias A     | 10. Buffer Input B   |
| 3. + Input          | 11. V <sup>+</sup>   |
| 4. - Input          | 12. Output B         |
| 5. Output A         | 13. - Input B        |
| 6. V <sup>-</sup>   | 14. + Input B        |
| 7. Buffer Input A   | 15. Diode Bias B     |
| 8. Buffer Output A  | 16. Amp Bias Input B |

■ EQUIVALENT CIRCUIT



4

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V <sup>+</sup> /V <sup>-</sup>	36 or ±18	V
Differential Input Voltage	V <sub>ID</sub>	±5	V
Diode Bias Current	I <sub>D</sub>	2	mA
Amp Bias Current	I <sub>ABC</sub>	2	mA
Buffer Output Current	I <sub>O</sub>	20	mA
Power Dissipation	P <sub>D</sub>	(DIP16) 570	mW
		(DMP16) 700	mW
DC Input Voltage	V <sub>IN</sub>	V <sup>+</sup> ~V <sup>-</sup>	
Operating Temperature Range	T <sub>opr</sub>	-40~+85	°C
Storage Temperature Range	T <sub>slg</sub>	-40~+125	°C

(note) At on a ceramic PCB (10×20×0.635mm)

■ ELECTRICAL CHARACTERISTICS

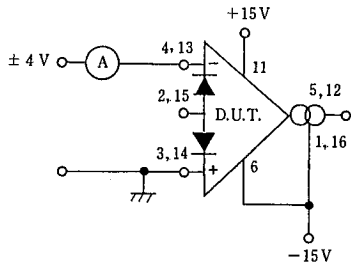
(V<sup>+</sup>/V<sup>-</sup>=±15V, T<sub>a</sub>=25°C, I<sub>ABC</sub>=500μA)

PARAMETER	SYMBOL	TEST CONDITION	13600			13700			UNIT
			MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
Input Offset Voltage (V <sub>OS</sub> )	V <sub>IO</sub>	I <sub>ABC</sub> =5μA Diode Bias Current, I <sub>D</sub> =500μA	—	0.4	5	—	0.4	4	mV
Input Offset Voltage			—	0.3	5	—	0.3	4	mV
V <sub>OS</sub> Including Diodes			—	0.5	5	—	0.5	5	mV
Input Offset Change		5μA ≤ I <sub>ABC</sub> ≤ 500μA	—	0.1	—	—	0.1	3	mV
Input Bias Current	I <sub>B</sub>	(-20~+75°C)	—	0.4	5	—	0.4	5	μA
Input Bias Current			—	1	8	—	1	8	μA
Forward Transconductance(gm)	gm	(-20~+75°C)	6,700	9,600	13,000	6,700	9,600	13,000	μΩ
			5,400	—	—	5,400	—	—	μΩ
gm Tracking		R <sub>L</sub> =0, I <sub>ABC</sub> =5μA	—	0.3	—	—	0.3	—	dB
Peak Output Current	I <sub>OP</sub>	R <sub>L</sub> =0, I <sub>ABC</sub> =5μA	—	5	—	—	0	—	μA
Peak Output Current		R <sub>L</sub> =0, I <sub>ABC</sub> =500μA	350	500	650	350	500	650	μA
Peak Output Current		R <sub>L</sub> =0, -20~+75°C	300	—	—	300	—	—	μA
Peak Output Voltage Positive	V <sub>OP</sub>	R <sub>L</sub> =∞, 5μA ≤ I <sub>ABC</sub> ≤ 500μA	+12	+14.2	—	+12	+14.2	—	V
Peak Output Voltage Negative		R <sub>L</sub> =∞, 5μA ≤ I <sub>ABC</sub> ≤ 500μA	-12	-14.4	—	-12	-14.4	—	V
Operating Current	I <sub>CC</sub>	I <sub>ABC</sub> =500μA, two circuit	—	2.6	—	—	2.6	—	mA
V <sub>OS</sub> Sensitivity Positive	SVR	ΔV <sub>OS</sub> /ΔV <sup>+</sup>	76.5	94	—	76.5	94	—	dB
V <sub>OS</sub> Sensitivity Negative		ΔV <sub>OS</sub> /ΔV <sup>-</sup>	76.5	94	—	76.5	94	—	dB
Input Offset Current	I <sub>IO</sub>		—	0.1	0.6	—	0.1	0.6	μA
CMRR	CMR		80	110	—	80	110	—	dB
Common Mode Range	V <sub>ICM</sub>		±12	±13.5	—	±12	±13.5	—	V
Cross talk	CT	20Hz < f < 20kHz (note 2)	—	-100	—	—	-100	—	dB
Differential Input Current	I <sub>ID</sub>	I <sub>ABC</sub> =0, Input=±4V	—	0.02	100	—	0.02	100	nA
Leakage Current	I <sub>LEAK</sub>	I <sub>ABC</sub> =0 (Refer to Test Circuit)	—	0.2	100	—	0.2	100	nA
Input Resistance	R <sub>IN</sub>		10	26	—	10	26	—	kΩ
Open Loop Bandwidth			—	2	—	—	2	—	MHz
Slew Rate	SR		—	50	—	—	50	—	V/μS
Buffer Input Current		(note 2)	—	0.4	5	—	0.4	5	μA
Peak Buffer Output Voltage		(note 2)	10	—	—	10	—	—	V

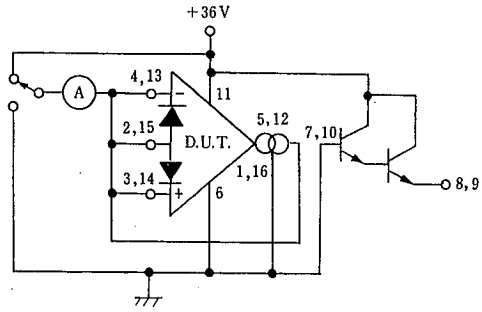
(note 1) Open unless otherwise specified. The inputs to the buffers are grounded and outputs are open.

(note 2) R<sub>OUT</sub>=5kΩ connected from the buffer output to V<sup>-</sup> and the input of buffer is connected to the transconductance amplifier output.  
I<sub>ABC</sub>=500μA

## ■ TEST CIRCUIT



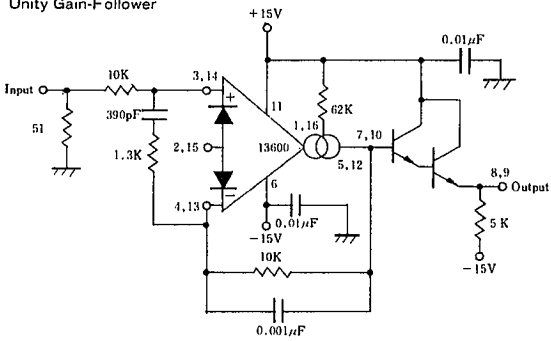
Differential Input Current



Leakage Current

## ■ TYPICAL APPLICATIONS

### Unity Gain-Follower

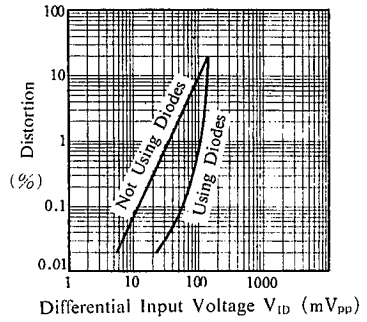


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■ TYPICAL CHARACTERISTICS

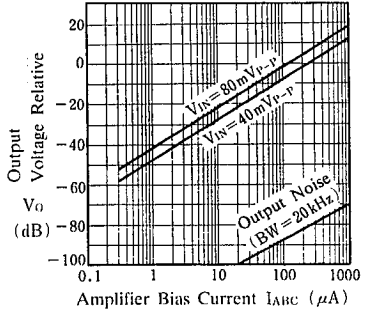
**Distortion vs. Differential Input Voltage**

( $V^+/V^- = \pm 15V$ ,  $R_L = 10k\Omega$ ,  $I_{ABC} = 1mA$ ,  $T_a = 25^\circ C$ )



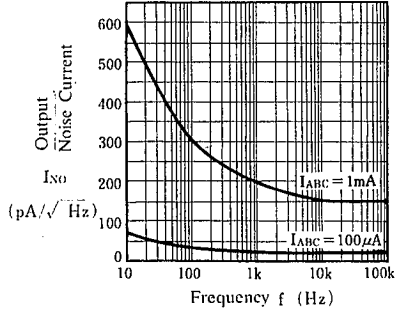
**Voltage vs. Amplifier Bias Current**

( $V^+/V^- = \pm 15V$ ,  $R_L = 10k\Omega$ ,  $T_a = 25^\circ C$ )

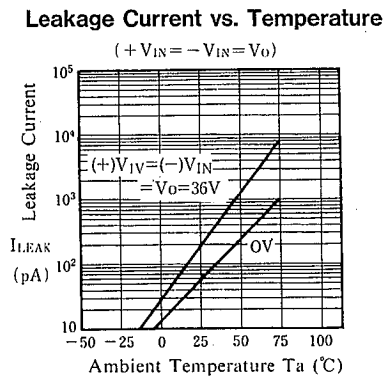
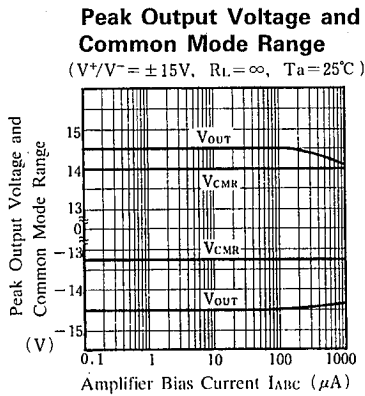
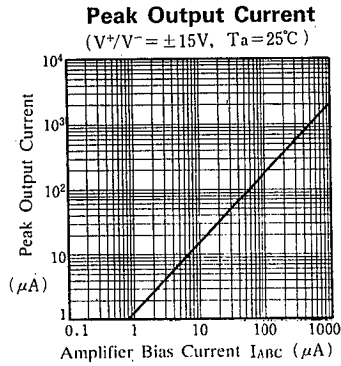
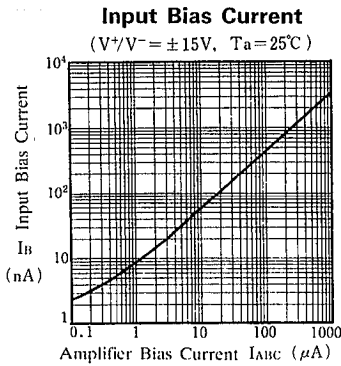
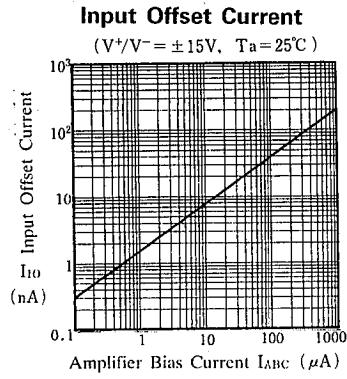
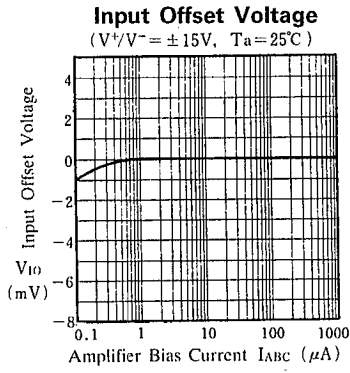


**Output Noise Current vs. Frequency**

( $T_a = 25^\circ C$ )

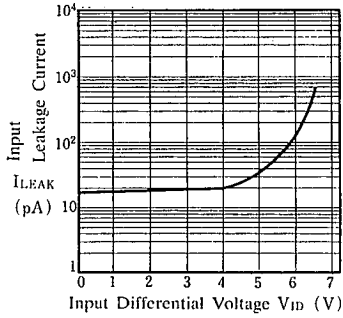


## TYPICAL CHARACTERISTICS

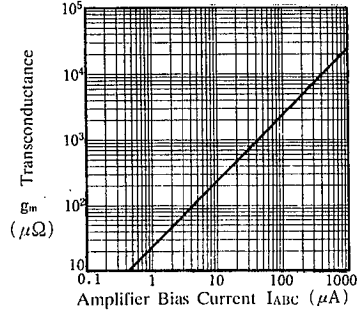


■ TYPICAL CHARACTERISTICS

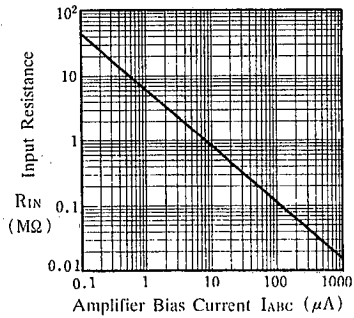
**Input Leakage Current**  
( $T_a = 25^\circ\text{C}$ )



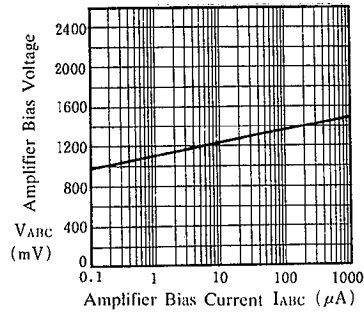
**Transconductance**  
( $V^+/V^- = \pm 15\text{V}$ , Pin2.15 Open,  $T_a = 25^\circ\text{C}$ )



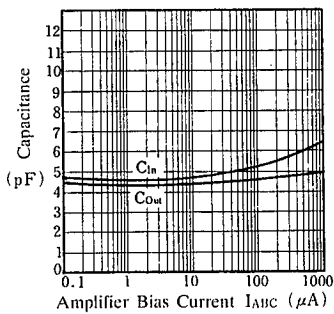
**Input Resistance**  
(Pin2.15 Open,  $T_a = 25^\circ\text{C}$ )



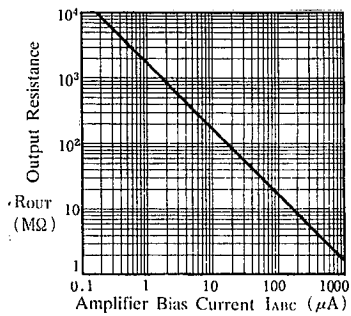
**Amplifier Bias Voltage vs. Amplifier Bias Current**  
( $T_a = 25^\circ\text{C}$ )



**Input and Output Capacitance**  
( $V^+/V^- = \pm 15\text{V}$ ,  $T_a = 25^\circ\text{C}$ )



**Output Resistance**  
( $T_a = 25^\circ\text{C}$ )



## MEMO

**[CAUTION]**

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