

NSBA114EDXV6T1, NSBA114EDXV6T5 SERIES

Preferred Devices

Dual Bias Resistor Transistors

PNP Silicon Surface Mount Transistors with Monolithic Bias Resistor Network

The BRT (Bias Resistor Transistor) contains a single transistor with a monolithic bias network consisting of two resistors; a series base resistor and a base-emitter resistor. These digital transistors are designed to replace a single device and its external resistor bias network. The BRT eliminates these individual components by integrating them into a single device. In the NSBA114EDXV6T1 series, two BRT devices are housed in the SOT-563 package which is ideal for low-power surface mount applications where board space is at a premium.

Features

- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- These are Pb-Free Devices

MAXIMUM RATINGS

($T_A = 25^\circ\text{C}$ unless otherwise noted, common for Q_1 and Q_2)

Rating	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-50	Vdc
Collector-Emitter Voltage	V_{CEO}	-50	Vdc
Collector Current	I_C	-100	mA

THERMAL CHARACTERISTICS

Characteristic (One Junction Heated)	Symbol	Max	Unit
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C (Note 1)	P_D	357 2.9	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	350	$^\circ\text{C}/\text{W}$
Characteristic (Both Junctions Heated)	Symbol	Max	Unit
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C (Note 1)	P_D	500 4.0	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	250	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

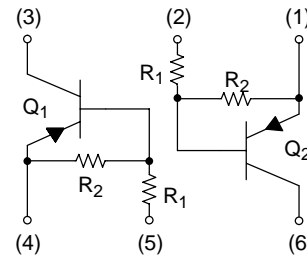
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-4 @ Minimum Pad



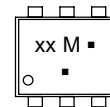
ON Semiconductor®

<http://onsemi.com>



SOT-563
CASE 463A
PLASTIC
STYLE 1

MARKING DIAGRAM



xx = Device Code
(Refer to page 2)
M = Date Code

▪ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
NSBA1xxxDXV6T1	SOT-563*	4000/Tape & Reel
NSBA1xxxDXV6T1G	SOT-563*	4000/Tape & Reel
NSBA1xxxDXV6T5	SOT-563*	8000/Tape & Reel
NSBA1xxxDXV6T5G	SOT-563*	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*This package is inherently Pb-Free.

DEVICE MARKING INFORMATION

See specific marking information in the device marking table on page 2 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

NSBA114EDXV6T1, NSBA114EDXV6T5 SERIES

DEVICE MARKING AND RESISTOR VALUES

Device*	Package	Marking	R1 (kΩ)	R2 (kΩ)
NSBA114EDXV6T1 / T5	SOT-563	0A	10	10
NSBA124EDXV6T1 / T5	SOT-563	0B	22	22
NSBA144EDXV6T1 / T5	SOT-563	0C	47	47
NSBA114YDXV6T1 / T5	SOT-563	0D	10	47
NSBA114TDXV6T1 / T5 (Note 2)	SOT-563	0E	10	∞
NSBA143TDXV6T1 / T5 (Note 2)	SOT-563	0F	4.7	∞
NSBA113EDXV6T1 / T5 (Note 2)	SOT-563	0G	1.0	1.0
NSBA123EDXV6T1 / T5 (Note 2)	SOT-563	0H	2.2	2.2
NSBA143EDXV6T1 / T5 (Note 2)	SOT-563	0J	4.7	4.7
NSBA143ZDXV6T1 / T5 (Note 2)	SOT-563	0K	4.7	47
NSBA124XDXV6T1 / T5 (Note 2)	SOT-563	0L	22	47
NSBA123JDXV6T1 / T5 (Note 2)	SOT-563	0M	2.2	47
NSBA115EDXV6T1 / T5 (Note 2)	SOT-563	0N	100	100
NSBA144WDXV6T1 (Note 2)	SOT-563	0P	47	22

*The "G" suffix indicates Pb-Free package available. Refer to Ordering Information Table on page 1.

2. New resistor combinations. Updated curves to follow in subsequent data sheets.

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted, common for Q_1 and Q_2)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector-Base Cutoff Current ($V_{CB} = -50\text{ V}$, $I_E = 0$)	I_{CBO}	-	-	-100	nAdc
Collector-Emitter Cutoff Current ($V_{CE} = -50\text{ V}$, $I_B = 0$)	I_{CEO}	-	-	-500	nAdc
Emitter-Base Cutoff Current ($V_{EB} = -6.0\text{ V}$, $I_C = 0$)	I_{EBO}	-	-	-0.5	mAdc
	NSBA114EDXV6T1	-	-	-0.2	
	NSBA124EDXV6T1	-	-	-0.1	
	NSBA144EDXV6T1	-	-	-0.2	
	NSBA114YDXV6T1	-	-	-0.9	
	NSBA114TDXV6T1	-	-	-1.9	
	NSBA143TDXV6T1	-	-	-4.3	
	NSBA113EDXV6T1	-	-	-2.3	
	NSBA123EDXV6T1	-	-	-1.5	
	NSBA143EDXV6T1	-	-	-0.18	
	NSBA143ZDXV6T1	-	-	-0.13	
	NSBA124XDXV6T1	-	-	-0.2	
	NSBA123JDXV6T1	-	-	-0.05	
	NSBA115EDXV6T1	-	-	-0.13	
	NSBA144WDXV6T1	-	-		
Collector-Base Breakdown Voltage ($I_C = -10\ \mu\text{A}$, $I_E = 0$)	$V_{(BR)CBO}$	-50	-	-	Vdc
Collector-Emitter Breakdown Voltage (Note 3) ($I_C = -2.0\text{ mA}$, $I_B = 0$)	$V_{(BR)CEO}$	-50	-	-	Vdc
ON CHARACTERISTICS (Note 3)					
Collector-Emitter Saturation Voltage ($I_C = -10\text{ mA}$, $I_E = -0.3\text{ mA}$) ($I_C = -10\text{ mA}$, $I_B = -5\text{ mA}$) NSBA113EDXV6T1/NSBA123EDXV6T1 ($I_C = -10\text{ mA}$, $I_B = -1\text{ mA}$) NSBA114TDXV6T1/NSBA143TDXV6T1 NSBA143EDXV6T1/NSBA143ZDXV6T1/NSBA124XDXV6T1	$V_{CE(sat)}$	-	-	-0.25	Vdc

3. Pulse Test: Pulse Width < 300 μs , Duty Cycle < 2.0%

NSBA114EDXV6T1, NSBA114EDXV6T5 SERIES

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted, common for Q₁ and Q₂) (continued)

Characteristic	Symbol	Min	Typ	Max	Unit	
ON CHARACTERISTICS (Note 3) (continued)						
DC Current Gain (V _{CE} = -10 V, I _C = -5.0 mA)	NSBA114EDXV6T1 NSBA124EDXV6T1 NSBA144EDXV6T1 NSBA114YDXV6T1 NSBA114TDXV6T1 NSBA143TDXV6T1 NSBA113EDXV6T1 NSBA123EDXV6T1 NSBA143EDXV6T1 NSBA143ZDXV6T1 NSBA124XDXV6T1 NSBA123JDXV6T1 NSBA115EDXV6T1 NSBA144WDXV6T1	h _{FE}	35 60 80 80 160 160 3.0 8.0 15 80 80 80 80 80	60 100 140 140 250 250 5.0 15 27 140 130 140 130 140	- - - - - - - - - - - - - -	
Output Voltage (on) (V _{CC} = -5.0 V, V _B = -2.5 V, R _L = 1.0 kΩ)	NSBA114EDXV6T1 NSBA124EDXV6T1 NSBA114YDXV6T1 NSBA114TDXV6T1 NSBA143TDXV6T1 NSBA113EDXV6T1 NSBA123EDXV6T1 NSBA143EDXV6T1 NSBA143ZDXV6T1 NSBA124XDXV6T1 NSBA123JDXV6T1 (V _{CC} = -5.0 V, V _B = -3.5 V, R _L = 1.0 kΩ) (V _{CC} = -5.0 V, V _B = -5.5 V, R _L = 1.0 kΩ) (V _{CC} = -5.0 V, V _B = -4.0 V, R _L = 1.0 kΩ)	V _{OL}	- - - - - - - - - - - - - - -	- - - - - - - - - - - - - - -	-0.2 -0.2 -0.2 -0.2 -0.2 -0.2 -0.2 -0.2 -0.2 -0.2 -0.2 -0.2 -0.2 -0.2 -0.2	Vdc
Output Voltage (off) (V _{CC} = -5.0 V, V _B = -0.5 V, R _L = 1.0 kΩ) (V _{CC} = -5.0 V, V _B = -0.05 V, R _L = 1.0 kΩ) (V _{CC} = -5.0 V, V _B = -0.25 V, R _L = 1.0 kΩ)	NSBA113EDXV6T1 NSBA114TDXV6T1 NSBA143TDXV6T1 NSBA123EDXV6T1 NSBA143ZDXV6T1	V _{OH}	-4.9	-	-	Vdc
Input Resistor	NSBA114EDXV6T1 NSBA124EDXV6T1 NSBA144EDXV6T1 NSBA114YDXV6T1 NSBA114TDXV6T1 NSBA143TDXV6T1 NSBA113EDXV6T1 NSBA123EDXV6T1 NSBA143EDXV6T1 NSBA143ZDXV6T1 NSBA124XDXV6T1 NSBA123JDXV6T1 NSBA115EDXV6T1 NSBA144WDXV6T1	R ₁	7.0 15.4 32.9 7.0 7.0 3.3 0.7 1.5 3.3 3.3 15.4 1.54 70 32.9	10 22 47 10 10 4.7 1.0 2.2 4.7 4.7 22 2.2 100 47	13 28.6 61.1 13 13 6.1 1.3 2.9 6.1 6.1 28.6 2.86 130 61.1	k Ω
Resistor Ratio	NSBA114EDXV6T1/NSBA124EDXV6T1/ NSBA144EDXV6T1/NSBA115EDXV6T1 NSBA114YDXV6T1 NSBA114TDXV6T1/NSBA143TDXV6T1 NSBA113EDXV6T1/NSBA123EDXV6T1/NSBA143EDXV6T1 NSBA143ZDXV6T1 NSBA124XDXV6T1 NSBA123JDXV6T1 NSBA144WDXV6T1	R ₁ /R ₂	0.8 0.17 - 0.8 0.055 0.38 0.038 1.7	1.0 0.21 - 1.0 0.1 0.47 0.047 2.1	1.2 0.25 - 1.2 0.185 0.56 0.056 2.6	

3. Pulse Test: Pulse Width < 300 μs, Duty Cycle < 2.0%

NSBA114EDXV6T1, NSBA114EDXV6T5 SERIES

ALL NSBA114EDXV6T1 SERIES DEVICES

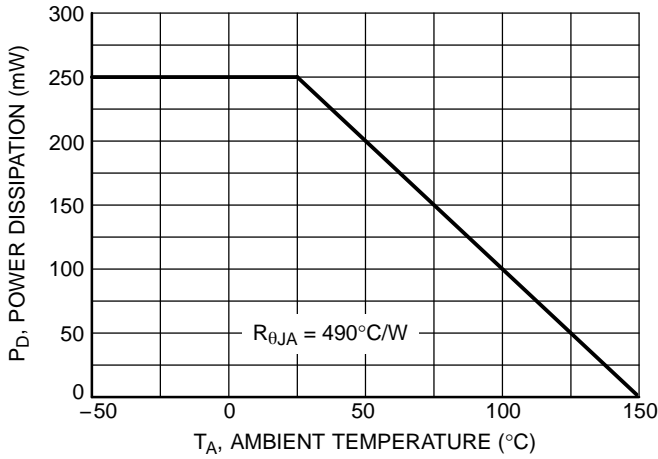


Figure 1. Derating Curve – ALL DEVICES

TYPICAL ELECTRICAL CHARACTERISTICS — NSBA114EDXV6T1

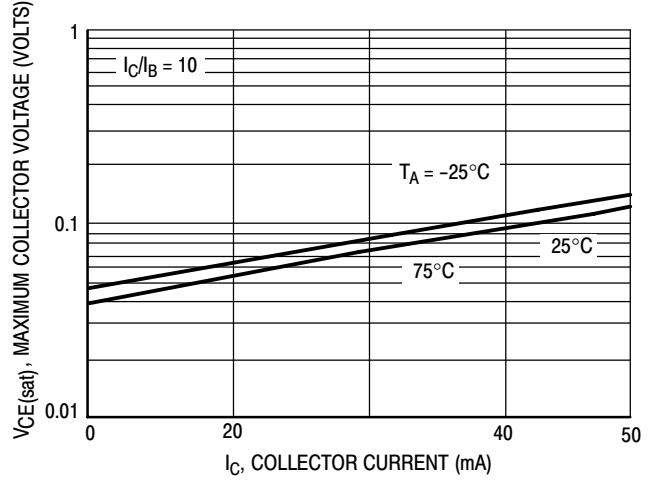


Figure 2. $V_{CE(sat)}$ versus I_C

TYPICAL ELECTRICAL CHARACTERISTICS — NSBA114EDXV6T1

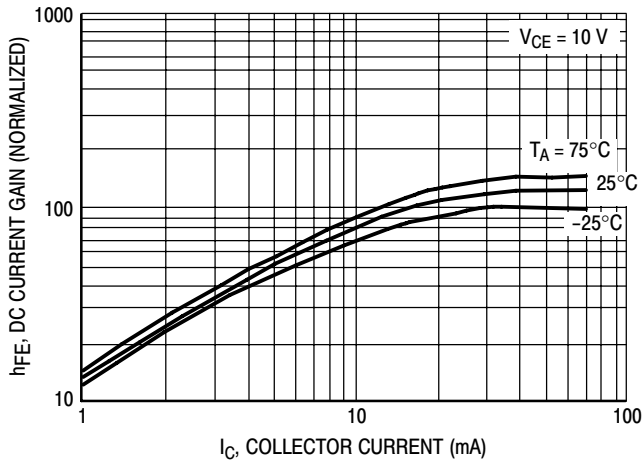


Figure 3. DC Current Gain

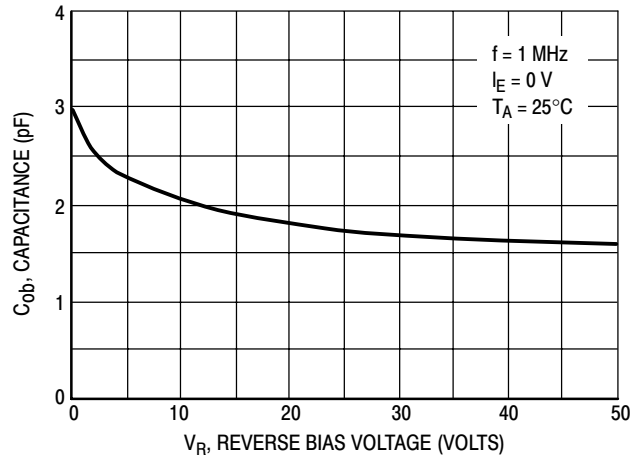


Figure 4. Output Capacitance

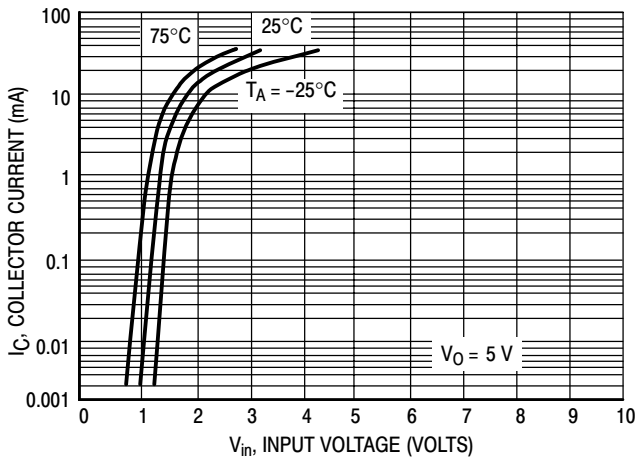


Figure 5. Output Current versus Input Voltage

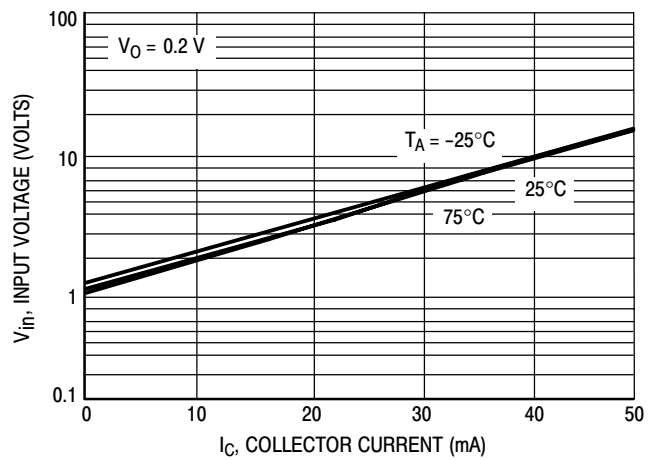


Figure 6. Input Voltage versus Output Current

NSBA114EDXV6T1, NSBA114EDXV6T5 SERIES

TYPICAL ELECTRICAL CHARACTERISTICS — NSBA124EDXV6T1

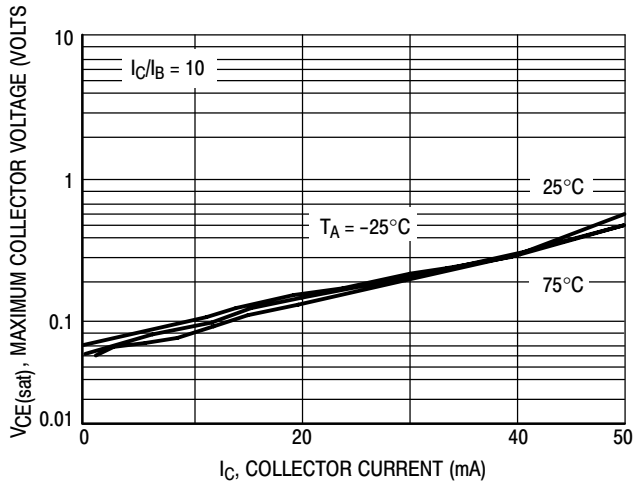


Figure 7. $V_{CE(sat)}$ versus I_C

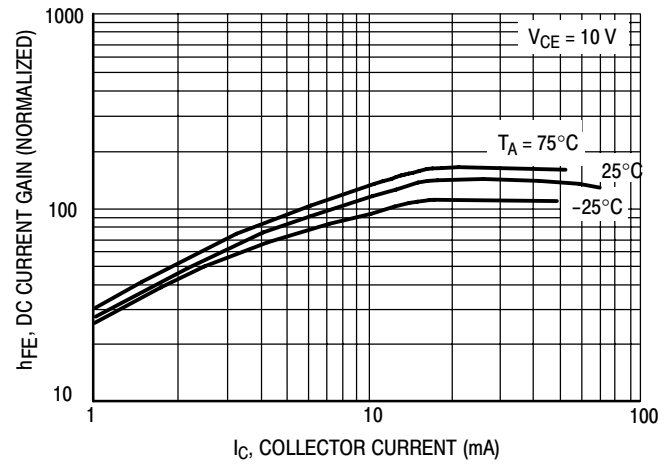


Figure 8. DC Current Gain

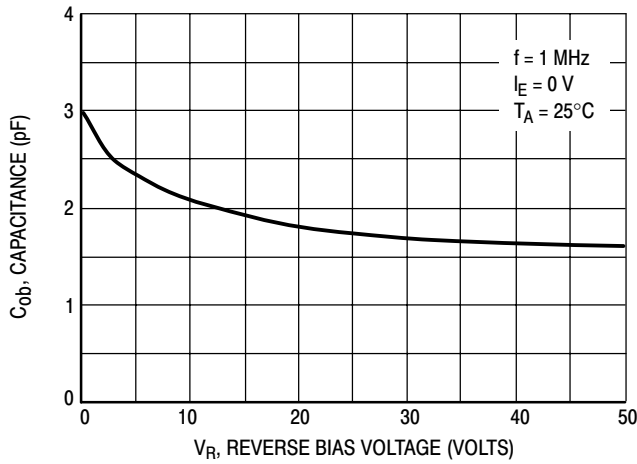


Figure 9. Output Capacitance

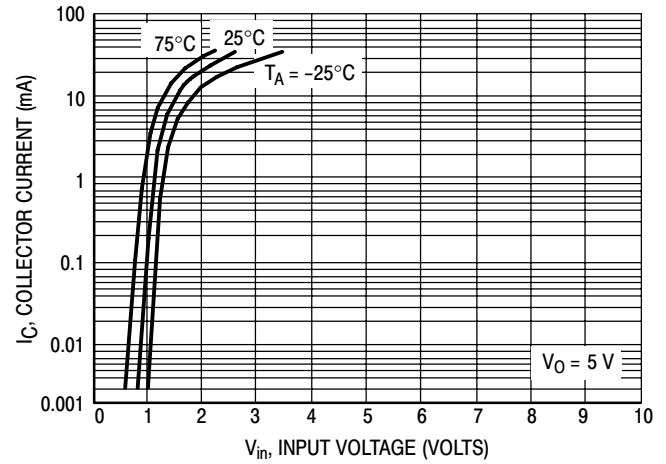


Figure 10. Output Current versus Input Voltage

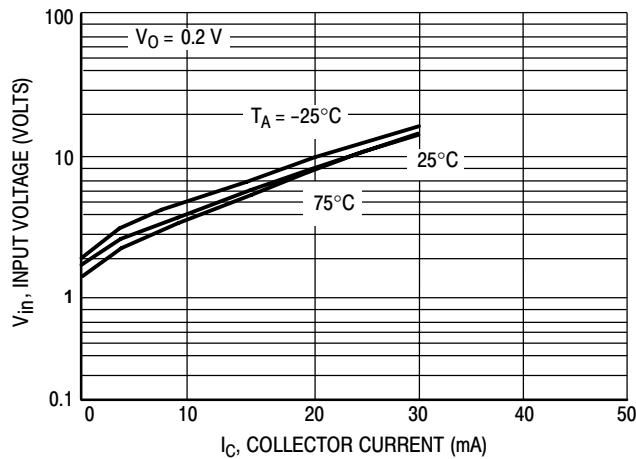


Figure 11. Input Voltage versus Output Current

NSBA114EDXV6T1, NSBA114EDXV6T5 SERIES

TYPICAL ELECTRICAL CHARACTERISTICS — NSBA114EDXV6T1

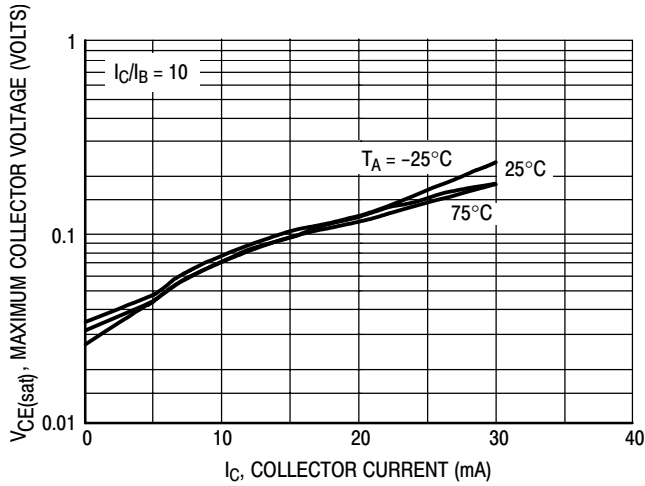


Figure 12. $V_{CE(sat)}$ versus I_C

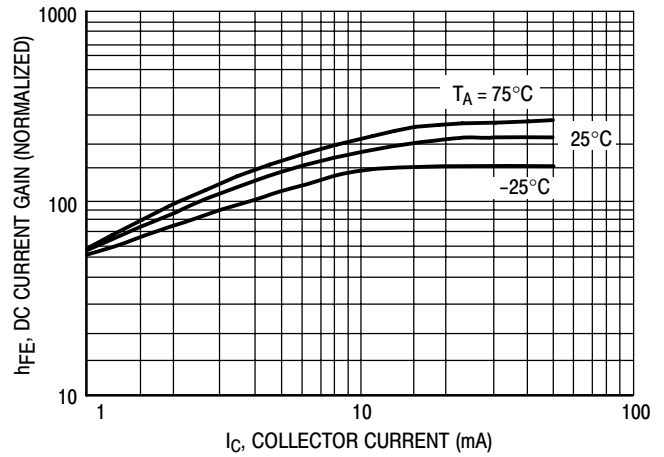


Figure 13. DC Current Gain

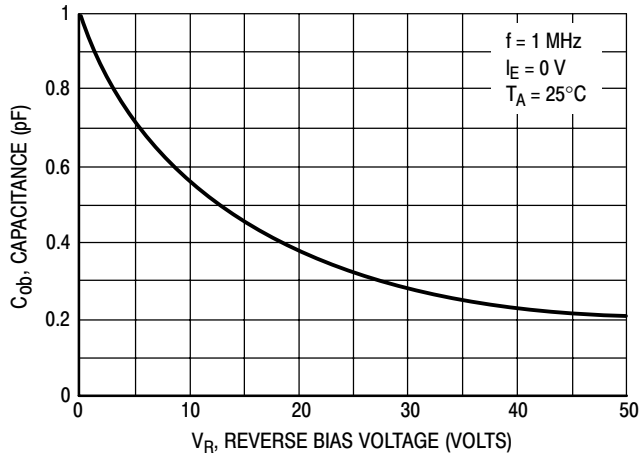


Figure 14. Output Capacitance

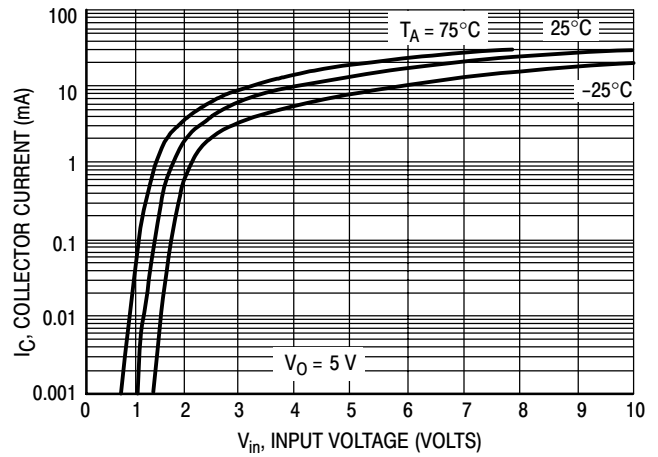


Figure 15. Output Current versus Input Voltage

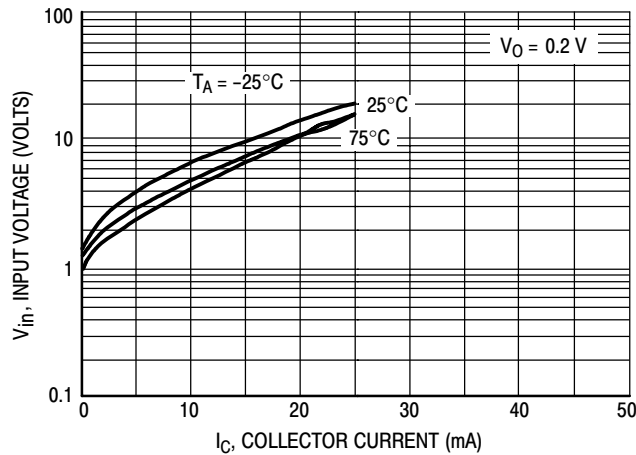


Figure 16. Input Voltage versus Output Current

NSBA114EDXV6T1, NSBA114EDXV6T5 SERIES

TYPICAL ELECTRICAL CHARACTERISTICS — NSBA114YDXV6T1

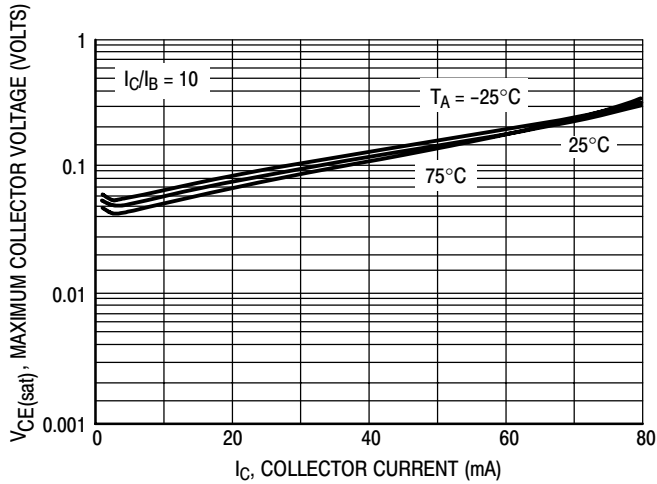


Figure 17. $V_{CE(sat)}$ versus I_C

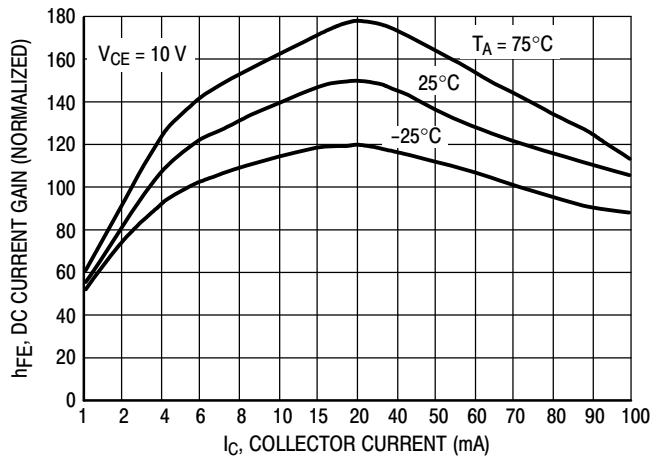


Figure 18. DC Current Gain

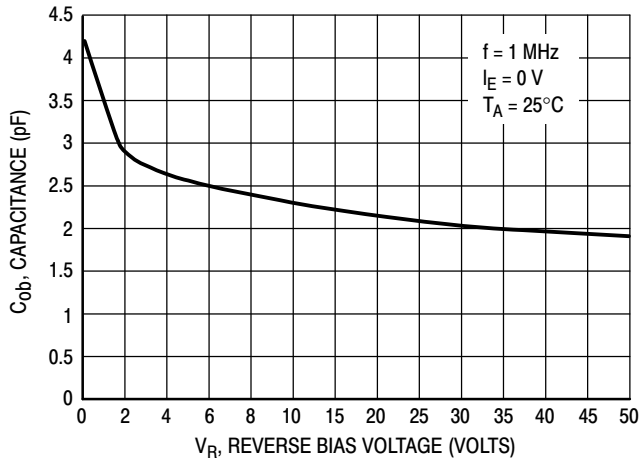


Figure 19. Output Capacitance

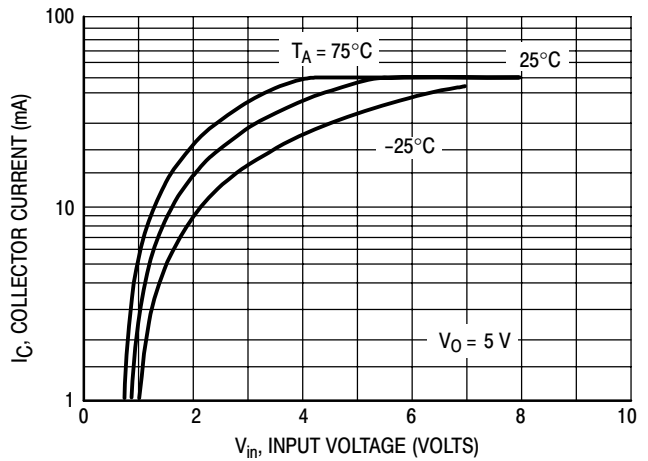


Figure 20. Output Current versus Input Voltage

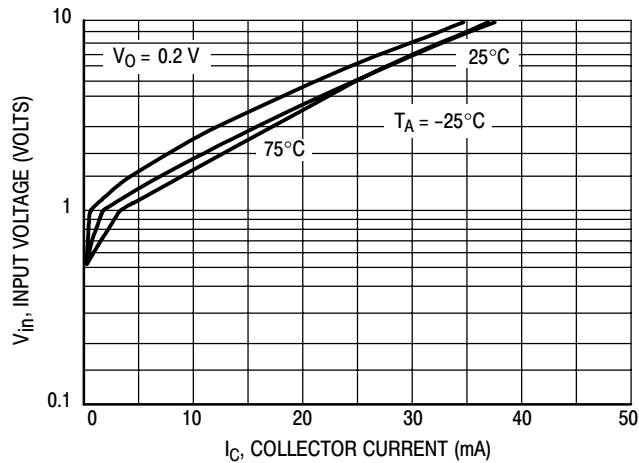


Figure 21. Input Voltage versus Output Current

NSBA114EDXV6T1, NSBA114EDXV6T5 SERIES

TYPICAL ELECTRICAL CHARACTERISTICS — NSBA114TDXV6T1

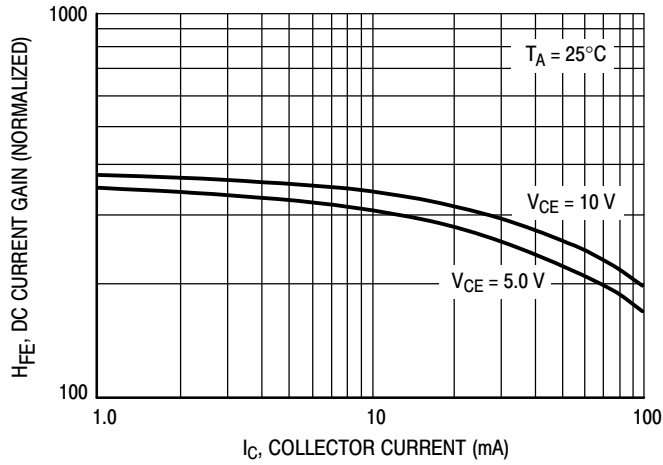


Figure 22. DC Current Gain

TYPICAL ELECTRICAL CHARACTERISTICS — NSBA143TDXV6T1

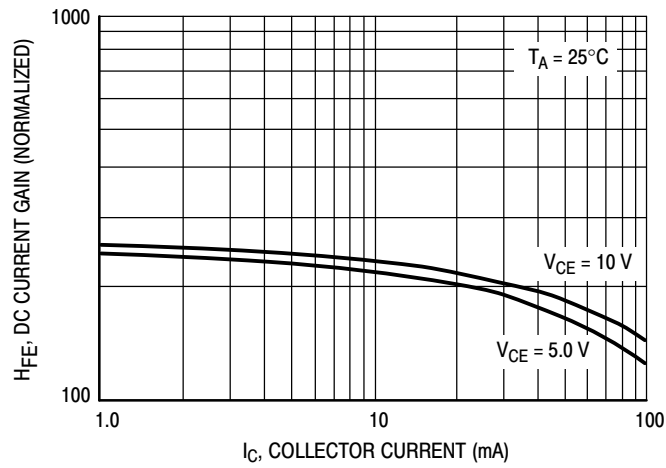


Figure 23. DC Current Gain

NSBA114EDXV6T1, NSBA114EDXV6T5 SERIES

TYPICAL ELECTRICAL CHARACTERISTICS — NSBA115EDXV6T1

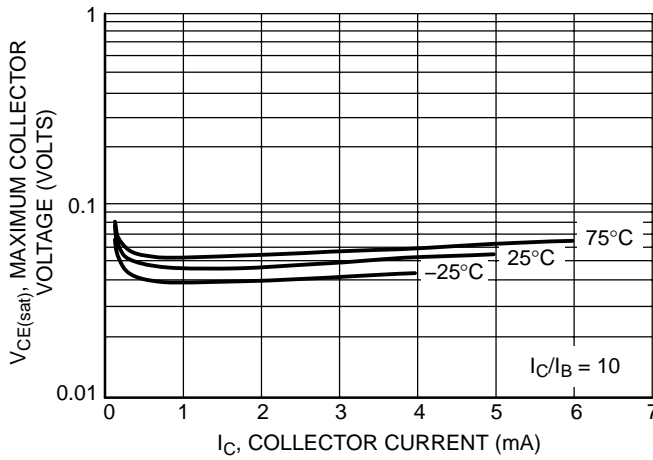


Figure 24. Maximum Collector Voltage versus Collector Current

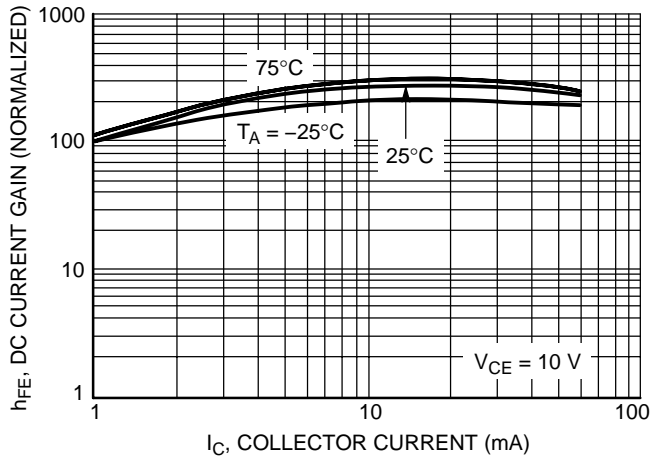


Figure 25. DC Current Gain

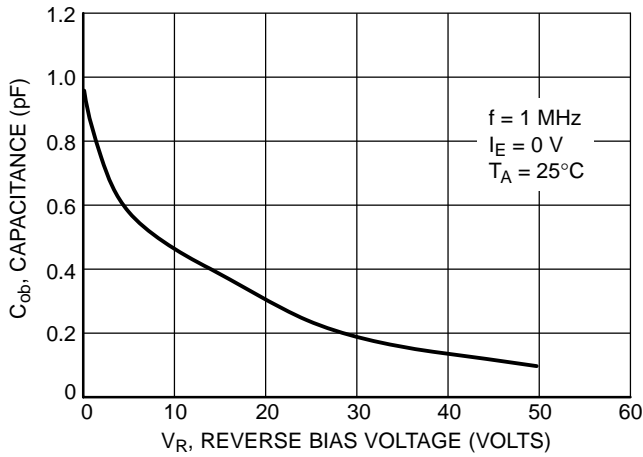


Figure 26. Output Capacitance

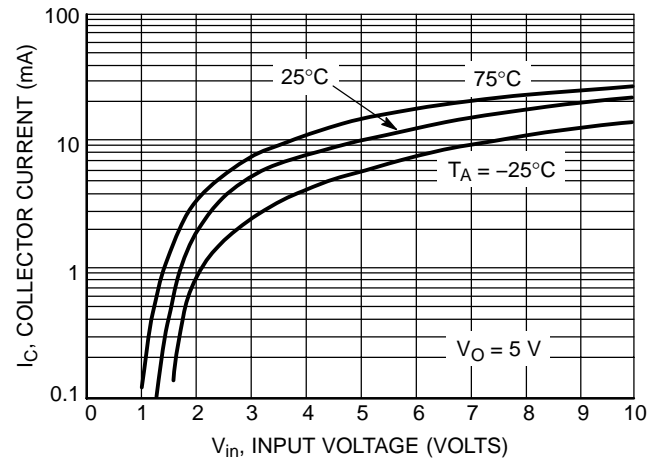


Figure 27. Output Current versus Input Voltage

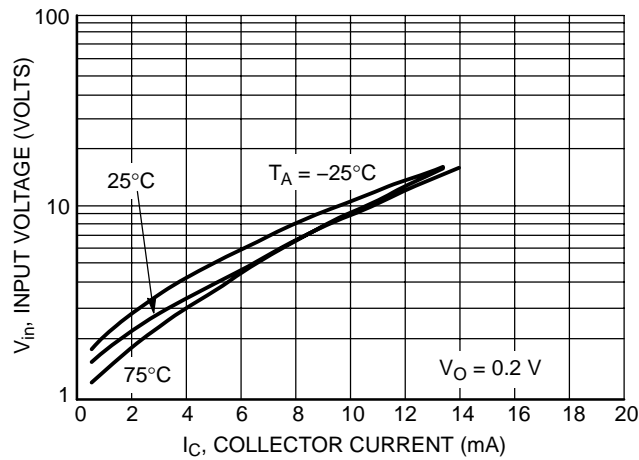


Figure 28. Input Voltage versus Output Current

NSBA114EDXV6T1, NSBA114EDXV6T5 SERIES

TYPICAL ELECTRICAL CHARACTERISTICS — NSBA144WDXV6T1

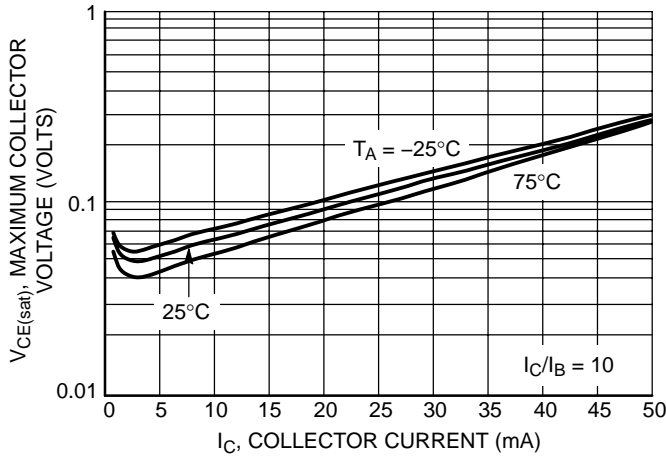


Figure 29. Maximum Collector Voltage versus Collector Current

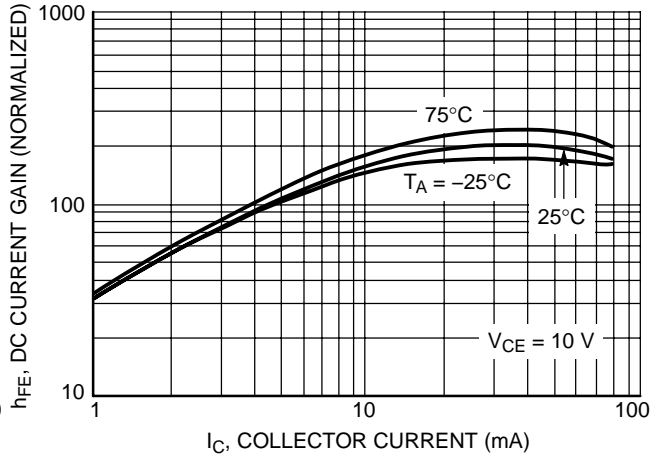


Figure 30. DC Current Gain

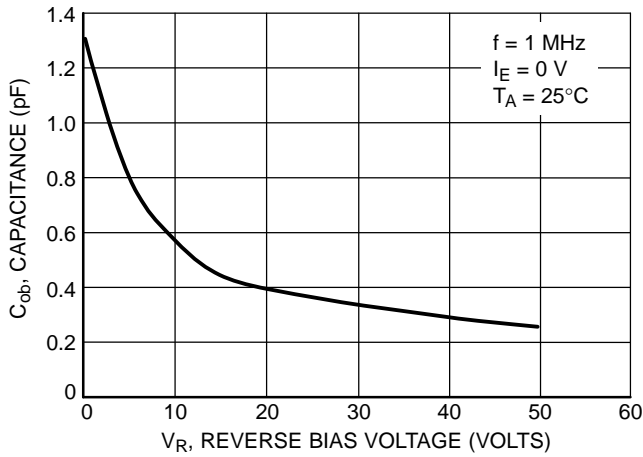


Figure 31. Output Capacitance

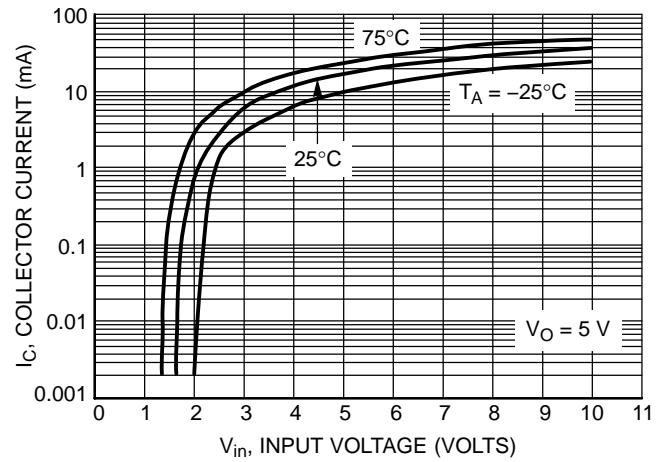


Figure 32. Output Current versus Input Voltage

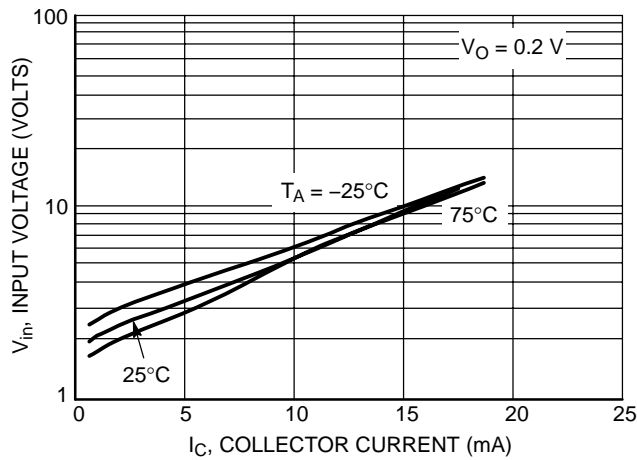
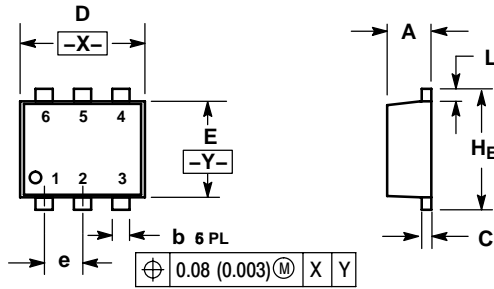


Figure 33. Input Voltage versus Output Current

NSBA114EDXV6T1, NSBA114EDXV6T5 SERIES

PACKAGE DIMENSIONS

SOT-563, 6 LEAD
CASE 463A-01
ISSUE F



NOTES:

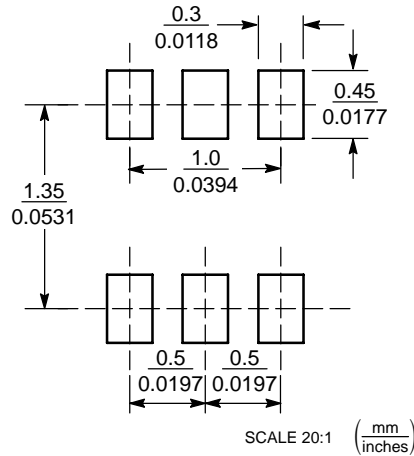
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.50	0.55	0.60	0.020	0.021	0.023
b	0.17	0.22	0.27	0.007	0.009	0.011
C	0.08	0.12	0.18	0.003	0.005	0.007
D	1.50	1.60	1.70	0.059	0.062	0.066
E	1.10	1.20	1.30	0.043	0.047	0.051
e	0.5 BSC			0.02 BSC		
L	0.10	0.20	0.30	0.004	0.008	0.012
HE	1.50	1.60	1.70	0.059	0.062	0.066

STYLE 1:

- PIN 1. EMITTER 1
 PIN 2. BASE 1
 PIN 3. COLLECTOR 2
 PIN 4. EMITTER 2
 PIN 5. BASE 2
 PIN 6. COLLECTOR 1

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
 Literature Distribution Center for ON Semiconductor
 P.O. Box 61312, Phoenix, Arizona 85082-1312 USA
 Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada
 Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada
 Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
 USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center
 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051
 Phone: 81-3-5773-3850

ON Semiconductor Website: <http://onsemi.com>

Order Literature: <http://www.onsemi.com/litorder>

For additional information, please contact your local Sales Representative.