





DC-DC CONVERTERS POLA Non-isolated

- 6 A output current
- 5 V input voltage
- Wide-output voltage adjust (0.8 Vdc to 3.6 Vdc)
- Auto-track[™] sequencing^{*}
- Pre-bias start-up capability
- Efficiencies up to 95%
- Output ON/OFF inhibit
- Output voltage sense
- Point-of-Load-Alliance (POLA) compatible
- Available RoHS compliant

The PTH05050 is a next generation series of non-isolated dc-dc converters offering some of the most advanced POL features available in the industry. The primary new feature provides for sequencing between multiple modules, a function, which is becoming a necessity for powering advanced silicon including DSP's, FPGA's and ASIC's requiring controlled power-up and power-down. Other industry leading features include pre-bias start-up capability and efficiencies up to 95%. The PTH05050 has an input voltage of 4.5 Vdc to 5.5 Vdc and offers a wide 0.8 Vdc to 3.6 Vdc output voltage range with up to 6 A output current, which allows for maximum design flexibility and a pathway for future upgrades.

All specifications are typical at nominal input, full load at 25 °C unless otherwise stated C_{in} = 100 μ F, C_{out} = 0 μ F

OUTPUT SPECIFICATIONS

Voltage adjustability	(See Note 4)	0.8-3.6 Vdc
Setpoint accuracy		±2.0% Vo
Line regulation		±10 mV typ.
Load regulation		±12 mV typ.
Total regulation		±3.0% Vo
Minimum load		0 A
Ripple and noise	20 MHz bandwidth	20 mV pk-pk
Temperature co-efficient	-40 °C to +85 °C	±0.5% Vo
Transient response (See Note 5)	Overshoot	70 µs recovery time /undershoot 100 mV

INPUT SPECIFICATIONS

Input voltage range	(See Note 3)	4.5-5.5 Vdc
Input current	No load	10 mA typ.
Remote ON/OFF	(See Note 1)	Positive logic
Start-up time		1 V/ms
Undervoltage lockout		3.7-4.3 Vdc typ.
Track input voltage	Pin 2 (See Note 6, 7)	±0.3 Vin

International Safety Standard Approvals



UL/cUL CAN/CSA-C22.2 No. 60950-1-03/UL 60950-1, File No. E174104

TÜV Product Service (EN60950) Certificate No. B 04 06 38572 044 CB Report and Certificate to IEC60950, Certificate No. US/8292/UL



NEW Product





SPECIFICATIONS

EMC CHARACTERISTICS

EN61000-4-2, IEC801-2 EN61000-4-6 EN61000-4-3

GENERAL SPECIFICATIONS

Efficiency	(See Efficiency Table	e) 95% max.		
Insulation voltage		Non-isolated		
Switching frequency		550 kHz to 650 kHz		
Approvals and standards		EN60950 UL/cUL60950		
Material flammability		UL94V-0		
Dimensions	(=) ==	0 x 12.57 x 8.50 mm ′0 x 0.495 x 0.335 in		
Weight		2.9 g (0.10 oz)		
MTBF	Telcordia SR-332	7,092,000 hours		
ENVIRONMENTAL SPECIFICATIONS				
Thermal performance	Operating ambient,	-40 °C to +85 °C		

(See Note 2)	temperature Non-operating	-40 °C to +85 °C
MSL ('Z' suffix only)	JEDEC J-STD-020C	Level 3
PROTECTION		
Short-circuit	Auto reset	12 A typ.

*Auto-track[™] is a trade mark of Texas Instruments







DC-DC COM	NVERTERS PO	LA Non-isolate	d					2
For the mos	st current data ar	nd application s	upport visit w	ww.artesyn.co	m/powergroup/p	roducts.htm	NE	W Product
	INPUT VOLTAGE	OUTPUT VOLTAGE			EFFICIENCY (MAX.)	REGU		MODEL NUMBER ^(9,10)
(MAX.) 21.6 W	4.5-5.5 Vdc	0.8-3.6 Vdc	(MIN.) 0 A	(MAX.) 6 A	95%	±10 mV	±12 mV	PTH05050
	Point	Product Family of Load Alliance Compatible Input Voltage 05 = 5 V Output Current 05 = 6 A anical Package Always 0 Output The ultra-wid select the PTI modules in or trimmed in a	put Voltage Ad e output voltage H05050. It is no 'der to cover diffé	trim range offers longer necessary erent output volta to 3.6 Vdc. Whe	WAST	Z = Surface- pin solde Pin Option A = Through- A = Surface-1 Output Volta W = Wide ies o users who ty of tage can be werter	Ays Reel ⁽⁸⁾ al Through-Ho al Through-Ho Mount (63/37 er material) Mount (96.5/3 er material) Hole Std. Pin Mount Tin/Lea	ble (Sn/Pb) Sn/Pb 3.0/0.5 Sn/Ag/Cu Length (0.140")

Notes

- Remote ON/OFF. Positive Logic 1
- Pin 3 open; or V > Vin 0.5 V Pin 3 GND; or V < 0.8 V (min 0.2 V). ON:
- OFF:
- 2 See Figure 1 for safe operating curve.
- A 100 µF electrolytic input capacitor is required for proper operation. The 3 capacitor must be rated for a minimum of 300 mA rms of ripple current. An external output capacitor is not required for basic operation. Adding 4
- 100 μ F of distributed capacitance at the load will improve the transient response. 5
- response. 1 A/µs load step, 50 to 100% I_{omax} , $C_{out} = 100 \,\mu$ F. If utilized Vout will track applied voltage by ±0.3 V (up to Vo set point). The pre-bias start-up feature is not compatible with Auto-TrackTM. This is because when the module is under Auto-Track 6 and will sink current if the output voltage is below that of a back-feeding source. Therefore to ensure a pre-bias hold-off, one of the following two techniques must be followed when input power is first applied to the module. The Auto-Track[™] function must either be disabled, or the module's output held off using the Inhibit pin. Refer to Application Note 158 for more details.
- Tape and reel packaging only available on the surface-mount versions. 8
- To order Pb-free (RoHS compatible) surface-mount parts replace the Q mounting option 'S' with 'Z', e.g. PTH05050WAZ. To order Pb-free (RoHS compatible) through-hole parts replace the mounting option 'H' with 'D', e.g. PTH05050WAD.
- 10 NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at http://www.artesyn.com/powergroup/products.htm to find a suitable alternative.

EFFICIENCY TABLE (I _O = 4 A)				
OUTPUT VOLTAGE	EFFICIENCY			
Vo = 1.0 V	85%			
Vo = 1.2 V	87%			
Vo = 1.5 V	89%			
Vo = 1.8 V	90%			
Vo = 2.0 V	91%			
Vo = 2.5 V	93%			
Vo = 3.3 V	95%			







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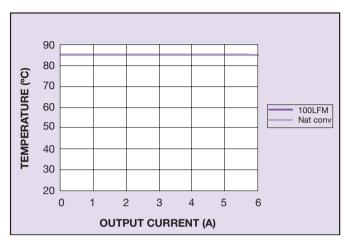


Figure 1 - Safe Operating Area Vin = 5 V, Output Voltage = 3.3 V (See Note A)

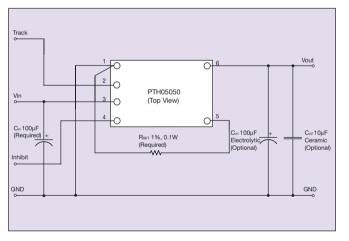


Figure 3 - Standard Application

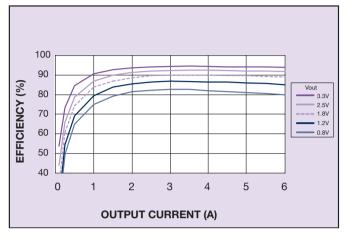


Figure 2 - Efficiency vs Load Current Vin = 5 V (See Note B)

Notes

- Α SOA curves represent the conditions at which internal components are
- within the Artesyn derating guidelines. Characteristic data has been developed from actual products tested at в 25 °C. This data is considered typical data for the converter.







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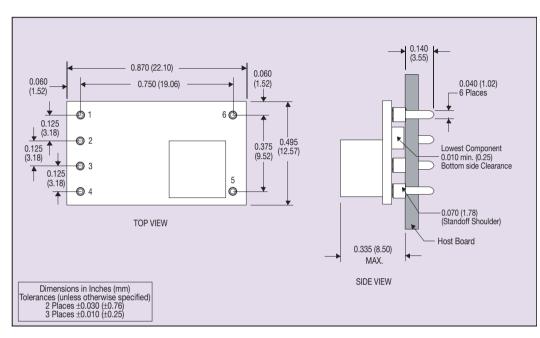
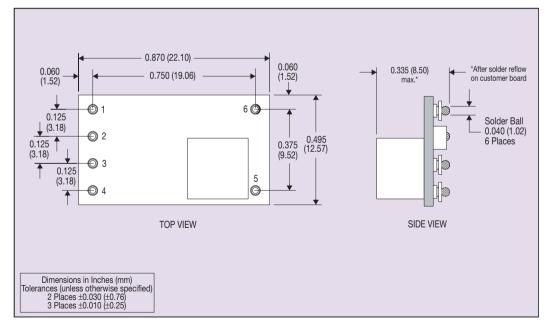


Figure 4 - Plated Through-Hole Mechanical Drawing

PIN CONNECTIONS				
PIN NO.	FUNCTION			
1	Ground			
2	Track			
3	Vin			
4	Inhibit*			
5	Vo adjust			
6	Vout			

*Denotes negative logic: Open = Normal operation Ground = Function active





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Please consult our website for the following items: V Application Note