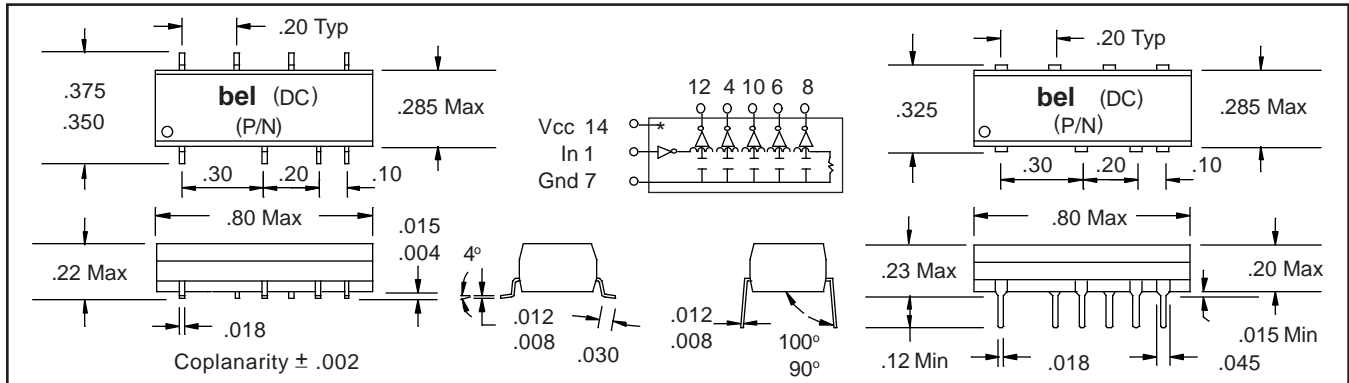




defining a degree of excellence

5 TAP LEADING EDGE CONTROL HIGH-SPEED CMOS DELAY MODULES

Cat 10-R3



Part Numbers

SMD	Thru-Hole	Total Delay	Delay per Tap	Rise Time
S450-0025-02	A450-0025-02	25 ns	5 ns	8 ns
S450-0030-02	A450-0030-02	30 ns	6 ns	8 ns
S450-0040-02	A450-0040-02	40 ns	8 ns	8 ns
S450-0050-02	A450-0050-02	50 ns	10 ns	8 ns
S450-0060-02	A450-0060-02	60 ns	12 ns	8 ns
S450-0070-02	A450-0070-02	70 ns	14 ns	8 ns
S450-0075-02	A450-0075-02	75 ns	15 ns	8 ns
S450-0080-02	A450-0080-02	80 ns	16 ns	8 ns
S450-0090-02	A450-0090-02	90 ns	18 ns	8 ns
S450-0100-02	A450-0100-02	100 ns	20 ns	8 ns
S450-0125-02	A450-0125-02	125 ns	25 ns	8 ns
S450-0150-02	A450-0150-02	150 ns	30 ns	8 ns
S450-0200-02	A450-0200-02	200 ns	40 ns	8 ns

Test Conditions @ 25°C

Ein	Pulse Voltage	5.0 Volts
Trin	Rise Time	6.0 ns (10%-90%)
PW	Pulse Width	1.2 x Total Delay
FP	Pulse Period	4 x Pulse Width
Iccl	Supply Current	25 ma Typical
Vcc	Supply Voltage	5.0 Volts

Electrical Characteristics

	Min.	Max.	Units
Vcc	4.75	5.25	V
Vin	0	Vcc	V
Vout	0	Vcc	V
Vih	3.15		V
Vil		0.9	V
Voh	4.40		V
	3.84		V
Vol		0.1	V
		0.4	V
Iih		1	ua
Iil		-1	ua
Icch		40	ua
Iccl		30	ma
Ta	-40°	85°	C
PW	40		%
d		50	%
Tc		100 + (25000/TD)	PPM/°C

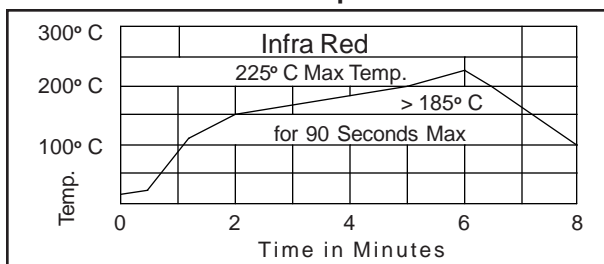
Tolerances

Input to Taps ± 2 ns or 5% , Whichever is Greater
 Tap to Tap ± 2 ns or 7% , Whichever is Greater
 Delays measured @ 50% levels on Leading Edge only
 with no loads on Taps
 Rise and Fall Times measured from 10% to 90% levels

Drive Capabilities

Nh	Logic 1 Fanout	-	10 LSTTL Loads Max.
Nl	Logic 0 Fanout	-	10 LSTTL Loads Max.

Recommended Temperature Profile



Notes

Transfer molded for better reliability
 Compatible with CMOS & HCMOS circuits
 Terminals: Electro-Tin plate phosphor bronze
 Performance warranty is limited to specified parameters listed
 SMD - Tape & Reel available:
 32mm Wide x 16mm Pitch, 500 pieces per 13" reel

Other Delays and Tolerances Available Consult Sales

Specifications subject to change without notice.

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