

## DESCRIPTION

The SC1563 is a high performance positive voltage regulator designed for use in applications requiring very low dropout voltage at up to 500 milliamps. Since it has superior dropout characteristics compared to regular LDOs, it can be used to supply 2.5V on motherboards or 2.8V on peripheral cards from the 3.3V supply with no heatsink. Additionally, the SC1563 has a shutdown pin, to further reduce power dissipation while shut down. The SC1563 provides excellent regulation over variations in line, load and temperature.

The SC1563 is available as a five terminal fixed or adjustable output voltage device with enable. The device is available in a 5-pin SOT-23 package and an 8-pin MSOP package for higher power dissipation/ lower junction temperature.

## FEATURES

- 450mV dropout @ 500mA
- Designed to operate with ceramic capacitors
- Adjustable output from 1.200V to 3.3V
- Multiple output voltage options (all parts also adjustable externally using resistors)
- Over current and over temperature protection
- Enable pin
- 1µA quiescent current in shutdown
- Low reverse leakage (output to input)
- Full industrial temperature range

## APPLICATIONS

- Battery powered systems
- Motherboards
- Peripheral cards
- PCMCIA cards

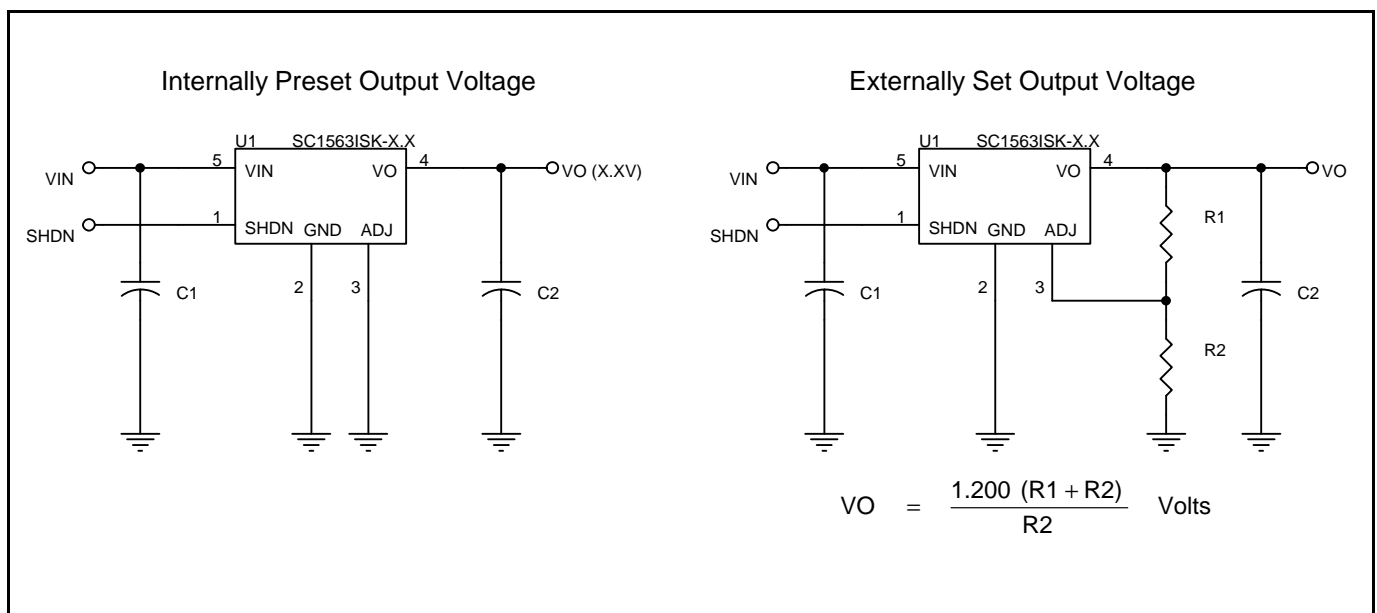
## ORDERING INFORMATION

DEVICE <sup>(1)(2)(3)</sup>	PACKAGE
SC1563ISK-X.XTR	SOT-23-5L
SC1563IMS-X.XTR	MSOP-8

Notes:

- (1) Where -X.X denotes voltage options. Available voltages are: 1.8V, 2.5V, 2.8V, 3.0V, 3.1V and 3.3V.
- (2) Output voltage can be adjusted using external resistors, see Pin Description.
- (3) Only available in tape and reel packaging. A reel contains 2500 devices (MSOP-8) or 3000 devices (SOT-23-5L).

## TYPICAL APPLICATION



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**ABSOLUTE MAXIMUM RATINGS**

Parameter	Symbol	Maximum	Units
Input Voltage	$V_{IN}$	5.5	V
Power Dissipation	$P_D$	Internally Limited	W
Thermal Resistance Junction to Ambient MSOP-8 <sup>(1)</sup> MSOP-8 <sup>(2)</sup> SOT-23-5L <sup>(1)</sup>	$\theta_{JA}$	206 95 256	°C/W
Thermal Resistance Junction to Case MSOP-8 SOT-23-5L	$\theta_{JC}$	39 81	°C/W
Operating Ambient Temperature Range	$T_A$	-40 to +85	°C
Operating Junction Temperature Range	$T_J$	-40 to 150	°C
Storage Temperature Range	$T_{STG}$	-65 to 150	°C
Lead Temperature (Soldering) 10 Sec.	$T_{LEAD}$	300	°C
ESD Rating	ESD	2	kV

Notes:

(1) Minimum pad size.

(2) 1 square inch of FR-4, double sided, 1oz. minimum copper weight.

**ELECTRICAL CHARACTERISTICS**

 Unless specified:  $V_{SHDN} = 0V$ 

 Adjustable Option ( $V_{ADJ} > V_{TH(ADJ)}$ ):  $V_{IN} = 2.2$  to 5.5V and  $I_O = 10\mu A$  to 500mA

 Fixed Options ( $V_{ADJ} = GND$ ):  $V_{IN} = (V_O + 0.7V)$  to 5.5V and  $I_O = 0A$  to 500mA

 Values in **bold** apply over the full operating temperature range.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>VIN</b>						
Supply Voltage Range	$V_{IN}$		<b>2.2</b>		<b>5.5</b>	V
Quiescent Current	$I_Q$	$V_{IN} = 3.3V$		100	<b>150</b>	$\mu A$
		$V_{IN} = 5.5V, V_{SHDN} = Open$		0.5	<b>2</b>	$\mu A$
<b>VO</b>						
Output Voltage <sup>(1)</sup> (Internal Fixed Voltage)	$V_O$	$V_{IN} = V_O + 0.7V, I_O = 10mA$	$0.99V_O$	$V_O$	$1.01V_O$	V
			<b><math>0.98V_O</math></b>	<b><math>V_O</math></b>	<b><math>1.02V_O</math></b>	
Line Regulation <sup>(1)</sup>	$REG_{(LINE)}$	$V_{IN} = (V_O + 0.25V)$ to 5.5V, $I_{OUT} = 10mA$		0.035	<b>0.3</b>	%
Load Regulation <sup>(1)</sup>	$REG_{(LOAD)}$	$V_{IN} = V_O + 0.7V$		0.2	<b>0.4</b>	%

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**ELECTRICAL CHARACTERISTICS (Cont.)**

 Unless specified:  $V_{SHDN} = 0V$ 

 Adjustable Option ( $V_{ADJ} > V_{TH(ADJ)}$ ):  $V_{IN} = 2.2$  to  $5.5V$  and  $I_O = 10\mu A$  to  $500mA$ 

 Fixed Options ( $V_{ADJ} = GND$ ):  $V_{IN} = (V_O + 0.7V)$  to  $5.5V$  and  $I_O = 0A$  to  $500mA$ 

 Values in **bold** apply over the full operating temperature range.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>VO (Cont.)</b>						
Dropout Voltage <sup>(2)</sup>	$V_D$	$I_{LOAD} = 2mA$		1	5	mV
					<b>10</b>	
		$I_{LOAD} = 100mA$		75	100	mV
					<b>150</b>	
		$I_{LOAD} = 250mA$		200	300	mV
					<b>400</b>	
		$I_{LOAD} = 500mA$		450	550	mV
				<b>700</b>		
Minimum Load Current <sup>(3)</sup>	$I_O$	$V_{IN} = V_O + 0.7V$		1	<b>10</b>	$\mu A$
Current Limit	$I_{CL}$		<b>0.50</b>		<b>1.00</b>	A
<b>ADJ</b>						
Reference Voltage <sup>(1)</sup>	$V_{REF}$	$V_{IN} = 2.2V, V_{ADJ} = V_{OUT}, I_O = 10mA$	1.188	1.200	1.212	V
			<b>1.176</b>		<b>1.224</b>	
Adjust Pin Current <sup>(4)</sup>	$I_{ADJ}$	$V_{ADJ} = V_{REF}$		50	<b>100</b>	nA
Adjust Pin Threshold <sup>(5)</sup>	$V_{TH(ADJ)}$		<b>0.10</b>	0.20	<b>0.40</b>	V
<b>SHDN</b>						
Shutdown Pin Current	$I_{SHDN}$	$V_{SHDN} = 0V, V_{IN} = 3.3V$		1	<b>10</b>	$\mu A$
Shutdown Pin Threshold	$V_{IH}$	$V_{IN} = 3.3V$	<b>1.8</b>			V
	$V_{IL}$	$V_{IN} = 3.3V$			<b>0.4</b>	
<b>OVER TEMPERATURE PROTECTION</b>						
High Trip Level	$T_{HI}$			165		$^{\circ}C$
Hysteresis	$T_{HYST}$			10		$^{\circ}C$

**NOTES:**

(1) Low duty cycle pulse testing with Kelvin connections required.

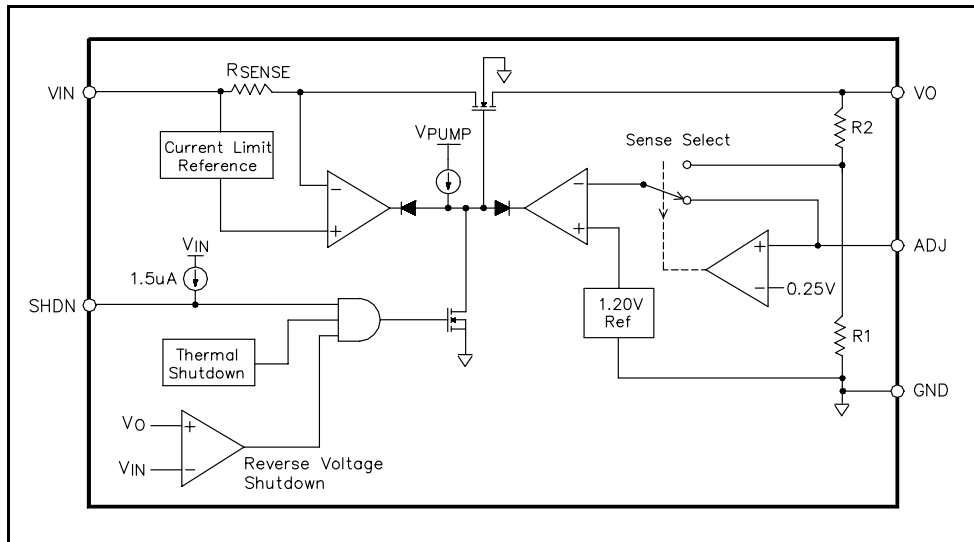
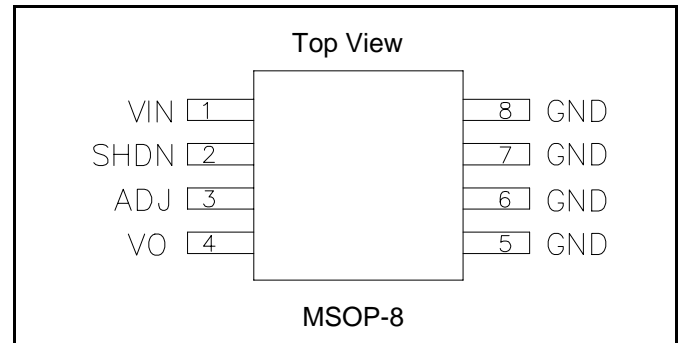
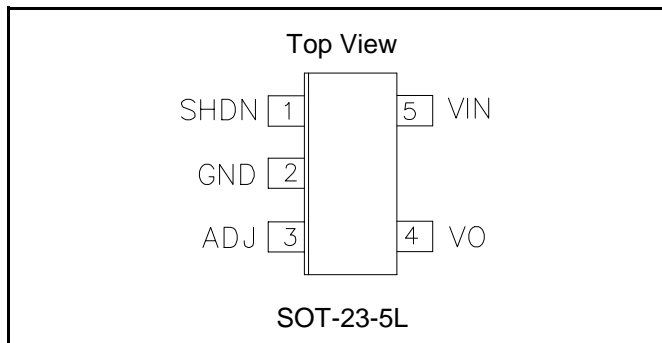
(2) Defined as the input to output differential at which the output voltage drops to 1% below the value measured at a differential of 0.7V.

(3) Required to maintain regulation. Voltage set resistors R1 and R2 are usually utilized to meet this requirement. Adjustable versions only.

(4) Guaranteed by design.

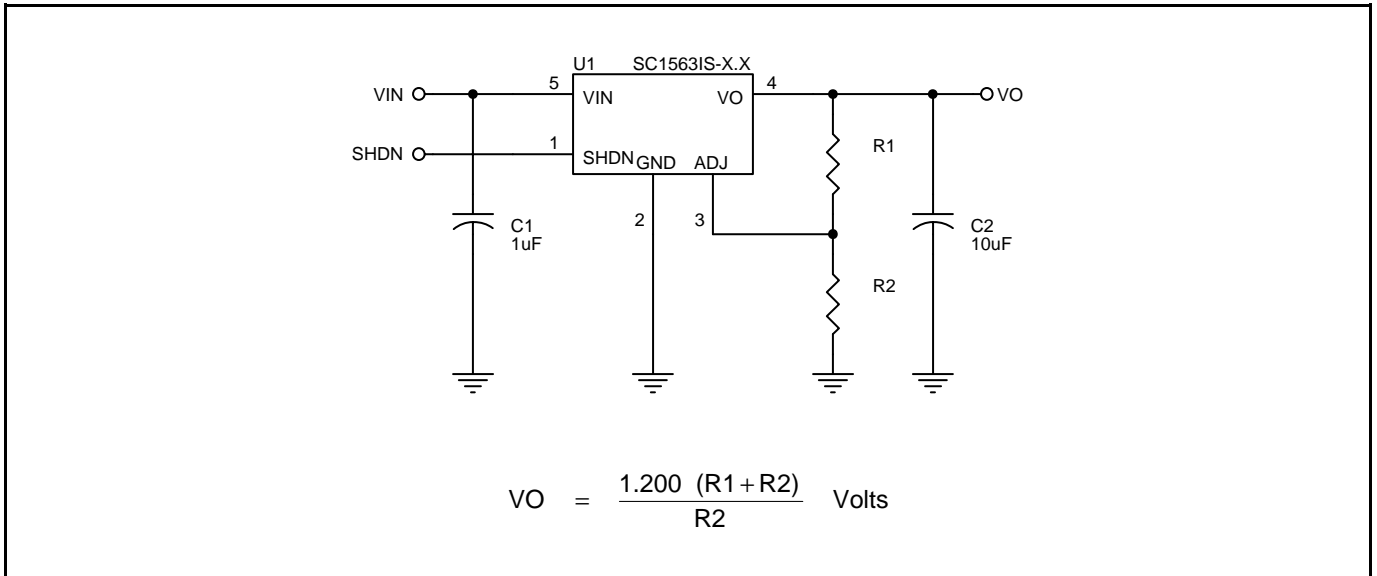
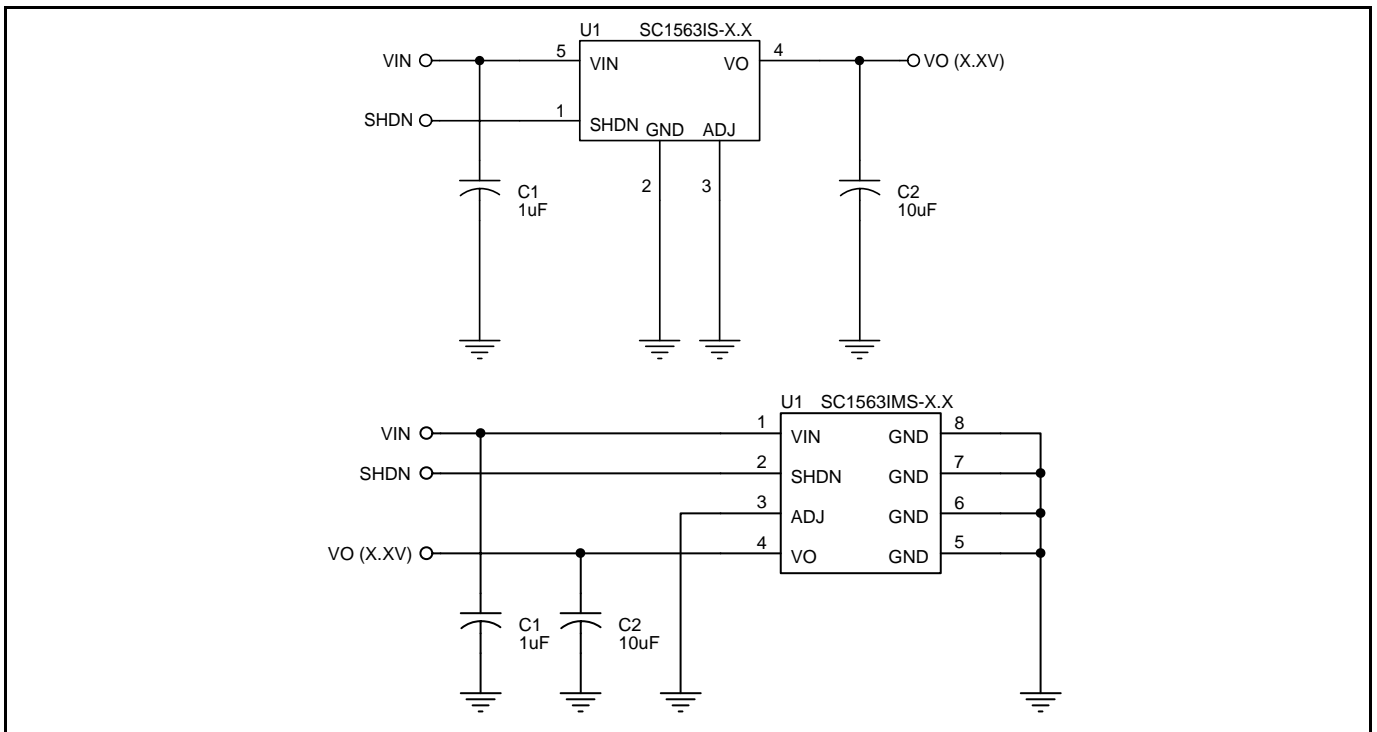
 (5) When  $V_{ADJ}$  exceeds this threshold, the "Sense Select" switch disconnects the internal feedback chain from the error amplifier and connects  $V_{ADJ}$  instead.

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**BLOCK DIAGRAM**

**PIN CONFIGURATIONS**

**PIN DESCRIPTION**

Pin Name	Pin Number		Pin Function
	SOT-23-5L	MSOP-8	
ADJ	3	3	This pin, when grounded, sets the output voltage to that set by the internal feedback resistors. If external feedback resistors are used, the output voltage will be (See Application Circuit): $V_O = \frac{1.200(R_1+R_2)}{R_2} \text{ Volts}$
GND	2	5-8	Reference ground. Connect all MSOP-8 GND pins. These pins may be used for heatsinking purposes.
VIN	5	1	Input voltage. For regulation at full load, the input to this pin must be between (VO + 0.7V) to 5.5V. Minimum VIN = 2.2V.
VO	4	4	This pin is the power output of this device.
SHDN	1	2	Shutdown Input. Leaving this pin open turns the regulator off, reducing the quiescent current to a fraction of its operating value (typically <100nA). The device will be enabled if this pin is pulled below 0.4V. Connect to GND if not being used.

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**TYPICAL APPLICATION (Externally Set Output Voltage)<sup>(1)(2)</sup>**

**TYPICAL APPLICATIONS (Internally Set Output Voltage)<sup>(2)</sup>**


Notes:

(1) Maximum VO setpoint is 3.3V.

(2) This device is designed to operate with ceramic input and output capacitors.

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## APPLICATIONS INFORMATION

### Introduction

The SC1563 is intended for applications such as graphics cards where high current capability and very low dropout voltage are required. It provides a very simple, low cost solution that uses little pcb real estate and typically does not require a heatsink. Additional features include an enable pin to allow for a very low power consumption standby mode, and a fully adjustable output.

### Component Selection

Input capacitor - a 1 $\mu$ F ceramic capacitor is recommended. This allows for the device being some distance from any bulk capacitance on the rail. Additionally, input droop due to load transients is reduced, improving load transient response. Additional capacitance may be added if required by the application.

Output capacitor - a minimum bulk capacitance of 1 $\mu$ F, along with a 0.1 $\mu$ F ceramic decoupling capacitor is recommended. Increasing the bulk capacitance will improve the overall transient response. The use of multiple lower value ceramic capacitors in parallel to achieve the desired bulk capacitance will not cause stability issues. Although designed for use with ceramic output capacitors, the SC1563 is extremely tolerant of output capacitor ESR values and thus will also work comfortably with tantalum output capacitors.

External voltage selection resistors - the use of 1% resistors, and designing for a current flow  $\geq 10\mu$ A is recommended to ensure a well regulated output (thus  $R2 \leq 120k\Omega$ ).

### Thermal Considerations

The power dissipation in the SC1563 is approximately equal to the product of the output current and the input to output voltage differential:

$$P_D \approx (V_{IN} - V_{OUT}) \cdot I_O$$

The absolute worst-case dissipation is given by:

$$P_{D(MAX)} = (V_{IN(MAX)} - V_{OUT(MIN)}) \cdot I_{O(MAX)} + V_{IN(MAX)} \cdot I_{Q(MAX)}$$

For a typical scenario,  $V_{IN} = 3.3V \pm 5\%$ ,  $V_{OUT} = 2.5V$  and  $I_O = 500mA$ , therefore:

$$V_{IN(MAX)} = 3.465V, V_{OUT(MIN)} = 2.450V \text{ and } I_{Q(MAX)} = 1.75mA,$$

Thus  $P_{D(MAX)} = 0.514W$ .

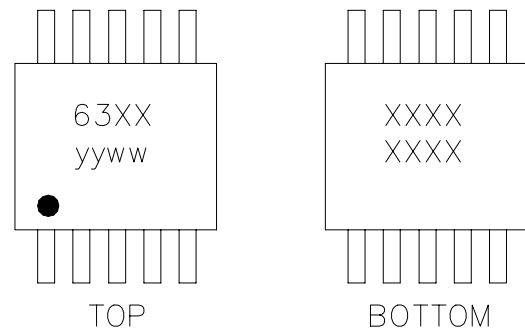
Using this figure, and assuming  $T_{A(MAX)} = 85^\circ C$ , we can calculate the maximum thermal impedance allowable to maintain  $T_J \leq 150^\circ C$ :

$$R_{TH(J-A)(MAX)} = \frac{(T_{J(MAX)} - T_{A(MAX)})}{P_{D(MAX)}} = \frac{(150 - 70)}{0.514} = 156^\circ C/W$$

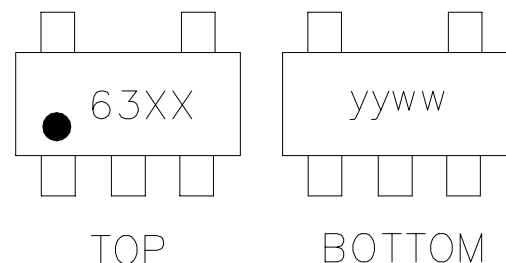
This should be achievable using pcb copper area to aid in conducting the heat away from the device. Internal ground/power planes and air flow will also assist in removing heat. For higher ambient temperatures additional board copper may be required. Use of the available MSOP-8 package will allow operation at full load where higher input to output voltage differentials exist.

## MARKING INFORMATION

MSOP-8

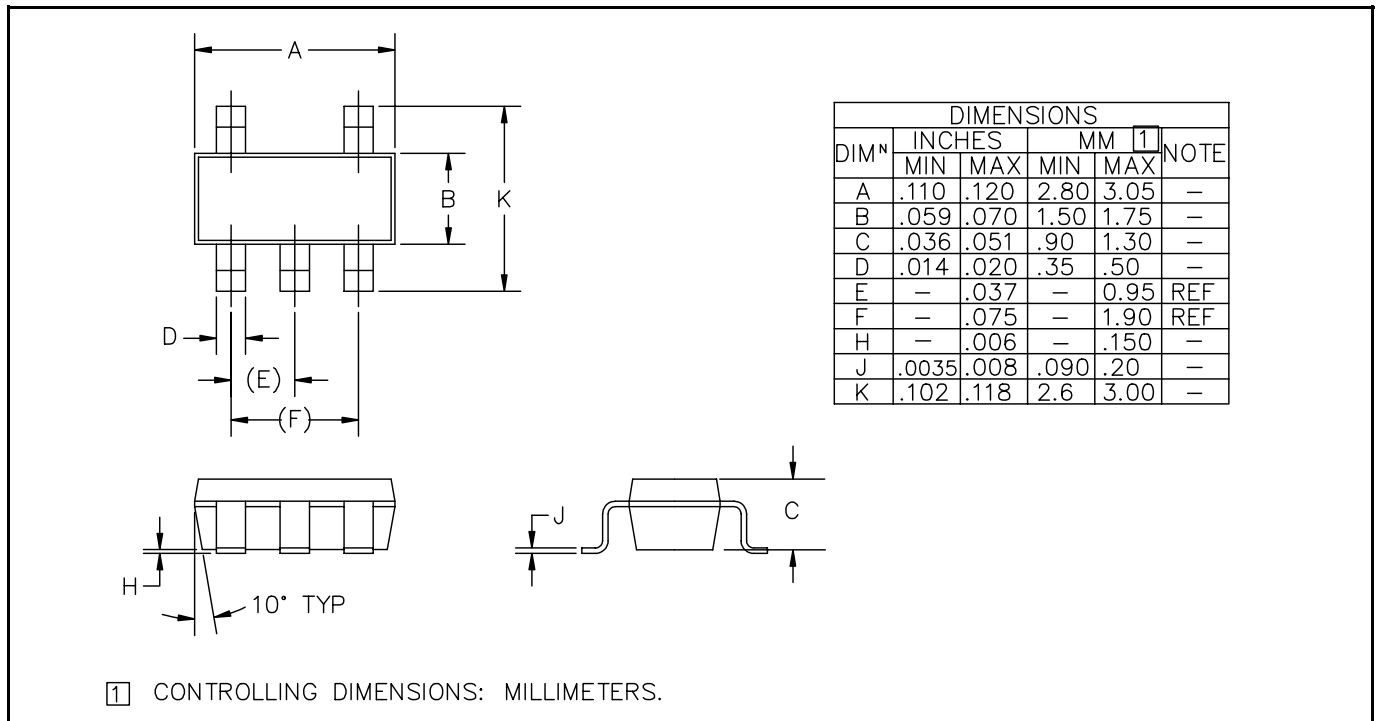
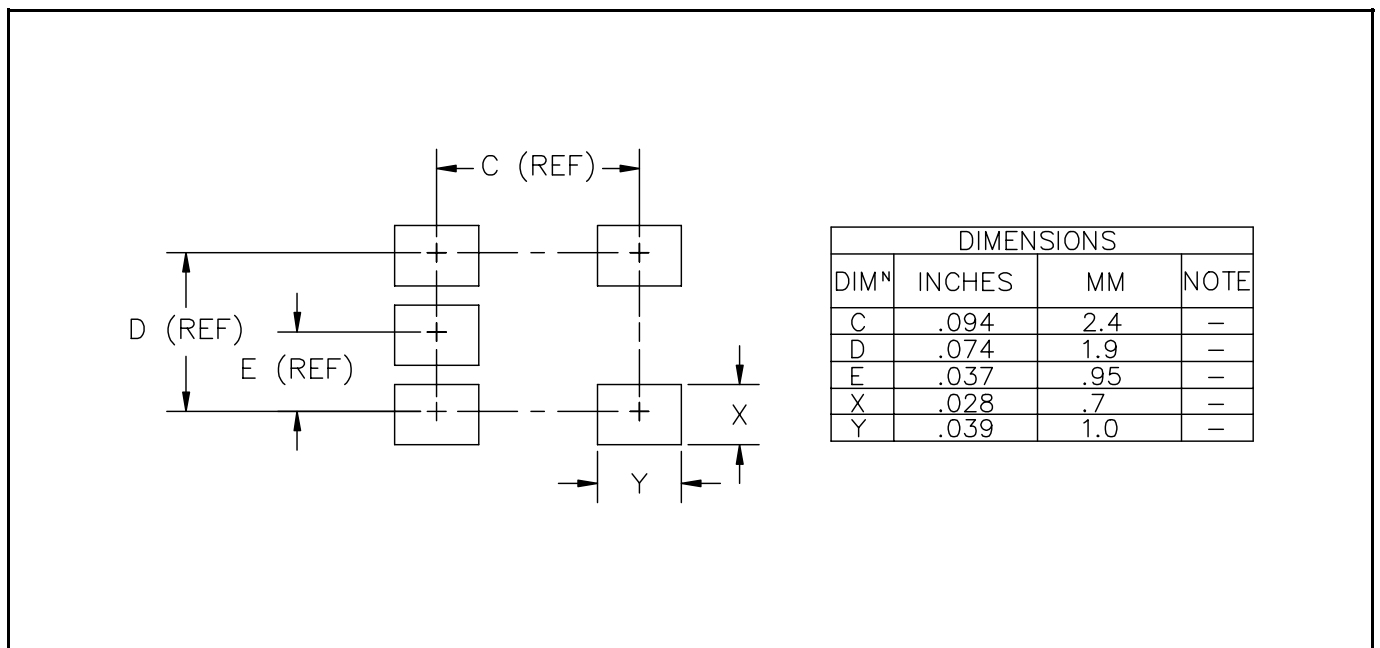


SOT-23-5L

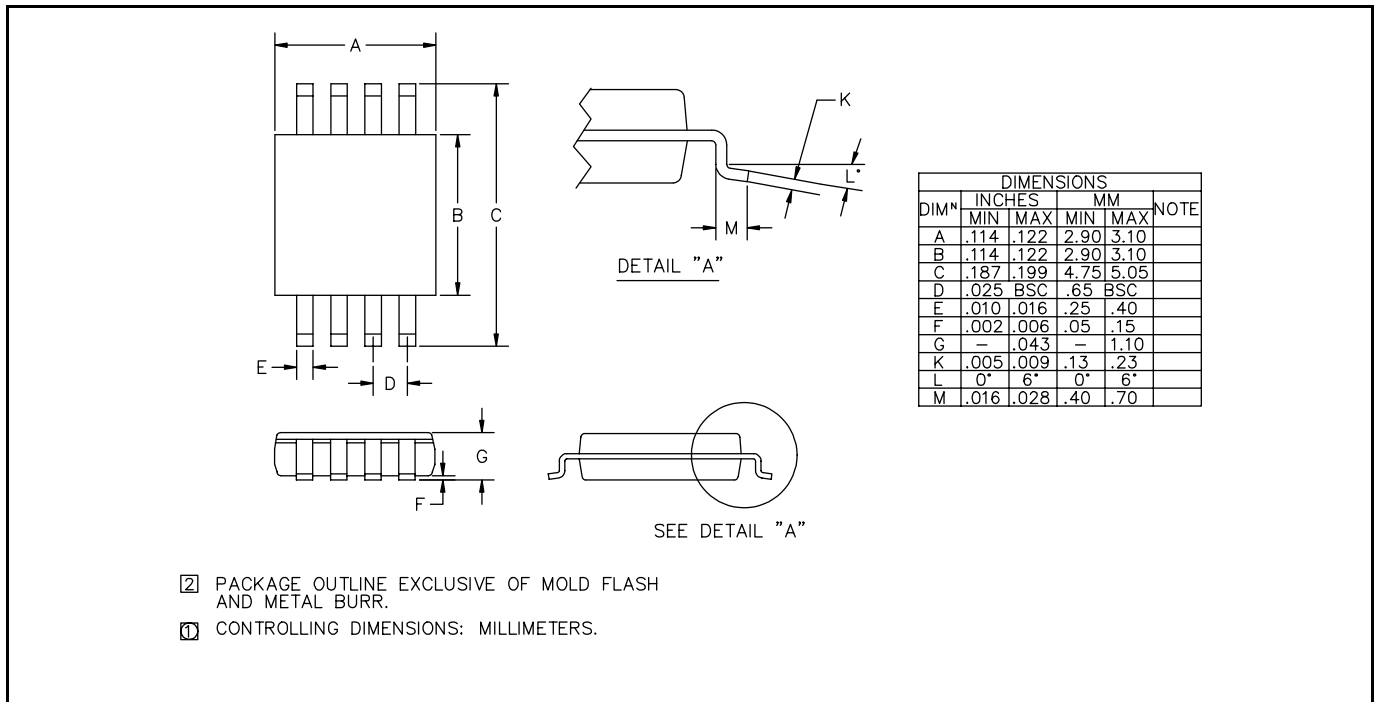
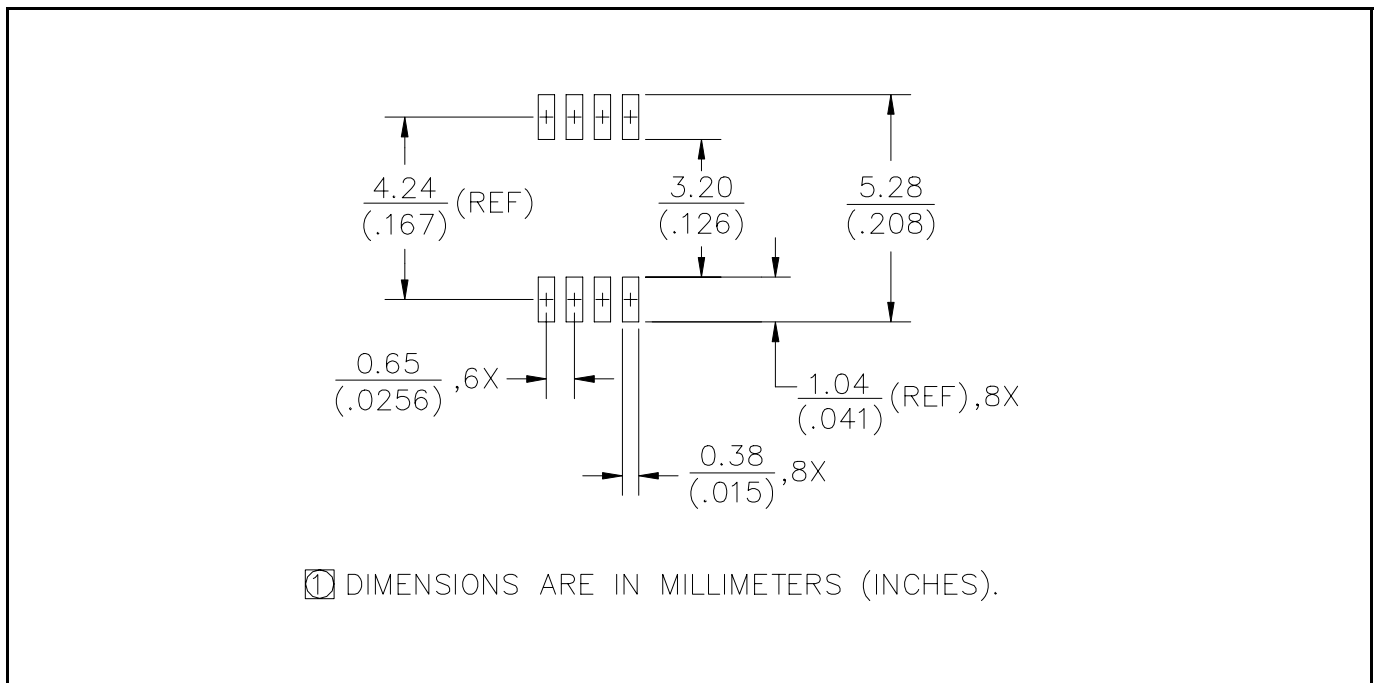


63XX = SC1563, voltage option (example: 6318)  
 yyww = Datecode (example: 0008)  
 XXXX = Lot number (example: E01102)

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**DEVICE OUTLINE - SOT-23-5**

**LAND PATTERN - SOT-23-5**


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**OUTLINE DRAWING - MSOP-8**

**LAND PATTERN - MSOP-8**


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