

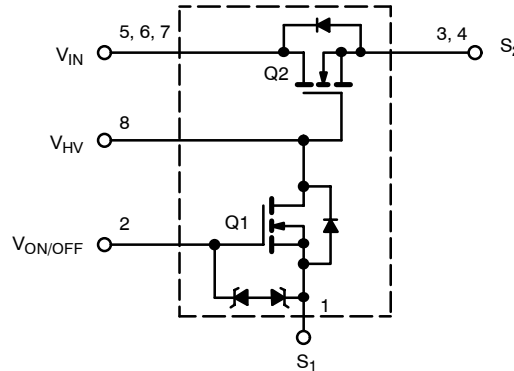
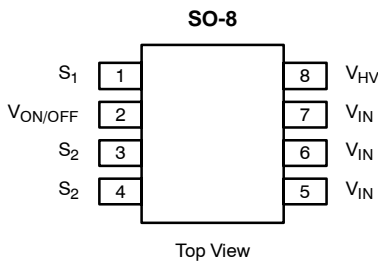


## Load Switch with Level-Shift

PRODUCT SUMMARY		
V <sub>DS2</sub> (V)	r <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)
30	0.015 @ V <sub>GS2</sub> = 10 V	7.0
	0.021 @ V <sub>GS2</sub> = 4.5 V	6.0



Product Is Completely Pb-free



Ordering Information: Si4701BDY—E3  
Si4701BDY-T1—E3 (with Tape and Reel)

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Limit	Unit
Input Voltage	V <sub>IN</sub>	30	V
Q2 Gate-Drive Voltage Referenced to S1 or S2	V <sub>HV</sub>	20	
ON/OFF Voltage	V <sub>ON/OFF</sub>	8	
Load Current	I <sub>L</sub>	Continuous <sup>a</sup>	7.0
		Pulsed <sup>b</sup>	±30
Continuous Intrinsic Diode Conduction <sup>a</sup>	I <sub>S</sub>	-1.15	A
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	1.25	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C
ESD Rating, MIL-STD-883D Human Body Model (100 pF, 1500 Ω)	ESD	3	kV

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient (t = steady state) <sup>a</sup>	R <sub>thJA</sub>	80	100	°C/W
Maximum Junction-to-Foot (Q2)	R <sub>thJC</sub>	25	30	

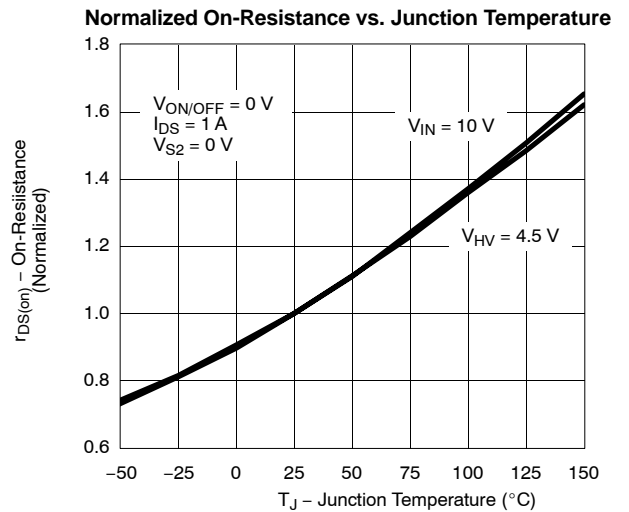
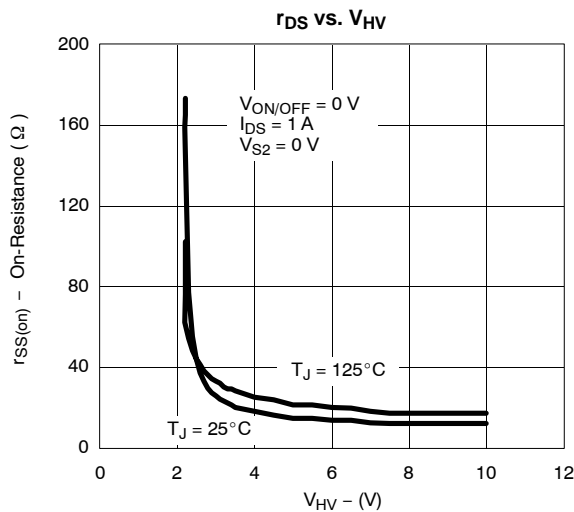
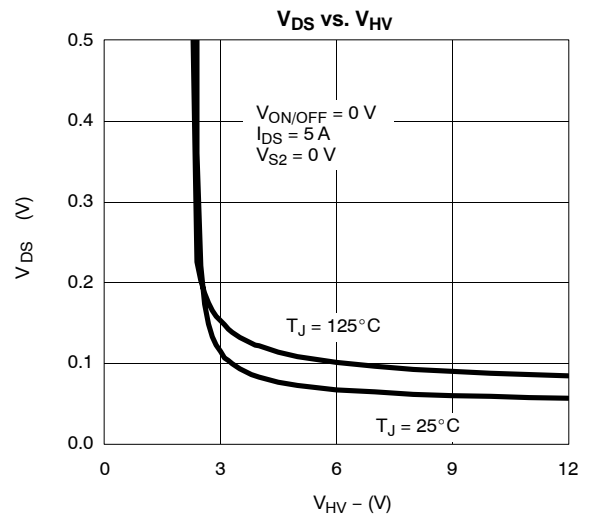
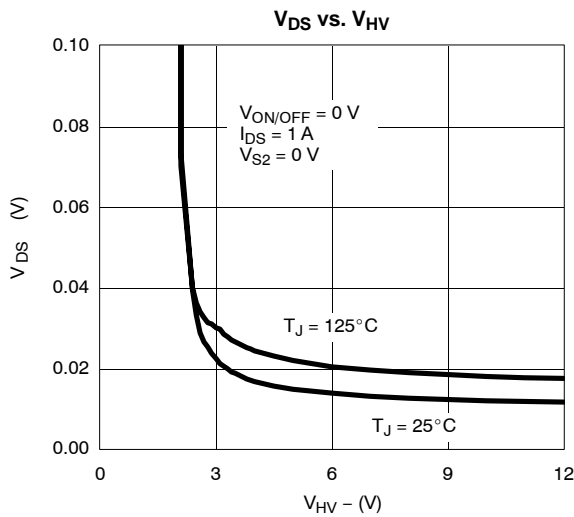
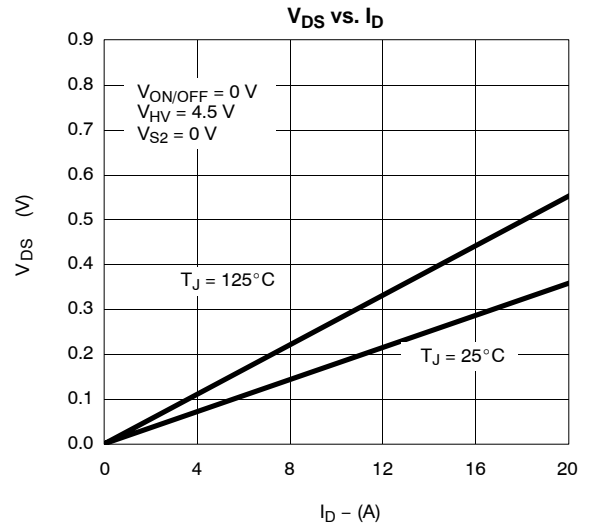
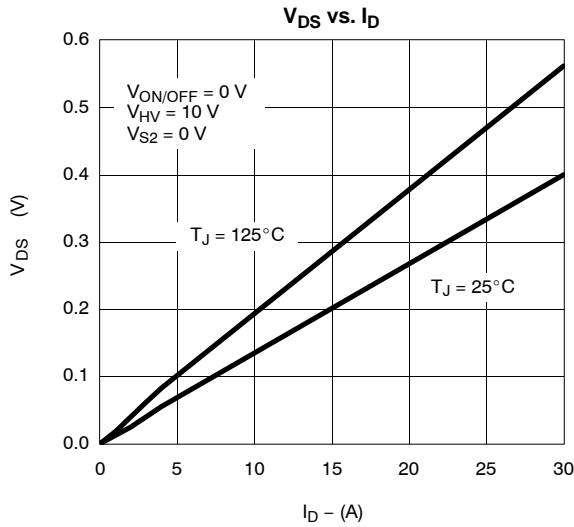
SPECIFICATIONS (T <sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>OFF Characteristics</b>						
Reverse Leakage Current	I <sub>FL</sub>	V <sub>IN</sub> = 30 V, V <sub>ON/OFF</sub> = 0 V, V <sub>HV</sub> = 0 V			1	μA
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = -1.15 A		0.7	1	V
<b>ON Characteristics</b>						
On-Resistance (Q2)	r <sub>DS(on)</sub>	V <sub>ON/OFF</sub> = 0 V, I <sub>D</sub> = 7 A, V <sub>HV</sub> = 10 V, V <sub>S2</sub> = 0 V		0.012	0.015	Ω
		V <sub>ON/OFF</sub> = 0 V, I <sub>D</sub> = 6 A, V <sub>HV</sub> = 4.5 V, V <sub>S2</sub> = 0 V		0.017	0.021	
On-State (Q2) Drain-Current	I <sub>D(on)</sub>	V <sub>IN-OUT</sub> ≤ 0.1 V, V <sub>IN</sub> = 5 V, V <sub>ON/OFF</sub> = 0 V, V <sub>HV</sub> = 10 V	20			A

Notes

- a. Surface Mounted on FR4 Board.
- b. Pulse test: pulse width ≤ 300 μs, duty cycle ≤ 2%.

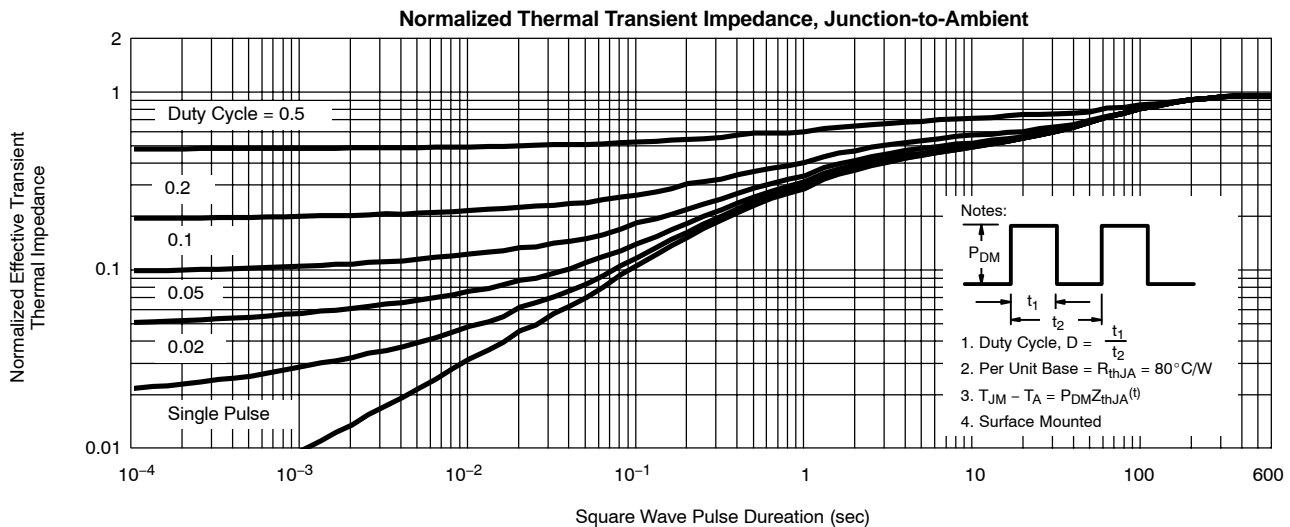
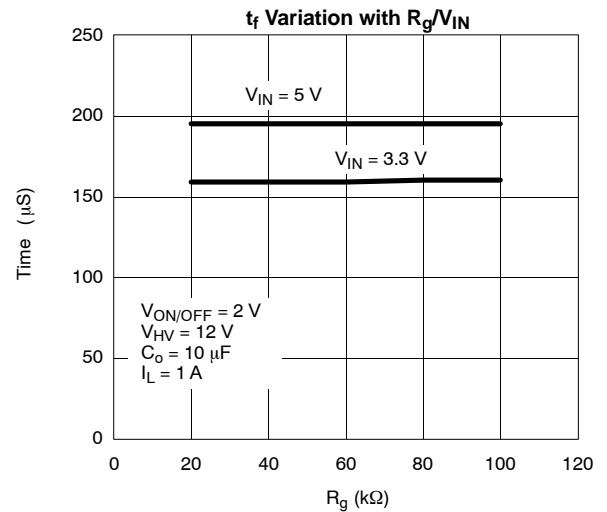
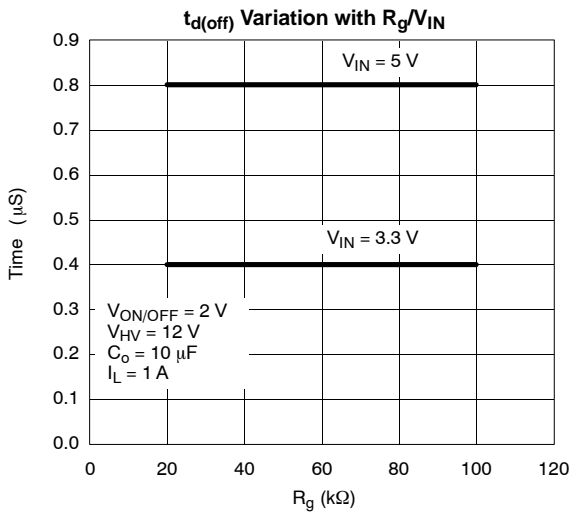
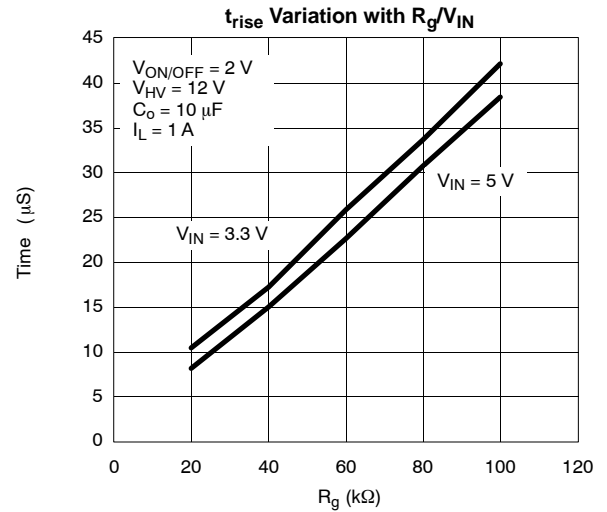
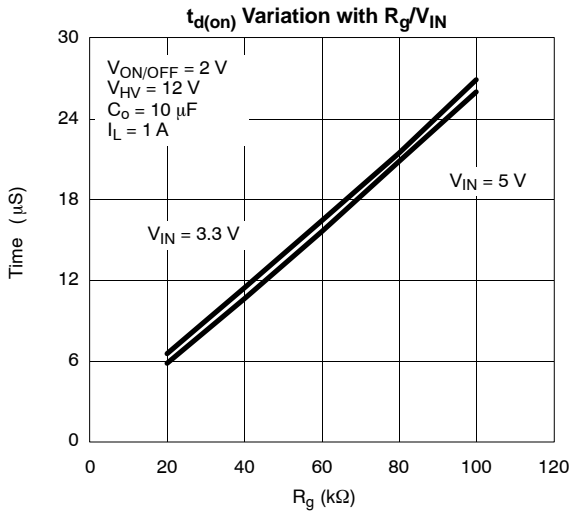
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**

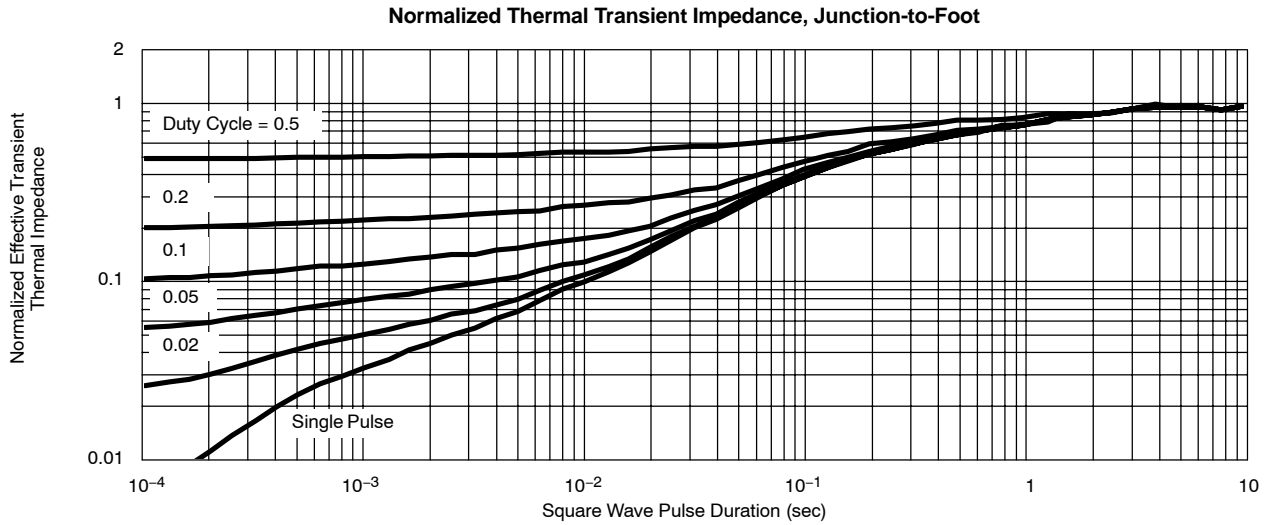




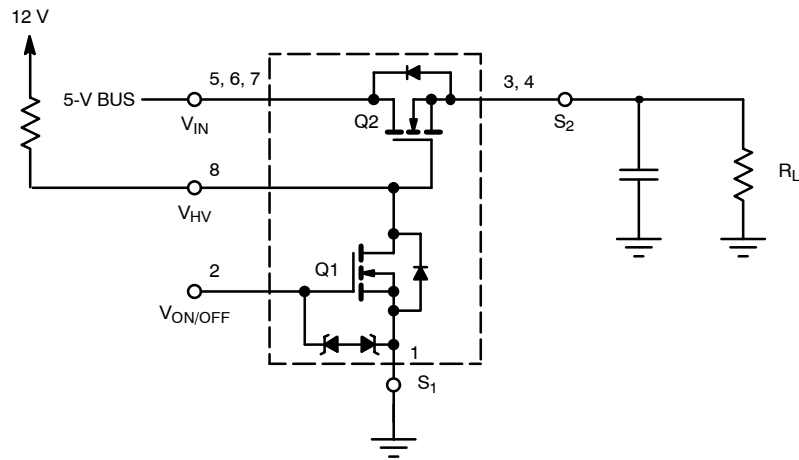
**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**



**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**



**TYPICAL APPLICATION CIRCUIT**



NOTE: Voltage difference between pull-up voltage, 12 V, and BUS voltage, 5 V, should be greater than 4.5 V.

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