PT78NR100 Series

1 Amp Plus to Minus Voltage Integrated Switching Regulator



SLTS058B

(Revised 8/31/2000)

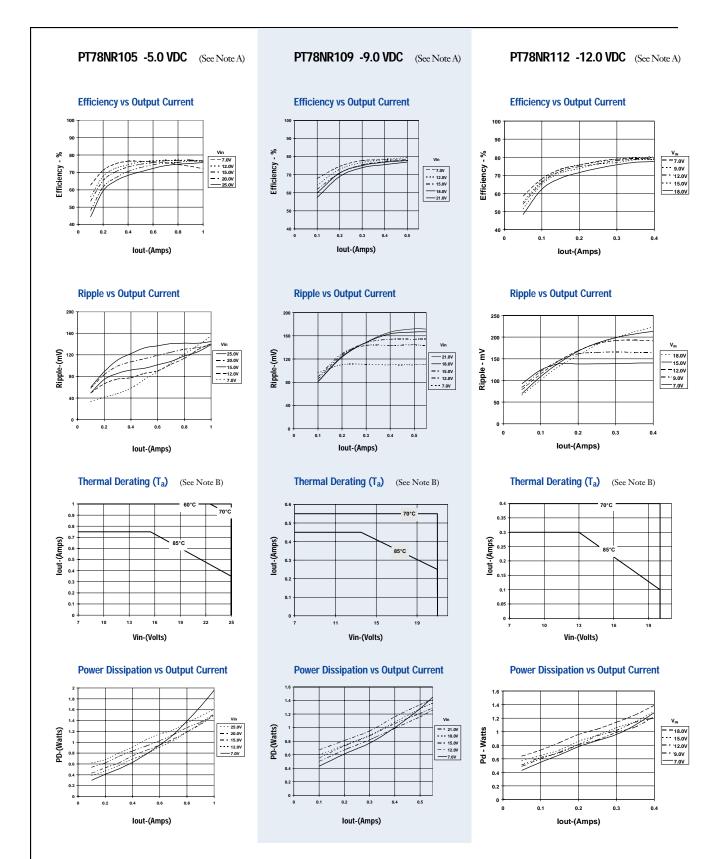
		 Negative output from positive input Wide Input Range Self-Contained Inductor Short Circuit Protection Over-Temperature Protection Fast Transient Response 	The PT78NR100 Series creates a negative output voltage from a posi- tive input voltage greater than 7V. These easy-to-use, 3-terminal, Inte- grated Switching Regulators (ISRs) have maximum output power of 5 watts and a negative output voltage that is laser trimmed. They also have excellent line and load regulation.			
		Pin-Out Information	Ordering Information			
		Pin Function		78NR1		
		$1 + V_{in}$	PI	ONKI	XX Y	
Standard Application		$\frac{1}{2}$ $-V_{out}$				~ 45
		3 GND	Output		-	age Suffix
Vin 1 PT78NR100 2	-Vout	HORIZONTAL MOUNT.	03 = -3. 05 = -5.			ertical Mount urface Mount
			52 = -5.1			Iorizontal
C1 $C2$ $C2$	+		06 = -6. 07 = -7.		Λ	Aount
	COM		08 = -8.	0 Volts		
C1 = Required 100µF electro		3 2 1	09 = -9. 10 = -10			
$C2 = Required 100 \mu F$ electro	lytic (1)		10 = -10			
		SUGGESTED BOARD LAYDUT COMPONENT SIDE VIEW	14 = -13 15 = -15			
Specifications		Pkg Style 500	15 = -15	.0 voits		
Characteristics				78NR100 SE		_
(T _a = 25°C unless noted)	Symbols	Conditions	Min	Тур	Max	Units
Output Current	Io	Over V _{in} range V _o =-5V V _o =-6V	0.05 (2) 0.05 (2)	_	1.00 0.8	
		V _o =-7, -8, -9V V _o =-10V	0.05(2) 0.05(2)	_	0.55 0.5	А
		V _o =-12V V _o =-13.9, -15V	0.05 (2) 0.05 (2)	_	0.40 0.30	
Short Circuit Current	Isc	V _{in} =10V	_	4×I _{max}	_	Apk
Inrush Current	I _{ir}	V _{in} =10V	—	4	—	A
Input Voltage Range	tir	On start-up		0.5	25	mSec V
mput vonage Kallge	V·				20	
1 0 0	V_{in}	$0.1 \le I_o \le I_{max}$ $V_o = -5V$ $V_o = -6, -7, -8, -9V$ $V_o = 10, 12V$	7 7	_	21	V
	Vin	$\begin{array}{c} 0.1 \leq I_{0} \leq I_{max} & V_{0} = -5 V \\ V_{0} = -6, -7, -8, -9V \\ V_{0} = -10, -12V \\ V_{0} = -13.9, -15V \end{array}$	7 7 7 7			
Output Voltage Tolerance	ν _{in} ΔV _o	$V_{o}=-6, -7, -8, -9V \\ V_{o}=-10, -12V \\ V_{o}=-13.9, -15V \\ Over V_{in} range$		-	21 18	V V
Output Voltage Tolerance	ΔV_o	$\begin{array}{c} V_{o}=-6, -7, -8, -9V\\ V_{o}=-10, -12V\\ V_{o}=-13.9, -15V\\ \hline \\ Over V_{in} range\\ T_{a}=-20^{\circ}C \text{ to } +70^{\circ}C \end{array}$	7 7 7 	 ±1.0	21 18 15 ±3.0	V V V
	ΔV _o Reg _{line}	$V_{0}=-6, -7, -8, -9V \\ V_{0}=-10, -12V \\ V_{0}=-13.9, -15V \\ Over V_{in} range \\ T_{a}=-20^{\circ}C \text{ to } +70^{\circ}C \\ Over V_{in} range \\ Over V_{$	7 7 7	=	21 18 15	V V V
Output Voltage Tolerance	ΔV_o	$\begin{array}{c} V_{o}=-6, -7, -8, -9V\\ V_{o}=-10, -12V\\ V_{o}=-13.9, -15V\\ \hline \\ Over V_{in} range\\ T_{a}=-20^{\circ}C \text{ to } +70^{\circ}C \end{array}$	7 7 7 —	 ±1.0 ±0.5	21 18 15 ±3.0 ±1.0	V V V %Vo %Vo
Output Voltage Tolerance Line Regulation Load Regulation V_oRipple/Noise Transient Response	ΔV _o Reg _{line} Reg _{load}	$V_{o}=-6, -7, -8, -9V \\ V_{o}=-10, -12V \\ V_{o}=-10, -12V \\ V_{o}=-13.9, -15V \\ \hline Over V_{in} range \\ T_{a}=-20^{\circ}C to +70^{\circ}C \\ \hline Over V_{in} range \\ \hline 0.1 \le I_{o} \le I_{max} \\ \hline V_{in}=10V, I_{o}=I_{max} \\ \hline 50\% load change \\ \hline \end{array}$	7777 		21 18 15 ± 3.0 ± 1.0 ± 1.0	V V V %Vo %Vo %Vo %Vo µSec
Output Voltage Tolerance Line Regulation Load Regulation V _o Ripple/Noise Transient Response (with 100µF output cap)	$\begin{array}{c} \Delta V_{o} \\ \hline Reg_{line} \\ \hline Reg_{load} \\ \hline V_{n} \\ \hline t_{tr} \end{array}$	$V_{o}=-6, -7, -8, -9V \\ V_{o}=-10, -12V \\ V_{o}=-10, -12V \\ V_{o}=-13.9, -15V \\ \hline Over V_{in} range \\ T_{a}=-20^{\circ}C to +70^{\circ}C \\ \hline Over V_{in} range \\ \hline 0.1 \le I_{o} \le I_{max} \\ \hline V_{in}=10V, I_{o}=I_{max} \\ \hline 50\% load change \\ V_{o} over/undershoot \\ \hline \end{array}$	7 7 7 — — — —		21 18 15 ±3.0 ±1.0 ±1.0	V V V %Vo %Vo %Vo %Vo %Vo
Output Voltage Tolerance Line Regulation Load Regulation V_oRipple/Noise Transient Response (with 100µF output cap) Efficiency	$\begin{array}{c} \Delta V_o \\ \hline Reg_{line} \\ \hline Reg_{load} \\ \hline V_n \\ t_{tr} \\ \hline \eta \end{array}$	$V_{o}=-6, -7, -8, -9V \\ V_{o}=-10, -12V \\ V_{o}=-10, -12V \\ V_{o}=-13.9, -15V \\ \hline Over V_{in} range \\ \hline T_{a}=-20^{\circ}C \text{ to } +70^{\circ}C \\ \hline Over V_{in} range \\ \hline 0.1 \le I_{o} \le I_{max} \\ \hline V_{in}=10V, I_{o}=I_{max} \\ \hline 50\% \text{ load change } \\ V_{o} \text{ over/undershoot} \\ \hline V_{in}=10V, I_{o}=0.5 \times I_{max}, V_{o} = -5V \\ \hline \end{array}$	7 7 7 — — — — — — — — — — — —	$\begin{array}{c} \\ \pm 1.0 \\ \pm 0.5 \\ \pm 0.5 \\ \pm 2 \\ 100 \\ 5.0 \\ 75 \end{array}$	21 18 15 ±3.0 ±1.0 <u>±1.0</u> <u>250</u> <u>-</u>	V V V %V _o %V _o %V _o µSec %V _o %
Output Voltage Tolerance Line Regulation Load Regulation V _o Ripple/Noise Transient Response (with 100µF output cap)	$\begin{array}{c} \Delta V_o \\ \hline Reg_{line} \\ Reg_{load} \\ V_n \\ t_{tr} \\ \hline \eta \\ f_o \end{array}$	$V_{o}=-6, -7, -8, -9V \\ V_{o}=-10, -12V \\ V_{o}=-10, -12V \\ V_{o}=-13.9, -15V \\ \hline Over V_{in} range \\ T_{a}=-20^{\circ}C to +70^{\circ}C \\ \hline Over V_{in} range \\ \hline 0.1 \le I_{o} \le I_{max} \\ \hline V_{in}=10V, I_{o}=I_{max} \\ \hline 50\% load change \\ V_{o} over/undershoot \\ \hline \end{array}$	7 7 7 — — — — — — — — — — 600	$\begin{array}{c}\\ +1.0\\ \pm 0.5\\ \pm 0.5\\ \pm 2\\ 100\\ 5.0\\ 75\\ 650\\ \end{array}$	21 18 15 ±3.0 ±1.0 ±1.0 250 700	V V V %Vo %Vo %Vo %Vo % kHz
Output Voltage Tolerance Line Regulation Load Regulation V_oRipple/Noise Transient Response (with 100µF output cap) Efficiency Switching Frequency Absolute Maximum Operating Temperaturte Range	$\begin{array}{c} \Delta V_o \\ \hline Reg_{line} \\ \hline Reg_{load} \\ V_n \\ t_{tr} \\ \hline \eta \\ \hline f_o \\ T_a \end{array}$	$V_{o}=-6, -7, -8, -9V \\ V_{o}=-10, -12V \\ V_{o}=-10, -12V \\ V_{o}=-13.9, -15V \\ \hline Over V_{in} range \\ \hline T_{a}=-20^{\circ}C \text{ to } +70^{\circ}C \\ \hline Over V_{in} range \\ \hline 0.1 \le I_{o} \le I_{max} \\ \hline V_{in}=10V, I_{o}=I_{max} \\ \hline 50\% \text{ load change } \\ V_{o} \text{ over/undershoot} \\ \hline V_{in}=10V, I_{o}=0.5 \times I_{max}, V_{o} = -5V \\ \hline Over V_{in} \text{ and } I_{o} \text{ ranges} \\ \hline \end{array}$	7 7 7 — — — — — — — — — — — —	$\begin{array}{c} \\ \pm 1.0 \\ \pm 0.5 \\ \pm 0.5 \\ \pm 2 \\ 100 \\ 5.0 \\ 75 \end{array}$	21 18 15 ±3.0 ±1.0 <u>±1.0</u> <u>250</u> <u>-</u>	V V V %Vo %Vo %Vo %Vo % kHz °C
Output Voltage Tolerance Line Regulation Load Regulation V _o Ripple/Noise Transient Response (with 100µF output cap) Efficiency Switching Frequency Absolute Maximum Operating Temperaturte Range Thermal Resistance	$\begin{array}{c} \Delta V_o \\ \hline Reg_{line} \\ Reg_{load} \\ V_n \\ t_{tr} \\ \hline \eta \\ f_o \\ T_a \\ \hline \theta_{ja} \end{array}$	$V_{o}=-6, -7, -8, -9V \\ V_{o}=-10, -12V \\ V_{o}=-10, -12V \\ V_{o}=-13.9, -15V \\ \hline V_{in} range \\ \hline T_{a}=-20^{\circ}C \text{ to } +70^{\circ}C \\ \hline Over V_{in} range \\ \hline 0.1 \le I_{o} \le I_{max} \\ \hline V_{in}=10V, I_{o}=I_{max} \\ \hline 50\% \text{ load change} \\ V_{o} \text{ over/undershoot} \\ \hline V_{in}=10V, I_{o}=0.5 \times I_{max}, V_{o} = -5V \\ \hline Over V_{in} \text{ and } I_{o} \text{ ranges} \\ \hline Free \text{ Air Convection, (40-60LFM)} \\ \hline \end{array}$	7 7 7 	$\begin{array}{c}\\ +1.0\\ \pm 0.5\\ \pm 0.5\\ \pm 2\\ 100\\ 5.0\\ 75\\ 650\\ \end{array}$	$\begin{array}{c} 21 \\ 18 \\ 15 \\ \pm 3.0 \\ \pm 1.0 \\ \pm 1.0 \\ \\ 250 \\ \\ 700 \\ \pm 85 \\ ^{(3)} \\ \end{array}$	V V V %Vo %Vo %Vo %Vo %Vo %KHz °C °C/W
Output Voltage Tolerance Line Regulation Load Regulation V _o Ripple/Noise Transient Response (with 100µF output cap) Efficiency Switching Frequency Absolute Maximum Operating Temperaturte Range Thermal Resistance Storage Temperature	$\begin{array}{c} \Delta V_{o} \\ \hline Reg_{line} \\ Reg_{load} \\ V_{n} \\ t_{tr} \\ \hline \eta \\ f_{o} \\ T_{a} \\ \hline \theta_{ja} \\ T_{s} \end{array}$	$V_{o}=-6, -7, -8, -9V \\ V_{o}=-10, -12V \\ V_{o}=-10, -12V \\ V_{o}=-13.9, -15V \\ 0 \\ V_{o}=-13.9, -15V \\ 0 \\ V_{in} range \\ 0.1 \le I_{o} \le I_{max} \\ 0.1 \le I_{o} \le I_{max} \\ V_{in}=10V, I_{o}=I_{max} \\ 0.1 \le I_{o} \le I_{max} \\ V_{in}=10V, I_{o}=I_{max} \\ 0.1 \\ V_{in}=10V, I_{o}=0.5 \\ V_{o} \\ 0 \\ V_{in}=10V, I_{o}=0.5 \\ V_{in}=0.5 \\ V_{o} \\ 0 \\ V_{in}=10V, I_{o}=0.5 \\ V_{in}=0.5 \\ V_{o} \\ 0 \\ V_{in}=10V, I_{o}=0.5 \\ V_{in}=0.5 \\ V_{i$	7 7 7 — — — — — 600 -40 — -40		$\begin{array}{c} 21 \\ 18 \\ 15 \\ \pm 3.0 \\ \pm 1.0 \\ \pm 1.0 \\ - \\ 250 \\ - \\ 700 \\ + 85 \\ (3) \\ - \\ \pm 125 \end{array}$	V V V %V₀ %V₀ %V₀ %V₀ % % kHz °C °C/W °C
Output Voltage ToleranceLine RegulationLoad RegulationV_oRipple/NoiseTransient Response (with 100 μ F output cap)EfficiencySwitching FrequencyAbsolute Maximum Operating Temperaturte Range Thermal ResistanceStorage Temperature Mechanical Shock	$\begin{array}{c} \Delta V_{o} \\ \hline Reg_{line} \\ \hline Reg_{load} \\ \hline V_{n} \\ t_{tr} \\ \hline \eta \\ \hline f_{o} \\ \hline T_{a} \\ \hline \theta_{ja} \\ \hline T_{s} \\ \hline \end{array}$	$V_{o}=-6, -7, -8, -9V \\ V_{o}=-10, -12V \\ V_{o}=-10, -12V \\ V_{o}=-13.9, -15V \\ Over V_{in} range \\ T_{a}=-20^{\circ}C to +70^{\circ}C \\ Over V_{in} range \\ 0.1 \le I_{o} \le I_{max} \\ V_{in}=10V, I_{o}=I_{max} \\ 50\% load change \\ V_{o} over/undershoot \\ V_{in}=10V, I_{o}=0.5 \times I_{max}, V_{o} = -5V \\ Over V_{in} and I_{o} ranges \\ Free Air Convection, (40-60LFM) \\ Over V_{in} Range \\ Free Air Convection, (40-60LFM) \\ \\ Per Mil-STD-883D, Method 2002.3 \\ \\ \end{array}$	7 7 7 	$\begin{array}{c} \\ \pm 1.0 \\ \pm 0.5 \\ \pm 0.5 \\ \pm 2 \\ 100 \\ 5.0 \\ 75 \\ 650 \\ \\ 45 \\ \\ 500 \end{array}$	$\begin{array}{c} 21 \\ 18 \\ 15 \\ \pm 3.0 \\ \pm 1.0 \\ \pm 1.0 \\ \\ 250 \\ \\ 700 \\ \pm 85 \\ ^{(3)} \\ \end{array}$	V V V %Vo %Vo %Vo %Vo % kHz °C
Output Voltage Tolerance Line Regulation Load Regulation V _o Ripple/Noise Transient Response (with 100µF output cap) Efficiency Switching Frequency Absolute Maximum Operating Temperaturte Range Thermal Resistance Storage Temperature	$\begin{array}{c} \Delta V_{o} \\ \hline Reg_{line} \\ Reg_{load} \\ V_{n} \\ t_{tr} \\ \hline \eta \\ f_{o} \\ T_{a} \\ \hline \theta_{ja} \\ T_{s} \end{array}$	$V_{o}=-6, -7, -8, -9V \\ V_{o}=-10, -12V \\ V_{o}=-10, -12V \\ V_{o}=-13.9, -15V \\ 0 \\ V_{o}=-13.9, -15V \\ 0 \\ V_{in} range \\ 0.1 \le I_{o} \le I_{max} \\ 0.1 \le I_{o} \le I_{max} \\ V_{in}=10V, I_{o}=I_{max} \\ 0.1 \le I_{o} \le I_{max} \\ V_{in}=10V, I_{o}=I_{max} \\ 0.1 \\ V_{in}=10V, I_{o}=0.5 \\ V_{o} \\ 0 \\ V_{in}=10V, I_{o}=0.5 \\ V_{in}=0.5 \\ V_{o} \\ 0 \\ V_{in}=10V, I_{o}=0.5 \\ V_{in}=0.5 \\ V_{o} \\ 0 \\ V_{in}=10V, I_{o}=0.5 \\ V_{in}=0.5 \\ V_{i$	7 7 7 — — — — — 600 -40 — -40		$\begin{array}{c} 21 \\ 18 \\ 15 \\ \pm 3.0 \\ \pm 1.0 \\ \pm 1.0 \\ - \\ 250 \\ - \\ 700 \\ + 85 \\ (3) \\ - \\ \pm 125 \end{array}$	V V V %V₀ %V₀ %V₀ %V₀ % % kHz °C °C/W °C

Notes: (1) The PT78NR100 Series requires a 100µF electrolytic or tantalum capacitor at both the input and output for proper operation in all applications. The input capacitor, C_1 must have a ripple current rating ≥ 600 mArms, and an ESR $\leq 0.2\Omega$. (2) The ISR will operate down to no load with reduced specifications.
 (3) See Thermal Derating chart.



Typical Characteristics

1 Amp Plus to Minus Voltage Integrated Switching Regulator



Note A: All data listed in the above graphs, except for derating data, has been developed from actual products tested at 25°C. This data is considered typical data for the ISR. Note B: Thermal derating graphs are developed in free air convection cooling of 40-60 LFM. (See Thermal Application Notes.)

13-May-2005



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O	rderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
	PT78NR103H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR103S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR103ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR103V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR105H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR105S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR105ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR105V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR106H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR106S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR106ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR106V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR107H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR107S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR107ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR107V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR108H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR108S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR108ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR108V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR109H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR109S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
I	PT78NR109ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR109V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
	PT78NR110H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM

PACKAGE OPTION ADDENDUM

13-May-2005

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
PT78NR110S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR110ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT78NR110V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR112H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR112S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR112ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT78NR112V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR114H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR114S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR114ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT78NR114V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR115H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR115S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR115ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT78NR115V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR152H	ACTIVE	SIP MOD ULE	EFA	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR152S	ACTIVE	SIP MOD ULE	EFC	3	25	TBD	Call TI	Level-1-215C-UNLIM
PT78NR152ST	ACTIVE	SIP MOD ULE	EFC	3	200	TBD	Call TI	Level-1-215C-UNLIM
PT78NR152V	ACTIVE	SIP MOD ULE	EFD	3	25	TBD	Call TI	Level-1-215C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame



retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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