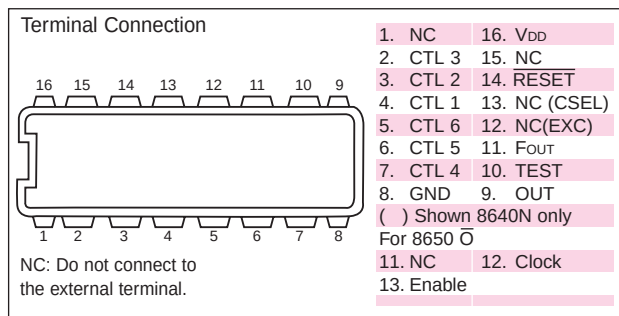


SPG Series



Terminal Name	Function
CTL1 to 6 OUT	Programs dividing ratio. (pull-down resistor incorporated) Output frequency preset by CTL1-6. (Refer to the setting procedure of output frequency)
FOUT	Constantly outputs the oscillation source frequency of built-in quartz crystal.
RESET	Stops output at RESET="L". (Pull up resistor incorporated)
TEST	Used for the input terminal for testing. When CTL4 is H, output will be 1000 times larger than the preset value at TEST="H". (Pull-down resistor incorporated)
EXC (8640N only)	Serves as input terminal when using an external clock by changing to the built-in oscillator. Effective only when CSEL is H
CSEL (8640N only)	When this terminal is made H, the external clock is selected (Pull down resistor incorporated).

Note: treatment of empty terminals. When RESET terminal is not used, this should be connected to VDD, when TEST terminal, CSEL terminal, and CTL1 to 6 terminals are not used, to GND.

Clock	Clock input (max. 1MHz)
ENABLE	Be sure to connect to V _{DD}

CTL1	CTL2	CTL3	Dividing ratio	CTL4	CTL5	CTL6	Dividing ratio
0	0	0	1/1	0	0	0	1/1 ($1/1^0$)
0	0	1	1/10	0	0	1	1/10 ($1/1^1$)
0	1	0	1/2	0	1	0	1/10 ² ($1/2^{2^0}$)
0	1	1	1/3	0	1	1	1/10 ³ ($1/2^{2^1}$)
1	0	0	1/4	1	0	0	1/10 ⁴ ($1/2^{2^2}$)
1	0	1	1/5	1	0	1	1/10 ⁵ ($1/2^{2^3}$)
1	1	0	1/6	1	1	0	1/10 ⁶ ($1/2^{2^4}$)
1	1	1	1/12	1	1	1	1/10 ⁷ ($1/2^{2^5}$)

*0 = L 1 = H

()8650D

8640AN											
Set terminal		CTL4	0	0	0	0	1	1	1	1	1
		CTL5	0	0	1	1	0	0	1	1	1
CTL1	CTL2	CTL3	0	1	0	1	0	1	0	1	1
0	0	0	600K	60K	6K	600	60	6	0.6	0.6	0.06
0	0	1	60K	6K	600	60	6	0.6	0.06	0.006	
0	1	0	300K	30K	3K	300	30	3	0.3	0.03	
0	1	1	200K	20K	2K	200	20	2	0.2	0.02	
1	0	0	150K	15K	1.5K	150	15	1.5	0.15	0.015	
1	0	1	120K	12K	1.2K	120	12	1.2	0.12	0.012	
1	1	0	100K	10K	1K	100	10	1	0.1	0.01	
1	1	1	50K	5K	500	50	5	0.5	0.05	0.005	

Set terminal		CTL4	0	0	0	0	1	1	1	1
		CTL5	0	0	1	1	0	0	1	1
CTL1	CTL2		0	1	0	1	0	1	0	1
0	0	0	1M	100K	10K	1K	100	10	1	1/10
0	0	1	100K	10K	1K	100	10	1	1/10	1/100
0	1	0	500K	50K	5K	500	50	5	1/2	1/20
0	1	1	333.3K	33.3K	3.3K	333.3	33.3	3.33	1/3	1/30
1	0	0	250K	25K	2.5K	250	25	2.5	1/4	1/40
1	0	1	200K	20K	2K	200	20	2	1/5	150
1	1	0	166.6K	16.6K	1.6K	166.6	16.6	1.6	1/6	1/60
1	1	1	83.3K	8.3K	833.3	83.3	8.3	0.83	1/12	1/120

Set terminal			CTL4	0	0	0	0	1	1	1	1
			CTL5	0	0	1	1	0	0	1	1
CTL1	CTL2			0	1	0	1	0	1	0	1
0	0	0		60K	6K	600	60	6	0.6	0.06	0.006
0	0	1		6K	600	600	60	0.6	0.06	0.006	0.0006
0	1	0		30K	3K	300	30	3	0.3	0.03	0.003
0	1	1		20K	2K	200	20	2	0.2	0.02	0.002
1	0	0		15K	1.5K	150	15	1.5	0.15	0.015	0.0015
1	0	1		12K	1.2K	120	12	1.2	0.12	0.012	0.0012
1	1	0		10K	1K	100	10	1	0.1	0.01	0.001
1	1	1		5K	500	50	5	0.5	0.05	0.005	0.0005

Set terminal		CTL4	0	0	0	0	1	1	1	1
		CTL5	0	0	1	1	0	0	1	1
CTL1	CTL2		0	1	0	1	0	1	0	1
0	0	0	100K	10K	1K	100	10	1	1/10	1/100
0	0	1	10K	1K	100	10	1	1/10	1/100	1/1000
0	1	0	50K	5K	500	50	5	1/20	1/10	1/200
0	1	1	33.3K	3.3K	333.3K	33.3	3.33	1/3	1/30	1/300
1	0	0	25K	2.5K	250	25	2.5	1/4	1/40	1/400
1	0	1	20K	2K	200	20	2	1/5	1/50	1/500
1	1	0	16.6K	1.6K	166.6	16.6	1.6	1/6	1/60	1/600
1	1	1	8.3K	833.3	83.3	8.3	0.83	1/12	1/120	1/200

Set terminal		CTL4	0	0	0	0	1	1	1	1
		CTL5	0	0	1	1	0	0	1	1
CTL1	CTL2		0	1	0	1	0	1	0	1
0	0	0	32768	3276.8	327.68	32.768	3.2768	0.32768	0.03276	0.00327
0	0	1	3276.8	327.68	32.768	3.276	0.327	0.0327	0.00327	0.00032
0	1	0	16384	1638.4	163.84	16.384	1.638	0.1638	0.01638	0.00163
0	1	1	10922.6	1092.26	109.226	10.922	1.092	0.1092	0.01092	0.00109
1	0	0	8192	819.2	81.92	8.192	0.819	0.0819	0.00819	0.00081
1	0	1	6553.6	655.36	65.536	6.553	0.655	0.0655	0.00655	0.00065
1	1	0	5461.3	546.13	54.613	5.461	0.546	0.0546	0.00546	0.00054
1	1	1	2730.6	273.06	27.306	2.730	0.273	0.0273	0.00273	0.00027

CTL1	CTL2	CTL3	CTL4	CTL5	CTL6	Output frequency (kHz)	Baud rate output example (fo/16)/(bits/sec)
0	0	0	0	0	0	768k	48000
1	0	1	0	0	0	153.6	9600
0	0	1	0	0	0	76.8	4800
0	1	0	0	0	1	38.4	2400
1	0	0	0	0	1	19.2	1200

CTL1	CTL2	CTL3	CTL4	CTL5	CTL6	Output frequency (kHz)	Baud rate output example (fo/16)/(bits/sec)
0	0	0	0	0	0	96.0	6000
1	0	1	0	0	0	19.2	1200
0	0	1	0	0	0	9.6	600
0	1	0	0	0	1	4.8	300
0	1	1	0	0	1	3.2	200
1	0	0	0	0	1	2.4	150
1	1	0	0	0	1	1.6	100
1	1	1	0	0	1	0.8	50

CTL1	CTL2	CTL3	CTL4	CTL5	CTL6	Output frequency (kHz)	Baud rate output example (f0/16)(bits/sec)
0	0	0	0	0	0	153.6	9600
0	0	0	0	0	1	76.8	4800
0	0	0	0	1	0	38.4	2400
0	0	0	0	1	1	19.2	1200
0	0	0	1	0	1	4.8	300
0	1	1	1	0	0	3.2	200
0	0	0	1	1	0	2.4	150
1	1	0	1	0	0	1.6	100
0	0	0	1	1	1	1.2	75
1	1	1	1	0	0	0.8	50

Note: Lower digits are omitted