

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC74HC590AP, TC74HC590AF

8 - BIT BINARY COUNTER / REGISTER WITH 3 - STATE OUTPUTS

The TC74HC590A is a high speed CMOS 8-BIT COUNTER/REGISTER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

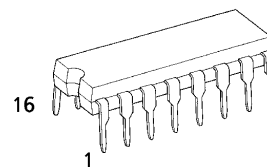
The internal counter counts on the positive going edge of Counter Clock (CCK) when Counter Clock Enable ($\overline{\text{CCKEN}}$) is low. When Counter Clear ($\overline{\text{CCLR}}$) is low, the internal counter is cleared asynchronously to the clock.

Data in the internal counter are loaded into the register at positive going edge of Register Clock (RCK), and the register outputs are controlled by enable input ($\overline{\text{G}}$).

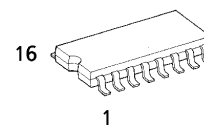
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

FEATURES:

- High Speed..... $f_{\text{MAX}} = 62\text{MHz}(\text{typ.})$ at $V_{\text{CC}} = 5\text{V}$
- Low Power Dissipation $I_{\text{CC}} = 4\mu\text{A}(\text{Max.})$ at $T_a = 25^\circ\text{C}$
- High Noise Immunity $V_{\text{NIH}} = V_{\text{NIL}} = 28\% V_{\text{CC}} (\text{Min.})$
- Output Drive Capability..... 15 LSTTL Loads For QA~QH
10 LSTTL Loads For $\overline{\text{RCO}}$
- Symmetrical Output Impedance...
 $|I_{\text{OH}}| = I_{\text{OL}} = 6\text{mA}(\text{Min.})$ For QA~QH
 $|I_{\text{OH}}| = I_{\text{OL}} = 4\text{mA}(\text{Min.})$ For $\overline{\text{RCO}}$
- Balanced Propagation Delays..... $t_{\text{PLH}} \approx t_{\text{PHL}}$
- Wide Operating Voltage Range.... $V_{\text{CC}} (\text{opr.}) = 2\text{V} \sim 6\text{V}$
- Pin and Function Compatible with 74LS590

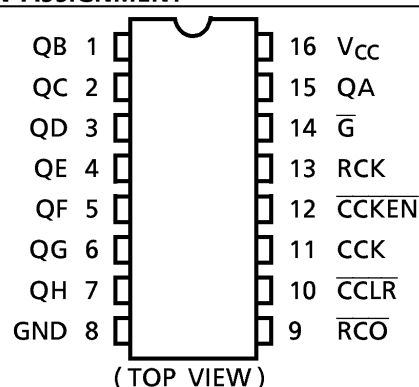


P (DIP16-P-300-2.54A)
Weight : 1.00g (Typ.)



F (SOP16-P-300-1.27)
Weight : 0.18g (Typ.)

PIN ASSIGNMENT



TRUTH TABLE

INPUT					FUNCTION
G	RCK	$\overline{\text{CCLR}}$	$\overline{\text{CCKEN}}$	CCK	
H	X	X	X	X	Q OUTPUTS DISABLE
L	X	X	X	X	Q OUTPUTS ENABLE
X		X	X	X	COUNTER DATA IS STORED INTO REGISTER
X		X	X	X	REGISTER STATE IS NOT CHANGED
X	X	L	X	X	COUNTER CLEAR
X	X	H	L		ADVANCE ONE COUNT
X	X	H	L		NO COUNT
X	X	H	H	X	NO COUNT

X : Don't care

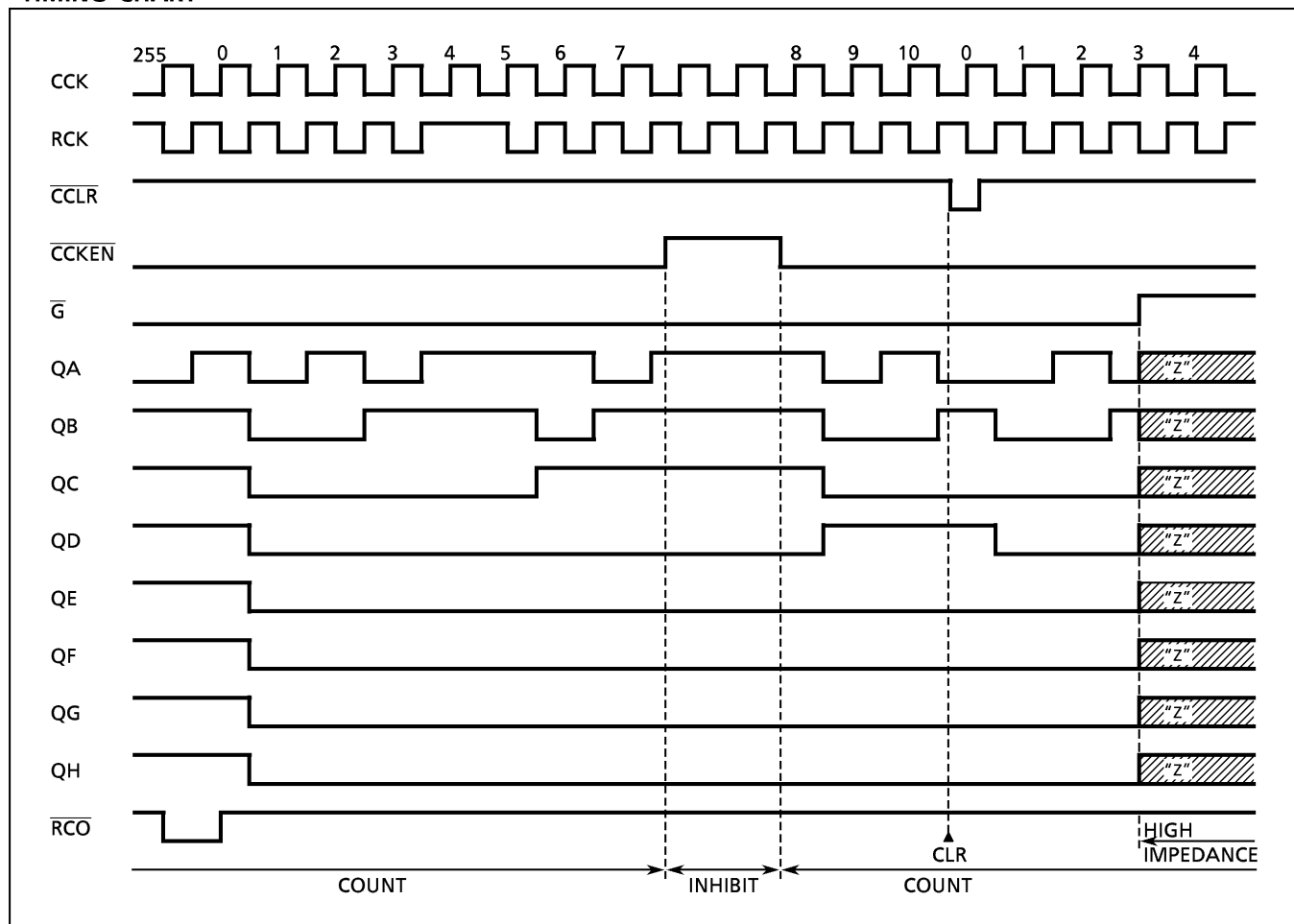
$$\overline{\text{RCO}} = \text{QA}' \cdot \text{QB}' \cdot \text{QC}' \cdot \text{QD}' \cdot \text{QE}' \cdot \text{QF}' \cdot \text{QG}' \cdot \text{QH}'$$

(QA'~QH' : Internal outputs of the counter)

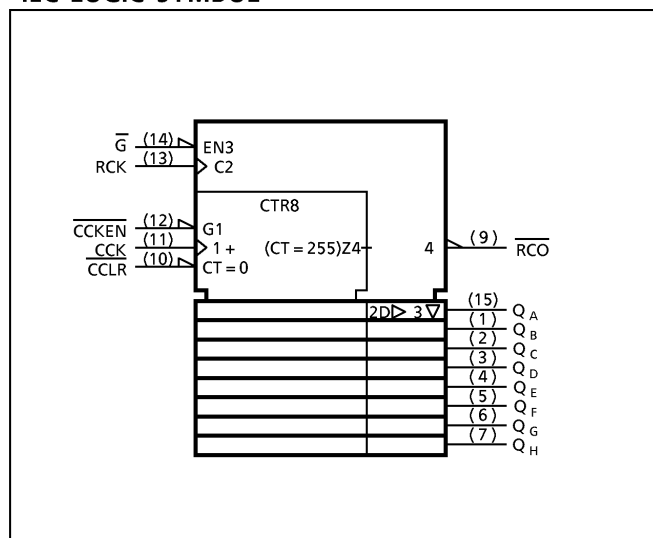
980508EBA2

● TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

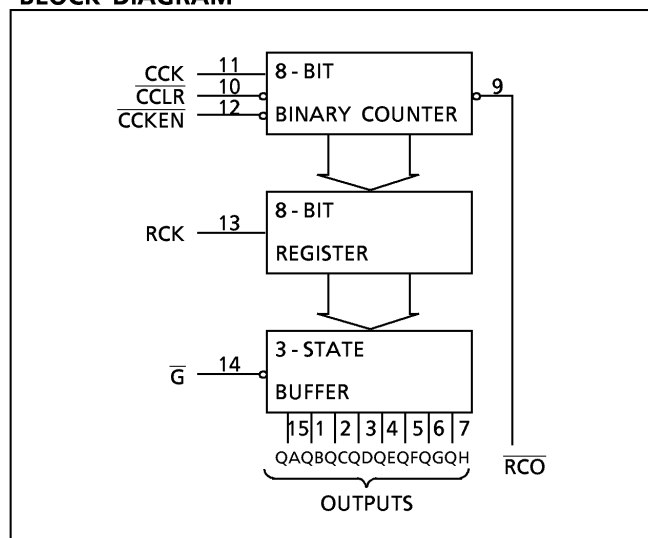
TIMING CHART



IEC LOGIC SYMBOL



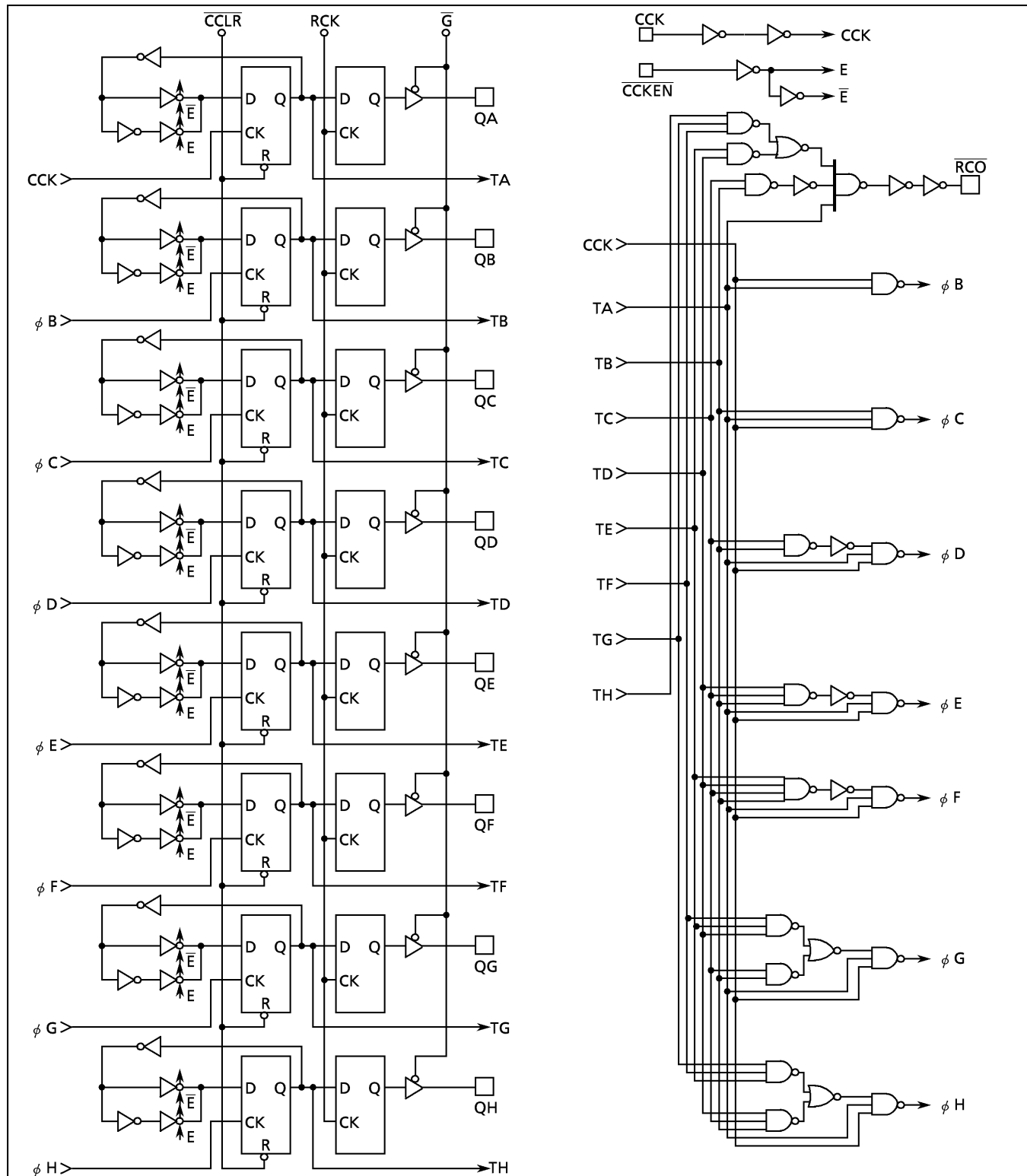
BLOCK DIAGRAM



980508EBA2'

- The products described in this document are subject to foreign exchange and foreign trade laws.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.

LOGIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage Range	V_{CC}	$-0.5 \sim 7$	V
DC Input Voltage	V_{IN}	$-0.5 \sim V_{CC} + 0.5$	V
DC Output Voltage	V_{OUT}	$-0.5 \sim V_{CC} + 0.5$	V
Input Diode Current	I_{IK}	± 20	mA
Output Diode Current	I_{OK}	± 20	mA
DC Output Current (RCO) ($Q_A \sim Q_H$)	I_{OUT}	± 25 ± 35	mA
DC V_{CC} /Ground Current	I_{CC}	± 75	mA
Power Dissipation	P_D	500 (DIP)* / 180 (SOP)	mW
Storage Temperature	T_{stg}	$-65 \sim 150$	°C

*500mW in the range of $T_a = -40^\circ\text{C} \sim 65^\circ\text{C}$. From $T_a = 65^\circ\text{C}$ to 85°C a derating factor of $-10\text{mW}/^\circ\text{C}$ shall be applied until 300mW.

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V_{CC}	$2 \sim 6$	V
Input Voltage	V_{IN}	$0 \sim V_{CC}$	V
Output Voltage	V_{OUT}	$0 \sim V_{CC}$	V
Operating Temperature	T_{opr}	$-40 \sim 85$	°C
Input Rise and Fall Time	t_r, t_f	0~1000 ($V_{CC} = 2.0\text{V}$) 0~500 ($V_{CC} = 4.5\text{V}$) 0~400 ($V_{CC} = 6.0\text{V}$)	ns

DC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITION		V _{CC} (V)	Ta = 25°C			Ta = -40~85°C		UNIT
					MIN.	TYP.	MAX.	MIN.	MAX.	
High - Level Input Voltage	V _{IH}			2.0 4.5 6.0	1.50 3.15 4.20	— — —	— — —	1.50 3.15 4.20	— — —	V
Low - Level Input Voltage	V _{IL}			2.0 4.5 6.0	— — —	— — —	0.50 1.35 1.80	— — —	0.50 1.35 1.80	V
High - Level Output Voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -20μA	2.0 4.5 6.0	1.9 4.4 5.9	2.0 4.5 6.0	— — —	1.9 4.4 5.9	— — —	V
		$\overline{\text{RCO}}$	I _{OH} = -4 mA I _{OH} = -5.2 mA	4.5 6.0	4.18 5.68	4.31 5.80	— —	4.13 5.63	— —	
		QA~QH	I _{OH} = -6 mA I _{OH} = -7.8 mA	4.5 6.0	4.18 5.68	4.31 5.80	— —	4.13 5.63	— —	
Low - Level Output Voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 20μA	2.0 4.5 6.0	— — —	0.0 0.0 0.0	0.1 0.1 0.1	— — —	0.1 0.1 0.1	V
		$\overline{\text{RCO}}$	I _{OL} = 4 mA I _{OL} = 5.2 mA	4.5 6.0	— —	0.17 0.18	0.26 0.26	— —	0.33 0.33	
		QA~QH	I _{OL} = 6 mA I _{OL} = 7.8 mA	4.5 6.0	— —	0.17 0.18	0.26 0.26	— —	0.33 0.33	
3 - State Output Off - State Current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND		6.0	—	—	±0.5	—	±5.0	μA
Input Leakage Current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	—	—	±0.1	—	±1.0	
Quiescent Supply Current	I _{CC}	V _{IN} = V _{CC} or GND		6.0	—	—	4.0	—	40.0	

TIMING REQUIREMENTS (Input t_r = t_f = 6ns)

PARAMETER	SYMBOL	TEST CONDITION		V _{CC} (V)	Ta = 25°C		Ta = -40~85°C	UNIT
					TYP.	LIMIT	LIMIT	
Minimum Pulse Width (CCK, RCK)	t _{W(H)} t _{W(L)}			2.0 4.5 6.0	— — —	75 15 13	95 19 16	ns
				2.0 4.5 6.0	— — —	75 15 13	95 19 16	
				2.0 4.5 6.0	— — —	75 15 13	95 19 16	
Minimum Set-up Time (CCKEN—CCK)	t _s			2.0 4.5 6.0	— — —	100 20 17	125 25 21	
				2.0 4.5 6.0	— — —	200 40 34	250 50 43	
				2.0 4.5 6.0	— — —	0 0 0	0 0 0	
Minimum Hold Time	t _h			2.0 4.5 6.0	— — —	0 0 0	0 0 0	
				2.0 4.5 6.0	— — —	75 15 13	95 19 16	
				2.0 4.5 6.0	— — —	6 33 39	5 26 31	
Minimum Removal Time (CCLR)	t _{rem}			2.0 4.5 6.0	— — —	75 15 13	95 19 16	MHz
				2.0 4.5 6.0	— — —	6 33 39	5 26 31	
				2.0 4.5 6.0	— — —	6 33 39	5 26 31	
Clock Frequency	f			2.0 4.5 6.0	— — —	6 33 39	5 26 31	MHz
				2.0 4.5 6.0	— — —	6 33 39	5 26 31	
				2.0 4.5 6.0	— — —	6 33 39	5 26 31	

AC ELECTRICAL CHARACTERISTICS ($C_L = 15\text{pF}$, $V_{CC} = 5\text{V}$, $T_a = 25^\circ\text{C}$, Input $t_r = t_f = 6\text{ns}$)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Transition Time (RCO)	t_{TLH} t_{THL}		—	4	8	ns
Propagation Delay Time (CCK—RCO)	t_{pLH} t_{pHL}		—	18	28	
Propagation Delay Time (CCLR—RCO)	t_{pLH}		—	20	30	
Maximum Clock Frequency	f_{MAX}		32	62	—	MHz

AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 6\text{ns}$)

PARAMETER	SYMBOL	TEST CONDITION	CL (pF)	V _{CC} (V)	Ta = 25°C			Ta = -40~85°C		UNIT
					MIN.	TYP.	MAX.	MIN.	MAX.	
Output Transition Time (Qn)	t_{TLH} t_{THL}		50	2.0 4.5 6.0	— — —	25 7 6	60 12 10	— — —	75 15 13	ns
Output Transition Time (RCO)	t_{TLH} t_{THL}		50	2.0 4.5 6.0	— — —	30 8 7	75 15 13	— — —	95 19 16	
Propagation Delay Time (CCK—RCO)	t_{pLH} t_{pHL}		50	2.0 4.5 6.0	— — —	75 22 17	163 33 28	— — —	205 41 35	
Propagation Delay Time (CCLR—RCO)	t_{pLH}		50	2.0 4.5 6.0	— — —	78 23 18	175 35 30	— — —	220 44 37	
Propagation Delay Time (RCK—Qn)	t_{pLH} t_{pHL}		50	2.0	—	62	145	—	180	
				4.5	—	19	29	—	36	
				6.0	—	15	25	—	31	
Output Enable time	t_{pZL} t_{pZH}	$R_L = 1\text{k}\Omega$	50	2.0	—	43	105	—	130	
				4.5	—	14	21	—	26	
				6.0	—	12	18	—	22	
Output Disable time	t_{pLZ} t_{pHZ}	$R_L = 1\text{k}\Omega$	50	2.0	—	33	105	—	130	
				4.5	—	16	21	—	26	
				6.0	—	12	18	—	22	
Maximum Clock Frequency	f_{MAX}		50	2.0 4.5 6.0	6 30 35	12 51 80	— — —	5 24 28	— — —	MHz
Input Capacitance	C_{IN}				—	5	10	—	10	pF
Power Dissipation Capacitance	$C_{PD} (1)$				—	34	—	—	—	

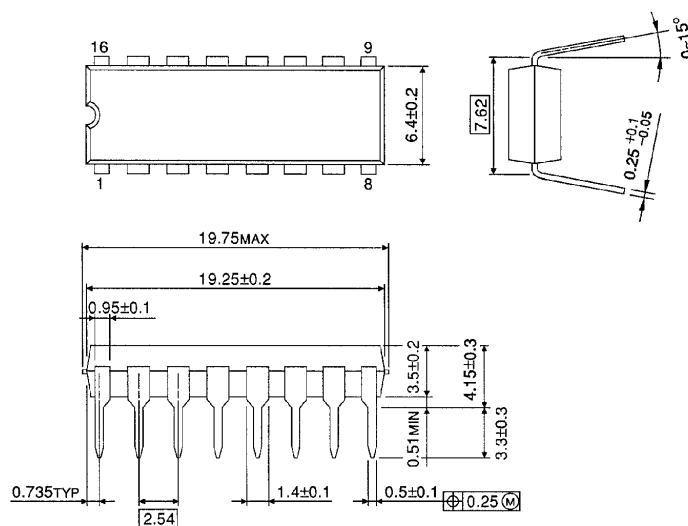
Note (1) C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation :

$$I_{CC}(\text{opr}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

DIP 16PIN OUTLINE DRAWING (DIP16-P-300-2.54A)

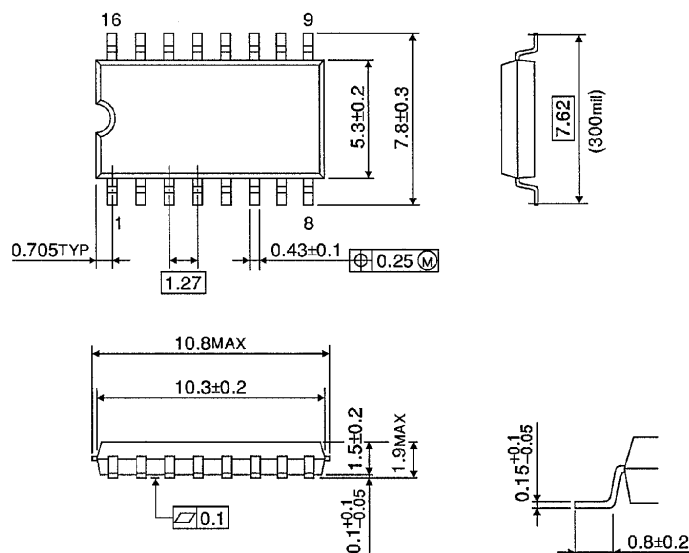
Unit in mm



Weight : 1.00g (Typ.)

SOP 16PIN (200mil BODY) OUTLINE DRAWING (SOP16-P-300-1.27)

Unit in mm



Weight : 0.18g (Typ.)