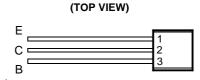
- 20 W Pulsed Power Dissipation
- 100 V Capability
- 2 A Continuous Collector Current
- 4 A Peak Collector Current



LP PACKAGE

MDTRAB

#### absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING			VALUE	UNIT
	TIPP115		-60	
Collector-base voltage (I <sub>E</sub> = 0)	TIPP116	$V_{CBO}$	-80	V
	TIPP117		-100	
	TIPP115		-60	
Collector-emitter voltage (I <sub>B</sub> = 0)	TIPP116	$V_{CEO}$	-80	V
	TIPP117		-100	
Emitter-base voltage			-5	V
Continuous collector current			-2	Α
Peak collector current (see Note 1)			-4	Α
Continuous base current			-50	mA
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			0.8	W
Pulsed power dissipation (see Note 3)			20	W
Operating junction temperature range			-55 to +150	°C
Storage temperature range			-55 to +150	°C
Lead temperature 3.2 mm from case for 10 seconds			260	°C

NOTES: 1. This value applies for  $t_p \leq 0.3$  ms, duty cycle  $\leq 10\%.$ 

- 2. Derate linearly to 150°C case temperature at the rate of 0.32 W/°C.
- 3.  $V_{CE}$  = 20 V,  $I_{C}$  = 1 A,  $P_{W}$  = 10 ms, duty cycle  $\leq$  2%.



## TIPP115, TIPP116, TIPP117 PNP SILICON POWER DARLINGTONS

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## electrical characteristics at 25°C case temperature

	PARAMETER		TEST CONDIT	TIONS	MIN	TYP	MAX	UNIT
V(DD)OFO	Collector-emitter breakdown voltage	I <sub>C</sub> = -10 mA	I <sub>B</sub> = 0	TIPP115 TIPP116	-60 -80			V
		(see Note 4)	1 <sub>B</sub> = 0	TIPP117	-100			V
I <sub>CEO</sub>	Collector-emitter cut-off current	$V_{CE} = -30 \text{ V}$ $V_{CE} = -40 \text{ V}$ $V_{CE} = -50 \text{ V}$	$V_{BE} = 0$ $V_{BE} = 0$ $V_{BE} = 0$	TIPP115 TIPP116 TIPP117			-2 -2 -2	mA
I <sub>CBO</sub>	Collector-base cut-off current	$V_{CE} = -60 \text{ V}$ $V_{CE} = -80 \text{ V}$ $V_{CE} = -100 \text{ V}$	$I_{B} = 0$ $I_{B} = 0$ $I_{B} = 0$	TIPP115 TIPP116 TIPP117			-1 -1 -1	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> = -5 V	I <sub>C</sub> = 0				-2	mA
h <sub>FE</sub>	Forward current transfer ratio	$V_{CE} = -4 V$ $V_{CE} = -4 V$	I <sub>C</sub> = -1 A I <sub>C</sub> = -2 A	(see Notes 4 and 5)	1000 500			
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	I <sub>B</sub> = -8 mA	I <sub>C</sub> = -2 A	(see Notes 4 and 5)			-2.5	٧
V <sub>BE</sub>	Base-emitter voltage	V <sub>CE</sub> = -4 V	I <sub>C</sub> = -2 A	(see Notes 4 and 5)			-2.8	>
V <sub>EC</sub>	Parallel diode forward voltage	I <sub>E</sub> = -4 A	I <sub>B</sub> = 0	(see Notes 4 and 5)			-3.5	٧

NOTES: 4. These parameters must be measured using pulse techniques,  $t_p$  = 300  $\mu$ s, duty cycle  $\leq$  2%.

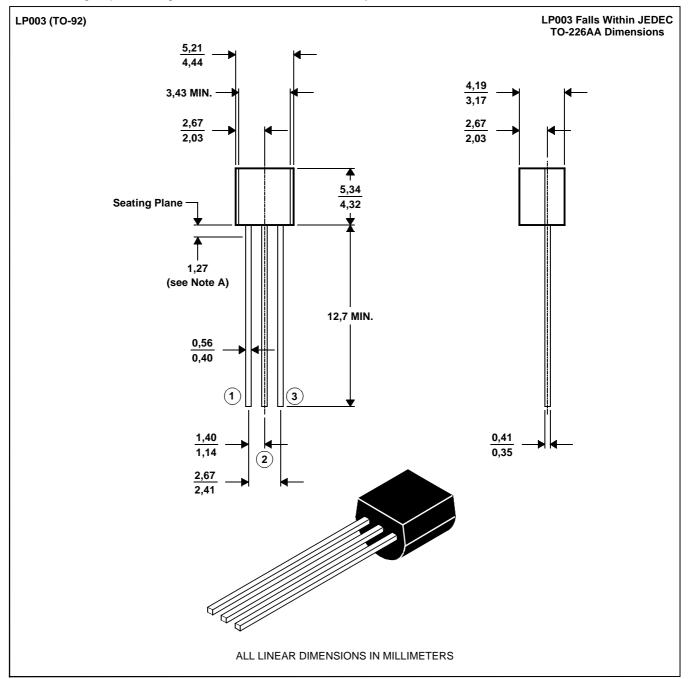
<sup>5.</sup> These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

#### **MECHANICAL DATA**

## LP003 (TO-92)

## 3-pin cylindical plastic package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: Lead dimensions are not controlled in this area.

**MDXXAX** 



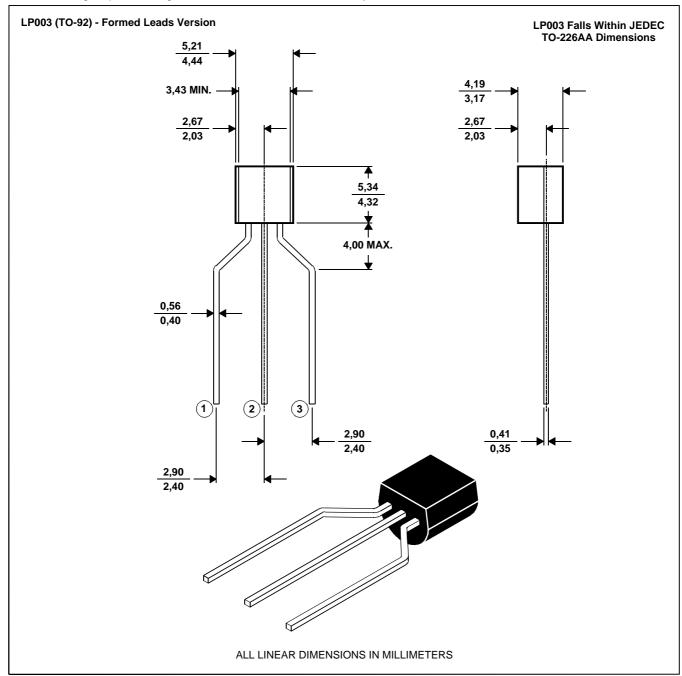
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#### **MECHANICAL DATA**

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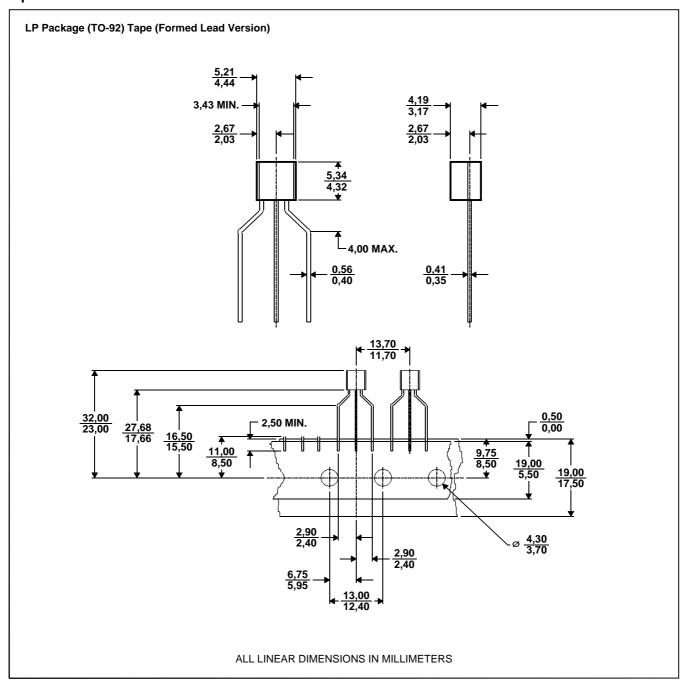


MDXXAR

#### PRODUCT INFORMATION

#### **MECHANICAL DATA**

LPR tape dimensions



**MDXXAS** 



# TIPP115, TIPP116, TIPP117 PNP SILICON POWER DARLINGTONS

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