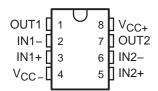
# **DUAL HIGH-OUTPUT-CURRENT OPERATIONAL AMPLIFIER**

SLOS453A - DECEMBER 2004 - REVISED JANUARY 2005

- Single/Dual Power-Supply Operation
- High Output Current . . . 70 mA, V<sub>CC+</sub> = 5 V
- Wide Operating Voltage . . . 3 V to 15 V (Single Supply)
- **Ideal for Headphone Drivers**

### D (SOIC), P (PDIP), OR PW (TSSOP) PACKAGE (TOP VIEW)



### description/ordering information

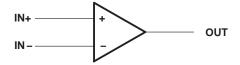
The TL3414A device is a dual operational amplifier that can be operated with single or dual power supplies. In addition to high gain and high output voltage swing, it is capable of driving a 70-mA load, making it ideally suited for simple, low-cost audio-amplifier applications, such as headphone amplifiers in DVD and CDRW applications.

### **ORDERING INFORMATION**

TA	PACKAG	Εţ	ORDERABLE PART NUMBER	TOP-SIDE MARKING
	PDIP (P)	Tube of 50	TL3414AIP	TL3414AIP
	SOIC (D)	Tube of 75	TL3414AID	704444
-40°C to 85°C		Reel of 2500	TL3414AIDR	Z3414A
	TCCOD (DW)	Tube of 150	TL3414AIPW	Z3414A
	TSSOP (PW)	Reel of 2000	TL3414AIPWR	Z3414A

<sup>†</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

### symbol (each amplifier)

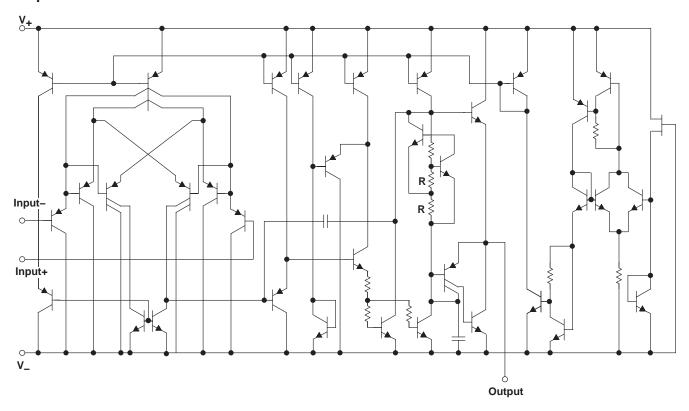




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### simplified schematic



## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V <sub>CC+</sub> (single supply)	
Supply voltage, V <sub>CC</sub> (single supply)	
Supply voltage, V <sub>CC+</sub> (dual supply)	7.5 V
Supply voltage, V <sub>CC</sub> (dual supply)	–7.5 V
Supply voltage, (V <sub>CC</sub> to V <sub>CC+</sub> )	
Input voltage, either input (see Note 1)	$V_{CC-}$ or $V_{CC+}$
Input current (see Note 2)	±10 mA
Duration of output short circuit (see Note 3)	Unlimited
Package thermal impedance, $\theta_{JA}$ (see Notes 4 and 5): D package	97°C/W
P package	85°C/W
PW package	149°C/W
Operating virtual junction temperature, T <sub>J</sub>	150°C
Storage temperature range, T <sub>stq</sub>	–40°C to 125°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. The magnitude of the input voltage must never exceed the magnitude of the supply voltage.
  - 2. Excessive input current will flow if a differential input voltage in excess of approximately 0.6 V is applied between the inputs, unless some limiting resistance is used.
  - 3. The output may be shorted to ground or either power supply. Temperature and/or supply voltages must be limited to ensure the maximum dissipation rating is not exceeded.
  - Maximum power dissipation is a function of T<sub>J</sub>(max), θ<sub>JA</sub>, and T<sub>A</sub>. The maximum allowable power dissipation at any allowable ambient temperature is P<sub>D</sub> = (T<sub>J</sub>(max) – T<sub>A</sub>)/θ<sub>JA</sub>. Operating at the absolute maximum T<sub>J</sub> of 150°C can impact reliability.
  - 5. The package thermal impedance is calculated in accordance with JESD 51-7.



### recommended operating conditions

		MIN	MAX	UNIT
Vcc	Supply voltage (single supply)	3	15	V
V <sub>CC+</sub>	Supply voltage (dual supply)	1.5	7.5	V
VCC-	Supply voltage (dual supply)	-1.5	-7.5	V
VID	Differential input voltage		15	V
VI	Input voltage	-0.3	15	V
TA	Operating free-air temperature range	-40	85	°C

## DC electrical characteristics, $V_{CC+} = 5 \text{ V}$ , $V_{CC-} = 0 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
۷ıO	Input offset voltage	$R_S = 0 \Omega$		2	5	mV	
I <sub>IO</sub>	Input offset current			15	100	nA	
I <sub>IB</sub>	Input bias current			300	600	nA	
AVD	Large-signal differential voltage amplification	$R_L = 2 k\Omega$	77	100		dB	
VICR	Common-mode input voltage range		V <sub>CC+</sub> -2V			V	
V	Output voltage swing	$R_L > 2 k\Omega$ , $V_{CC+} = 5 V$	3.5				
V <sub>OM</sub> O		$I_O = 70 \text{ mA}, V_{CC+} = 5 \text{ V}$	3.2			V	
CMRR	Common-mode rejection ratio		70	79		dB	
ksvR <sup>†</sup>	Supply-voltage rejection ratio		80	90		dB	
ICC	Supply current (all amplifiers)	R <sub>L</sub> = open circuit (full temperature range)	3	4	6	mA	

 $<sup>^{\</sup>dagger}$  Measured with V  $_{CC\pm}$  differentially and simultaneously varied from 5 V to 8.6 V

# AC electrical characteristics, $V_{CC+} = 5 \text{ V}$ , $V_{CC-} = 0 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS	TYP	UNIT
SR	Slew rate at unity gain		0.83	V/μs
GBW	Gain bandwidth product		1.1	MHz
Vn	Equivalent input noise voltage	f = 1 kHz	18	nV/√Hz

# DC electrical characteristics, $V_{CC+}$ = 8.6 V, $V_{CC-}$ = 0 V, $T_A$ = 25°C (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
VIO	Input offset voltage	$R_S = 0 \Omega$		2	5	mV
lιο	Input offset current			15	100	nA
I <sub>IB</sub>	Input bias current			300	600	nA
A <sub>VD</sub>	Large-signal differential voltage amplification	$R_L = 2 k\Omega$	88	105		dB
VICR	Common-mode input voltage range		V <sub>CC+</sub> -2V			V
	0	$R_L > 2 k\Omega, V_{CC+} = 8.6 V$	7			
VOM	Output voltage swing	$I_{O} = 70 \text{ mA}, V_{CC+} = 8.6 \text{ V}$	6.7			٧
CMRR	Common-mode rejection ratio		80	90		dB
k <sub>SVR</sub> †	Supply-voltage rejection ratio		80	90		dB
Icc	Supply current (all amplifiers)	R <sub>L</sub> = open circuit (full temperature range)	3	4	6	mA

 $<sup>^\</sup>dagger$  Measured with  $V_{CC\pm}$  differentially and simultaneously varied from 5 V to 8.6 V



# TL3414A DUAL HIGH-OUTPUT-CURRENT OPERATIONAL AMPLIFIER

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# AC electrical characteristics, $V_{CC+}$ = 8.6 V, $V_{CC-}$ = 0 V, $T_A$ = 25°C (unless otherwise noted)

PARAMETER		TEST CONDITIONS		UNIT
SR	Slew rate at unity gain		1.3	V/μs
GBW	Gain bandwidth product		2	MHz
Vn	Equivalent input noise voltage	f = 1 kHz	18	nV/√ <del>Hz</del>







### **PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
TL3414AID	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL3414AIDE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL3414AIDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL3414AIDRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL3414AIP	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
TL3414AIPE4	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
TL3414AIPW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL3414AIPWE4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL3414AIPWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL3414AIPWRE4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

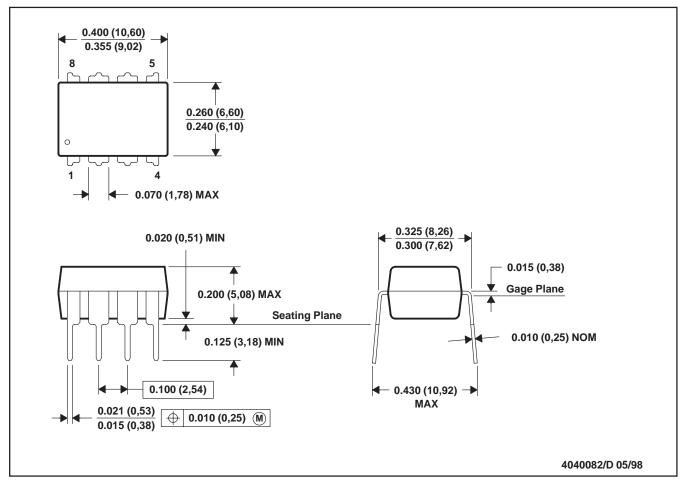
(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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### P (R-PDIP-T8)

### PLASTIC DUAL-IN-LINE



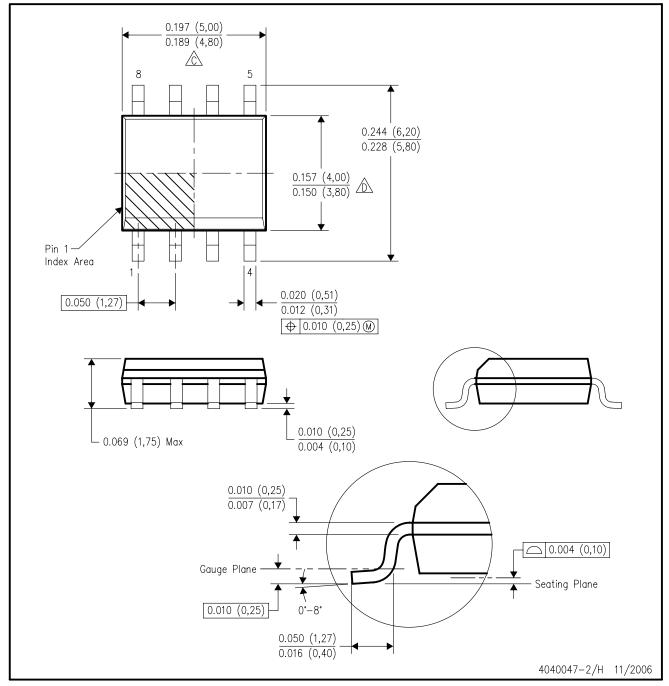
NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001

For the latest package information, go to  $http://www.ti.com/sc/docs/package/pkg\_info.htm$ 

# D (R-PDSO-G8)

### PLASTIC SMALL-OUTLINE PACKAGE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AA.



### PW (R-PDSO-G\*\*)

### 14 PINS SHOWN

### PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-153

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Mailing Address: Texas Instruments

Post Office Box 655303 Dallas, Texas 75265

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