

X200 SERIES (CMOS) STANDARD SPECIFICATIONS

(Similar to M55310/18)

FREQUENCY RANGE FREQUENCY ACCURACY @ + 25 °C FREQUECY STABILITY Vs. TEMPERATU OPERATING TEMPERATURE RANGE	1.5 Hz to 12 MHz <u>+</u> 0.0015% (<u>+</u> 15 PPM) RE See Options Below See Options Below		0.200" Max.
INPUT VOLTAGE (See note below)	+ 5 VDC to + 15 VDC <u>+</u> 10%	0.105" 0.095" Pin 1 is identified with a Sq. Corner	0.020" 0.016"
INPUT CURRENT	5 mA Max. @ + 5 VDC 25 mA Max. @ + 15 VDC	0.507" Max.	$ \overbrace{Max.}{0.439"} $
OUTPUT	CMOS	Max.	0.155" 0.295" 0
LOAD	200 K Ω in parallel with 50 pf	0.305	
SYMMETRY	60/40% @ 50% Output Leve	0.295"	05"
RISE & FALL TIMES (10% to 90% Output Level)	150 nS Max. @ + 5 VDC 50 nS Max. @ + 15 VDC	Pin Con	ax.
START-UP TIME		14	B+
< 10 MHz	20 mS Max.	7	GND
≥ 10 MHz	15 mS Max.	8	OUTPUT
		All Others	N/C
FREQUENCY STABILITY Vs. VOLTAGE	<u>+</u> 0.0005%(<u>+</u> 5 PPM) Max. (for 10% change in Voltage)		
AGING @ +25 °C	<u>+</u> 0.0005% (<u>+</u> 5 PPM)/ year Max.		
PACKAGE, SEAL & LEAD FINISH	Conforms with the Requirements of MI	L-PRF-55310	

Note: Input Voltage must be specified for 200 Series CMOS parts, minimum input voltage required depends upon output frequency and operating temperature range. Consult factory for your specific application.

Contact Xsis Engineering for special requirements such as, Output Symmetry, Start-up Time, Frequency Accuracy, Complementary Outputs, Multiple Outputs, etc.

ORDERING INFORMATION (Select from options below):



05/00

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