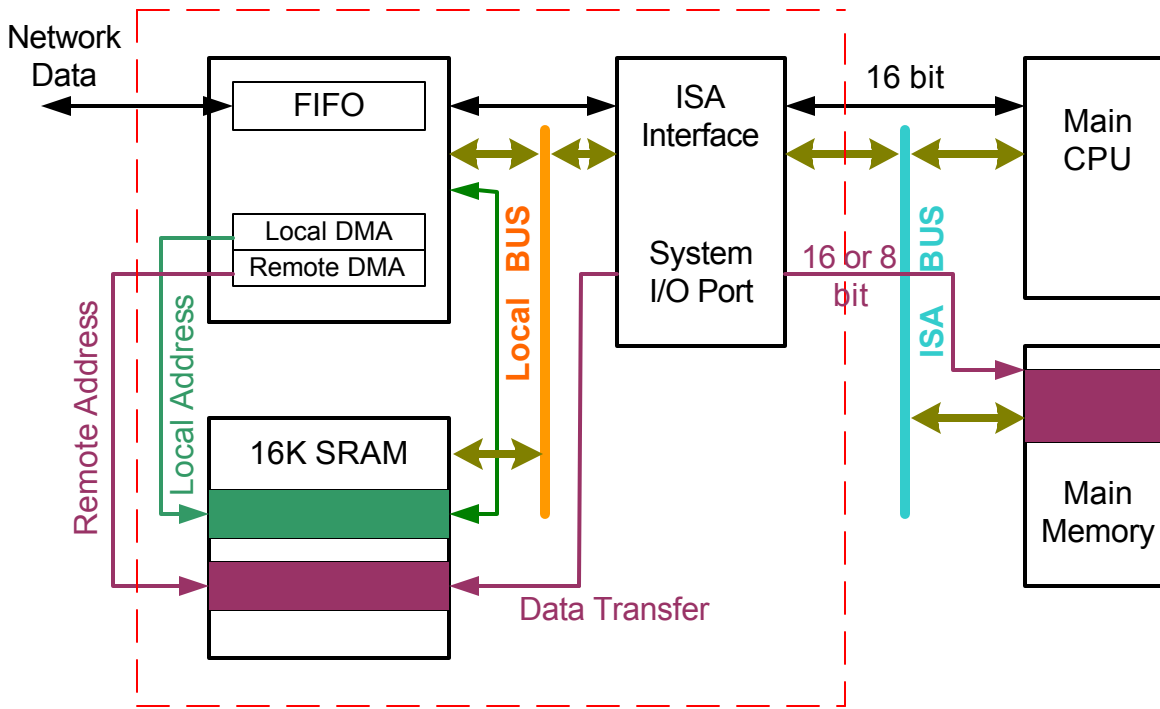


# 8019AS Bus Architecture



## I/O Port Mode Register I/O Map

Base+00H	8019AS Core Registers (8 bit mode)
Base+0FH	
Base+10H	Remote DMA Port
Base+17H	
Base+18H	Reset Port
Base+1FH	

## I/O Port Mode Memory Map 16Bit Mode

0000H	D15	D0
001FH		
4000H	Transmit Buffer	
7FFFH	Receive Buffer	
8000H		
C000H		
FFFFH		

Transmit Buffer = 4000H-4B00H  
Receive Buffer = 4C00H-8000H

## I/O Port Mode Memory Map 8 Bit Mode

0000H	D15	D0
001FH		
4000H	Transmit Buffer	
7FFFH	Receive Buffer	
8000H		
C000H		
FFFFH		

Transmit Buffer=4000H-4500H  
Receive Buffer=4C00H-6000H